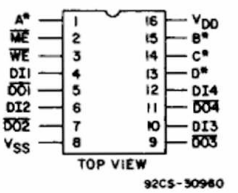
CD40114B **Preliminary Data**

COS/MOS 64-Bit Random Access Memory

High-Voltage Types (20-Volt Rating)

Features:

- Input address latch
- 3-state outputs
- Low-power TTL compatible
- Equivalent to and pin-compatible with National 74C89
- Pin -compatible with 74S189
- Buffered inputs and outputs
- 100% tested for quiescent current at 20 V
- Standardized, symmetrical output characteristics
- 5-V, 10-V, and 15-V parametric ratings
- Meets all requirements of JEDEC Tentative Standard No. 13A, "Standard Specifications for description of "B" Series CMOS Devices"



Terminal Assignment

The RCA-CD40114B is a 16-word x 4-bit random access memory (RAM) with four address inputs, four data inputs, a WRITE ENABLE ($\overline{WE}$) input, a MEMORY ENABLE ($\overline{ME}$) input, and four 3-state data outputs. The four address inputs are decoded internally to select one of the 16 possible word locations. The address information is latched on the negative edge of the $\overline{ME}$ input by an internal address register. The selected output assumes a high-impedance condition when the device is writing or disabled. The $\overline{ME}$ input and the 3-state outputs allow memory expansion.

Applications:

- Main frame memories
- Memory storage
- Scratch-pad memories
- Games

$\overline{ME}$	$\overline{WE}$	OPERATION	CONDITION OF OUTPUTS
L	L	Write	3-STATE
L	H	Read	Complement of Selected Word
H	L	Inhibit, Storage	3-STATE
H	H	Inhibit, Storage	3-STATE

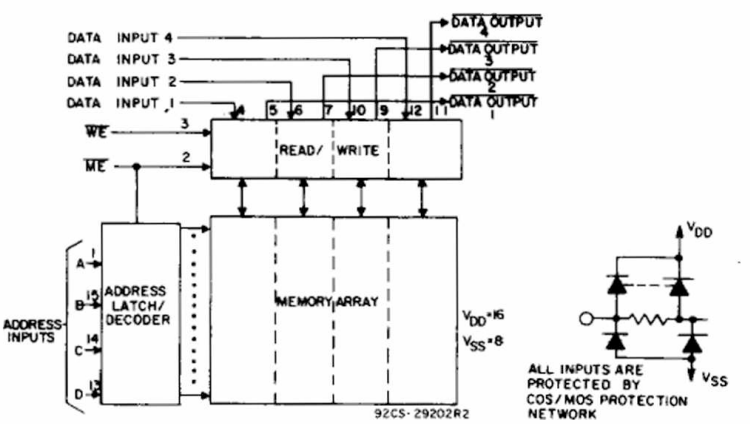


Fig. 1 — Functional Block Diagram