SCAS060A - D2957, JULY 1987 - REVISED APRIL 1993

 Inputs Are TTL-Voltage Compatible Local Bus-Latch Capability 	DW OR NT PACKAGE (TOP VIEW)
 Flow-Through Architecture to Optimize	A1 1 24 GAB
PCB Layout	A2 2 23 B1
 Center-Pin V_{CC} and GND Configurations to	A3 3 22 B2
Minimize High-Speed Switching Noise	A4 4 21 B3
 EPIC[™] (Enhanced-Performance Implanted	GND 5 20 B4
CMOS) 1-µm Process	GND 6 19 V _{CC}
 500-mA Typical Latch-Up Immunity at 125°C 	GND[] 7 18
 Package Options Include Plastic Small-	A5 0 9 16 0 B6
Outline Packages, and Standard Plastic	A6 0 10 15 0 B7
300-mil DIPs	A7 0 11 14 0 B8
000 5 0	A8∏ 12 13∏ <u>G</u> BA

description

These octal bus transceivers are designed for asynchronous two-way communication between data buses. The control function implementation allows for maximum flexibility in timing.

These devices allow data transmission from the A bus to the B bus or from the B bus to the A bus depending upon the logic levels at the enable inputs ($\overline{G}BA$ and GAB).

The enable inputs can be used to disable the device so that the buses are effectively isolated.

The dual-enable configuration gives these devices the capability to store data by simultaneous eanbling of $\overline{G}BA$ and GAB. Each output reinforces its input in this transceiver configuration. Thus, when both control inputs are enabled and all other data sources to the two sets of bus lines are at high impedance, both sets of bus lines (16 in all) will remain at their last states. The 8-bit codes appearing on the two sets of buses will be complementary for the 74ACT11620.

The 74ACT11620 is characterized for operation from -40° C to 85°C.

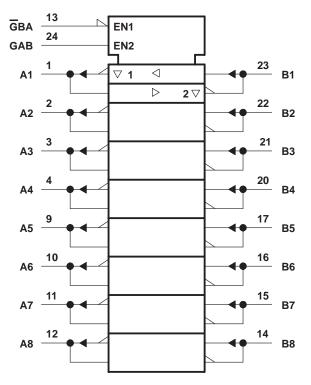
FUNCTION TABLE

ENABLI	E INPUTS	OPERATION			
GBA	GAB	OPERATION			
L	L	B data to A bus			
Н	Н	A data to B bus			
Н	L	Isolation			
	н	B data to A bus,			
-	11	A data to B bus			

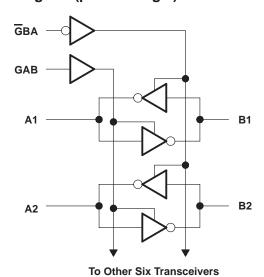
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logic symbol†



logic diagram (positive logic)



absolute maximum ratings over operating free-air temperature range (unless otherwise noted)‡

Supply voltage range, V _{CC}	
Input voltage range, V _I (see Note 1)	\dots -0.5 V to V _{CC} + 0.5 V
Output voltage range, V _O (see Note 1)	$-0.5 \text{ V to V}_{CC} + 0.5 \text{ V}$
Input clamp current, I_{IK} ($V_I < 0$ or $V_I > V_{CC}$)	± 20 mA
Output clamp current, I _{OK} (V _O < 0 or V _O > V _{CC})	$ \pm 50 \text{ mA}$
Continuous output current, $I_O(V_O = 0 \text{ to } V_{CC})$	± 50 mA
Continuous current through V _{CC} or GND	± 200 mA
Storage temperature range	–65°C to 150°C

[‡] Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

NOTE 1: The input and output voltage ratings may be exceeded if the input and output current ratings are observed.



[†] This symbol is in accordance with ANSI/IEEE Std 91-1984 and IEC Publication 617-12.

recommended operating conditions

		MIN	MAX	UNIT
VCC	Supply voltage	4.5	5.5	V
VIH	High-level input voltage	2		V
VIL	Low-level input voltage		0.8	V
٧ _I	Input voltage	0	VCC	V
VO	Output voltage	0	VCC	V
ІОН	High-level output current		-24	mA
lOL	Low-level output current		24	mA
Δt/Δν	Input transition rise or fall rate	0	10	ns/V
TA	Operating free-air temperature	- 40	85	°C

electrical characteristics over recommended operating free-air temperature range

PARAMETER		TEST SOMBITIONS	vcc	T _A = 25°C			MINI MAY		
		TEST CONDITIONS		MIN	TYP	MAX	MIN	MAX	UNIT
		50. 4	4.5 V	4.4			4.4		
		ΙΟΗ = - 50 μΑ	5.5 V	5.4			5.4		
Vон		1 24 4	4.5 V	3.94			3.8		V
		I _{OH} = – 24 mA	5.5 V	4.94			4.8		
		I _{OH} = - 75 mA [†]	5.5 V				3.85		
		50. 4	4.5 V			0.1		0.1	V
		I _{OL} = 50 μA	5.5 V			0.1		0.1	
VOL		1 24 mA	4.5 V			0.36		0.44	
		I _{OL} = 24 mA	5.5 V			0.36		0.44	
		I _{OL} = 75 mA [†]	5.5 V					1.65	
loz	A or B ports‡	$V_O = V_{CC}$ or GND	5.5 V			± 0.5		± 5	μΑ
II	GBA or GAB	$V_I = V_{CC}$ or GND	5.5 V			± 0.1		± 1	μΑ
ICC \		$V_I = V_{CC}$ or GND, $I_O = 0$	5.5 V			8		8	μΑ
ΔI _{CC} §	i	One input at 3.4 V, Other inputs at GND or V _{CC}	5.5 V			0.9		1	mA
Ci	GBA or GAB	$V_I = V_{CC}$ or GND	5 V		4				pF
Со	A or B ports	$V_O = V_{CC}$ or GND	5 V		12				pF

[†] Not more than one output should be tested at a time, and the duration of the test should not exceed 10 ms.



 $[\]mbox{\ensuremath{\mbox{\footnotesize 1}}}$ For I/O ports, the parameter IOZ includes the input leakage.

[§] This is the increase in supply current for each input that is at one of the specified TTL voltage levels rather than 0 V or VCC.

74ACT11620 OCTAL BUS TRANSCEIVER WITH 3-STATE OUTPUTS SCAS060A - D2957, JULY 1987 - REVISED APRIL 1993

switching characteristics over recommended ranges of supply voltage and operating free-air temperature (unless otherwise noted) (see Figure 1)

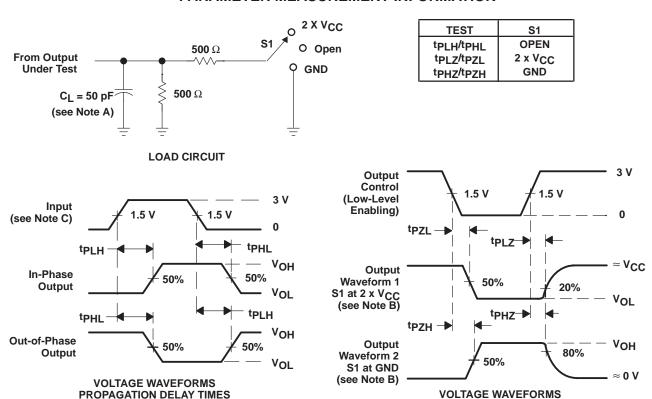
242445752	FROM	ТО		T _A = 25°C				UNIT
PARAMETER	(INPUT)	(OUTPUT)	MIN	TYP	MAX	MIN MAX		UNIT
t _{PLH}	A or B	D or A	1.5	5.7	8.5	1.5	9.4	
^t PHL	AOIB	B or A	1.5	5.9	7.7	1.5	8.6	ns
^t PZH	G BA	٨	1.5	7.2	9.1	1.5	10.3	
t _{PZL}	GBA	A	1.5	7.1	9.2	1.5	10.1	ns
^t PHZ	- GBA	٨	1.5	7.9	9.6	1.5	10.4	
t _{PLZ}	GBA	А	1.5	8.3	10	1.5	10.9	ns
^t PZH	0.15	В	1.5	7.5	10.2	1.5	11.3	
t _{PZL}	GAB	В	1.5	7.7	9.8	1.5	11	ns
t _{PHZ}	GAB	В	1.5	7.2	8.9	1.5	9.4	
tPLZ	GAB		1.5	7.2	8.9	1.5	9.6	ns

operating characteristics, V_{CC} = 5 V, T_A = 25 $^{\circ}C$

PARAMETER			TEST CON	TYP	UNIT	
	Decree districts the second state of the secon	Outputs enabled	0 50 - 5	6 4 MILE	54	
Cpd	Power dissipation capacitance per transceiver	Outputs disabled	$C_L = 50 \text{ pF},$	f = 1 MHz	11	pF

ENABLE AND DISABLE TIMES

PARAMETER MEASUREMENT INFORMATION



NOTES: A. C_I includes probe and jig capacitance.

- B. Waveform 1 is for an output with internal conditions such that the output is low except when disabled by the output control. Waveform 2 is for an output with internal conditions such that the output is high except when disabled by the output control.
- C. All input pulses are supplied by generators having the following characteristics: PRR \leq 10 MHz, $Z_Q = 50~\Omega$, $t_f \leq 2.5~\text{ns}$.
- D. The outputs are measured one at a time with one input transition per measurement.

Figure 1. Load Circuit and Voltage Waveforms





ti.com 24-Jun-2005

PACKAGING INFORMATION

Orderable Device	Status ⁽¹⁾	Package Type	Package Drawing	Pins Package Qty	Eco Plan ⁽²⁾	Lead/Ball Finish	MSL Peak Temp ⁽³⁾
74ACT11620NT	OBSOLETE	PDIP	NT	24	TBD	Call TI	Call TI

⁽¹⁾ The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

OBSOLETE: TI has discontinued the production of the device.

(2) Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS) or Green (RoHS & no Sb/Br) - please check http://www.ti.com/productcontent for the latest availability information and additional product content details.

TBD: The Pb-Free/Green conversion plan has not been defined.

Pb-Free (RoHS): TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.

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(3) MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

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