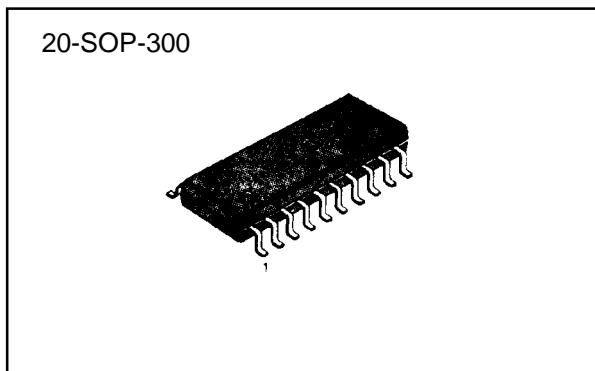


STEPPING MOTOR DRIVER

The KA2821D is a monolithic integrated circuit, and suitable for the two-phase stepping motor driver of 3.5" FDD system.



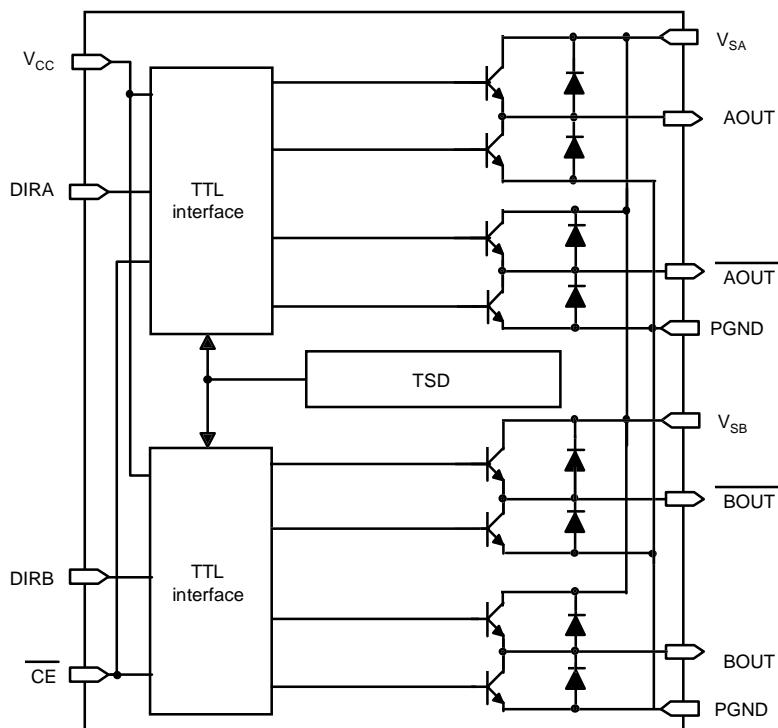
FEATURES

- Built-in chip enable function(active low)
- Low saturation voltage
- Low power dissipation
- Input level : TTL, LSTTL, 5V CMOS compatible
- Standard MPU direct interface
- Built-in TSD
- 2-CH H-bridge driver

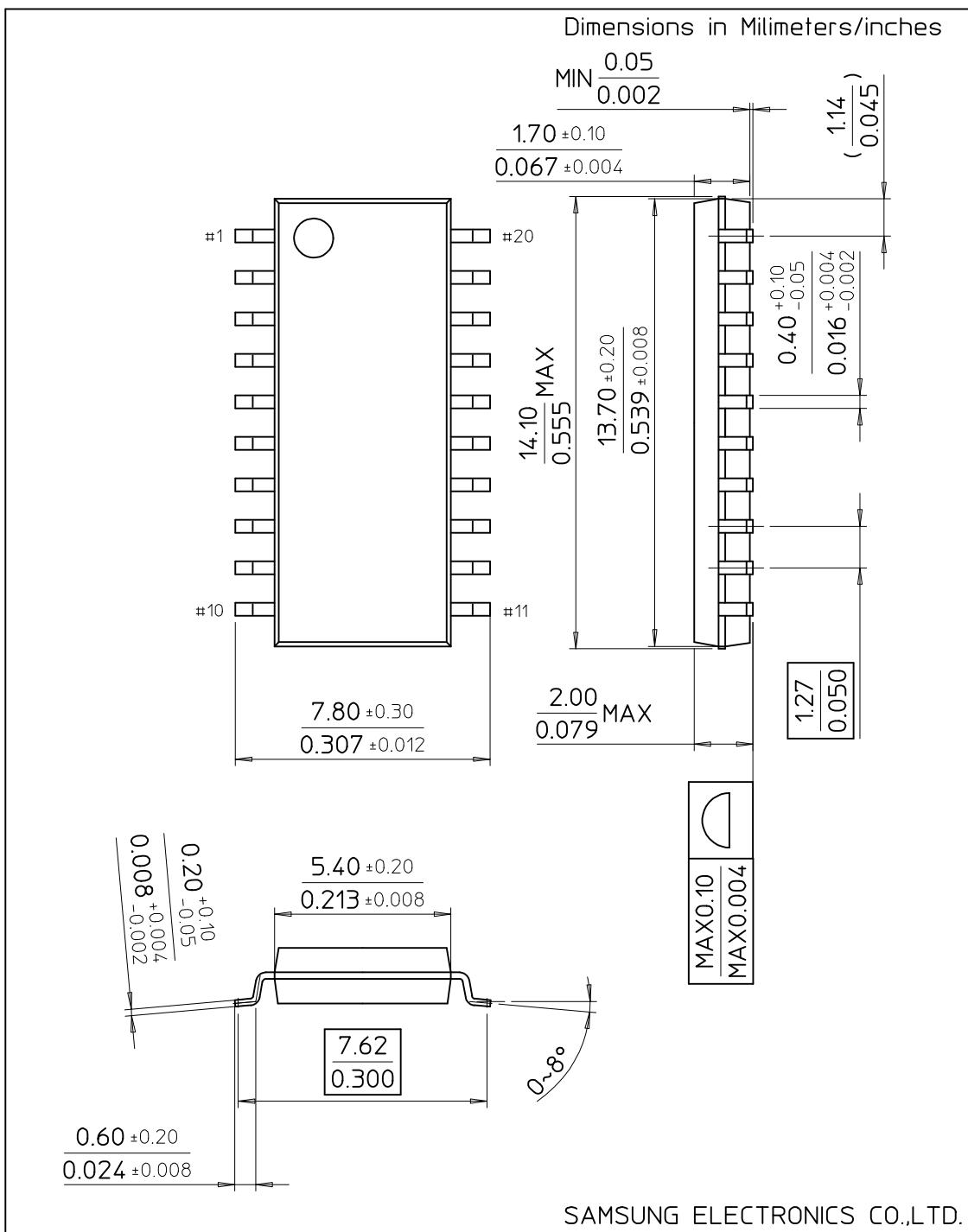
ORDERING INFORMATION

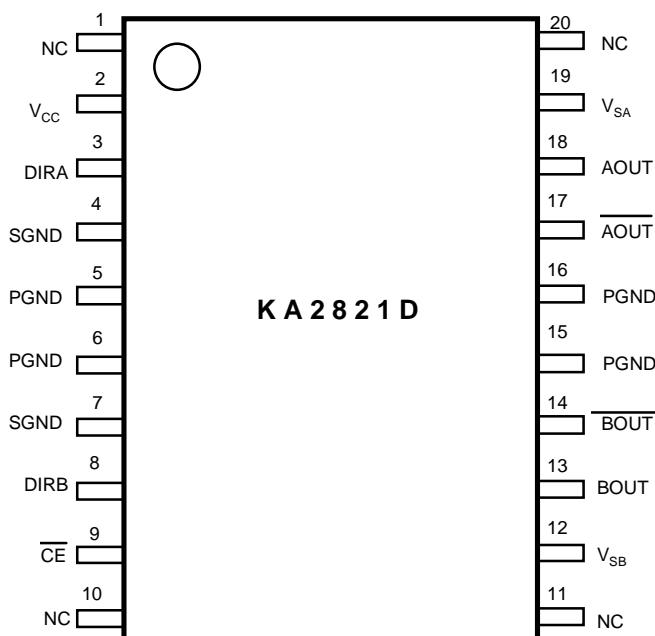
| Device | Package | Operating Temperature |
|---------|------------|-----------------------|
| KA2821D | 20-SOP-300 | - 20°C ~ + 75°C |

BLOCK DIAGRAM



20-SOP-300



PIN CONFIGURATION**PIN DESCRIPTION**

| Pin No. | Symbol | Function | Channel |
|---------|--------------------------|----------------------------------|---------|
| 1 | NC | No connection | |
| 2 | V _{CC} | Logic part supply voltage | A,B |
| 3 | DIRA | A-channel direction input | A |
| 4 | SGND | Signal ground | A,B |
| 5 | PGND | Power ground | A,B |
| 6 | PGND | Power ground | A,B |
| 7 | SGND | Signal ground | A,B |
| 8 | DIRB | B-channel direction input | B |
| 9 | CE | Chip Enable input | A,B |
| 10 | NC | No connection | |
| 11 | NC | No connection | |
| 12 | V _{SB} | B-channel seeking supply voltage | B |
| 13 | BOUT | B-channel output | B |
| 14 | $\overline{\text{BOUT}}$ | B-channel inverting output | B |
| 15 | P-GND | Power ground | A,B |
| 16 | P-GND | Power ground | A,B |
| 17 | $\overline{\text{AOUT}}$ | A-channel inverting output | A |
| 18 | AOUT | A-channel output | A |
| 19 | V _{SA} | A-channel seeking supply voltage | A |
| 20 | NC | No connection | |

ABSOLUTE MAXIMUM RATING (Ta=25°C)

| Characteristics | Symbol | Value | Unit |
|-------------------------------------|---------------------|--------------|-------------|
| Logic part supply voltage | V _{CC} | 7.0 | V |
| Seeking suply voltage | V _{SA,B} | 15.0 | V |
| Input voltage | V _{IN} | 0~VCC | V |
| Seeking output current (continuous) | I _{OS} | 330 | mA |
| Seeking output current (peak) | I _{OSPEAK} | 500 | mA |
| Package power dissipation | P _D | 1.0 | W |
| Operating temperature range | T _{OPR} | -20 to 75 | °C |
| Storage temperature range | T _{STG} | -40 to 125 | °C |

RECOMMAND OPERATING CONDITIONS

| Characteristics | Symbol | Min | Typ | Max | Unit |
|---------------------------|-------------------|------------|------------|------------|-------------|
| Logic part supply voltage | V _{CC} | 4.5 | 5.0 | 5.5 | V |
| Seeking supply voltage | V _{SA,B} | 4.5 | - | 13.8 | V |

ELECTRICAL CHARACTERISTICS(Ta = 25°C, V_{CC} = 5V, V_{SA} = 12V, V_{SB} = 12V, unless otherwise specified)

| Characteristics | Symbol | Conditions | Min | Typ | Max | Unit |
|---|------------------------------|----------------------------------|-----|-----|-----|------|
| Digital input "L" voltage | V _{IL} | | - | - | 0.8 | V |
| Digital high level input voltage | V _{IH} | | 2.0 | - | - | V |
| Digital low input current | I _{IL} | V _{IN} =0.8V | - | 0 | 10 | uA |
| Digital high input current | I _{IH1} | V _{IN} =2.0V | - | 1 | 10 | uA |
| | I _{IH2} | V _{IN} =5V | - | 0.3 | 1.0 | mA |
| | I _{V_{CCL}} | CE=0.8V | - | 25 | 3 | mA |
| | I _{V_{SL}} | CE=0.8V | - | 6 | 10 | mA |
| Supply current | I _{V_{CCH}} | CE=0.8V | - | 25 | 33 | mA |
| | I _{V_{SH}} | CE=2.0V | - | 1 | 2 | mA |
| Output sustain voltage | V _{SUS} | I _O =10mA CE=0.8V | 18 | - | - | V |
| V _{SA,B} output saturation voltage | V _{SAT1} | I _O =330mA CE=2.0V | - | 1.5 | 2.0 | V |
| Output clamp voltage | VFU | I _O =130mA (Upper) | - | 3.0 | 5.0 | V |
| | VFL | I _O =330mA (Lower) | - | 1.5 | 2.0 | V |
| Output delay time | T _{PLH} | Input Pulse (2KHz) | - | 1.0 | 5.0 | us |
| | T _{PHL} | Input Pulse (2KHz) | - | 1.0 | 5.0 | us |
| TSD operating temperature | TSD | | 125 | 150 | | °C |
| TSD hysteresis | △TSD | | - | 25 | | °C |

FUNCTION DESCRIPTION

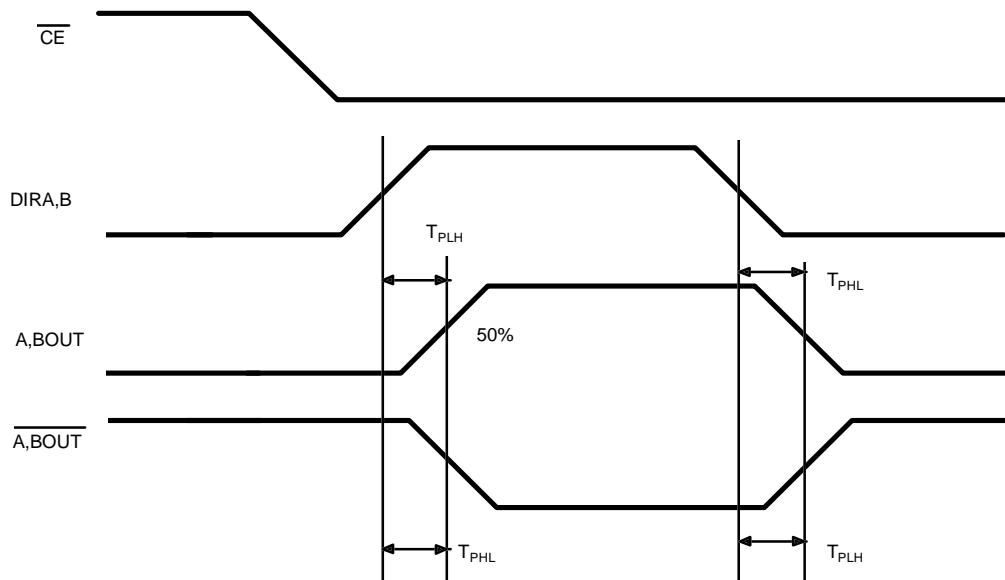
1. MOTOR CONTROL LOGIC

Mode Selection-Truth Table

| Input | | Output | | Operating Mode |
|-------|------|--------|-------------------|----------------|
| CE | DIRY | YOUT | \overline{YOUT} | |
| L | L | L | H | Seeking Mode |
| L | H | H | L | |
| H | L | X | X | Open Mode |
| H | H | X | X | |

- DIRY : DIRA or DIRB (Direction Input)
- YOUT : AOUT or BOUT (Non-Inverting Output)
- \overline{YOUT} : AOUT or BOUT (Inverting Output)
- Y : Indicate each channel (A and B)
- X : High Impedance

Timing Chart



2. MAXIMUM DRIVE CURRENT CAPACITY as follows

- Peak Seeking output current : 0.5A
- Continuous Seeking output current : 0.33A
- Holding output current : 0.2A

APPLICATION CIRCUIT

