

signetics

12-INPUT NAND GATE
WITH TRI-STATE OUTPUTS

S54S134
N74S134

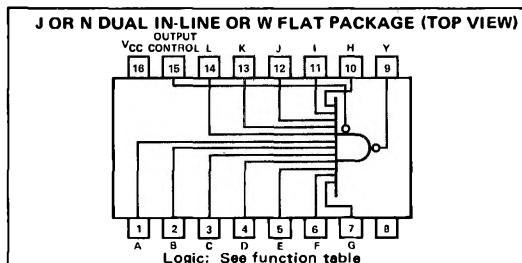
DIGITAL 54/74 TTL SERIES

FUNCTION TABLE

INPUTS												OUTPUT CONTROL	OUTPUT Y
A	B	C	D	E	F	G	H	I	J	K	L		
H	H	H	H	H	H	H	H	H	H	H	H	L	L
ANY NUMBER OF INPUTS LOW												L	H
X	X	X	X	X	X	X	X	X	X	X	X	H	Z

H = high logic level, L = low logic level, X = irrelevant
Z = high-impedance (output off)

PIN CONFIGURATION



RECOMMENDED OPERATING CONDITIONS

	S54S134			N74S134			UNIT
	MIN	NOM	MAX	MIN	NOM	MAX	
Supply voltage, V _{CC}	4.5	5	5.5	4.75	5	5.25	V
Normalized fan-out from each output, N	High logic level			40		130	
	Low logic level			10		10	
Operating free-air temperature, T _A	-55		125	0		70	°C

ELECTRICAL CHARACTERISTICS over recommended operating free-air temperature range unless otherwise noted

PARAMETER	TEST CONDITIONS*		S54S134		N74S134		UNIT
	MIN	TYP**	MAX	MIN	TYP**	MAX	
V _{IH} High-level input voltage				2		2	V
V _{IL} Low-level input voltage				0.8		0.8	V
V _I Input clamp voltage	V _{CC} = MIN, I _I = -18 mA			-1.2		-1.2	V
V _{OH} High-level output voltage	V _{CC} = MIN, V _{IL} = 0.8 V, I _{OH} = -2 mA	2.4	3.4				V
V _{OL} Low-level output voltage	V _{CC} = MIN, V _{IL} = 0.8 V, I _{OH} = -6.5 mA			2.4	3.2		
I _{O(off)} Off-state (high-impedance-state) output current	V _{CC} = MIN, V _O = 2 V, I _{OL} = 20 mA	0.5		0.5			V
I _I Input current at maximum input voltage	V _{CC} = MAX, V _I = 5.5 V			50		50	μA
I _{IH} High-level input current	V _{CC} = MAX, V _I = 2.7 V			-50		-50	μA
I _{IL} Low-level input current	V _{CC} = MAX, V _I = 0.5 V			1		1	mA
I _{OS} Short-circuit output current‡	V _{CC} = MAX	-40	-100	-40	-100		mA
I _{CC} Supply current	Output high	All inputs at 0 V		7	13	7	mA
	Output low	V _{CC} = MAX	Output control at 0 V, Other inputs at 5 V	9	16	9	
	Output off		All inputs at 5 V	14	25	14	

*For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions for the applicable device type.

**All typical values are at V_{CC} = 5 V, T_A = 25°C.

‡Duration of the short-circuit test should not exceed one second.

SWITCHING CHARACTERISTICS, V_{CC} = 5 V, T_A = 25°C, N = 10

PARAMETER	TEST CONDITIONS		MIN	TYP	MAX	UNIT	
t _{PLH} Propagation delay time, low-to-high-level output	C _L = 15 pF, R _L = 280 Ω		2	4	6	ns	
	C _L = 50 pF, R _L = 280 Ω				5.5		
t _{PHL} Propagation delay time, high-to-low-level output	C _L = 15 pF, R _L = 280 Ω		2	5	7.5		
	C _L = 50 pF, R _L = 280 Ω				7		
t _{ZH} Output enable time to high level	C _L = 50 pF, R _L = 280 Ω				13	19.5	ns
t _{ZL} Output enable time to low level					14	21	ns
t _{HZ} Output disable time from high level	C _L = 5 pF				5.5	8.5	ns
t _{LZ} Output disable time from low level					9	14	ns

NOTE 1: Load circuit and waveforms are shown on page 2-293