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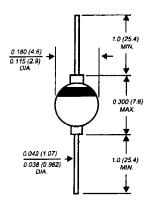
FAX: (973) 376-8960

1N5550 THRU 1N5552

GLASS PASSIVATED JUNCTION RECTIFIER

Reverse Voltage - 200 to 1000 Volts Forward Current - 3.0 Amperes

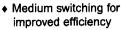
Case Style G4



Dimensions in inches and (millimeters)

FEATURES

- ◆ Glass passivated cavity-free junction
- High temperature metallurgically bonded construction
- · Hermetically sealed package
- · Capable of meeting environmental standards of MIL-S-19500



• High temperature soldering guaranteed: 350°C/10 seconds, 0.375" (9.5mm) lead length, 5 lbs. (2.3kg) tension

MECHANICAL DATA

Case: Solid glass body

Terminals: Solder plated axial leads,

Polarity: Color band denotes cathode end

Mounting Position: Any

Weight: 0.037 ounce, 1.04 grams

MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

Ratings at 25°C ambient temperature unless otherwise specified.

	SYMBOLS	1N5550	1N5551	1N5552	UNITS
*Maximum repetitive peak reverse voltage	VRRM	200	400	600	Volts
Maximum RMS voltage	VRMS	140′	280	420	Volts
*Maximum DC blocking voltage	VDC	200	400	600	Volts
*Minimum reverse breakdown voltage at 50µA	V(BR)	240	460	660	Volts
*Maximum average forward rectified current 0.375" (9.5mm) lead length at T _A =55°C	l(AV)	3.0			Amps
Peak forward surge current 8.3ms single half sine-wave superimposed on rated load (JEDEC Method)	İFSM	100.0			Amps
Maximum instantaneous forward voltage at 9.0A	VF	1.2			Volts
*Maximum DC reverse current TA=25°C at rated DC blocking voltage TA=100°C TA=200°C	le	1.0 25.0 1500.0			μА
*Maximum junction capacitance (NOTE 1)	C1	150	120	100	pF
*Maximum reverse recovery time (NOTE 2)	trr	2.0			μs
Typical thermal resistance (NOTE 3)	Reja Rejl	22.0 12.0			°C/W
*Operating and storage temperature range	ТЈ, Тѕтс	-65 to +200			°C

(1) Measured at 1.0 MHz and applied reverse voltage of 12.0 Volts
(2) Reverse recovery test conditions: IF=0.5A, IR=1.0A, In=0.25A
(3) Thermal resistance from junction to ambient and from junction to lead at 0.375" (9.5mm) lead length, with both leads mounted between heat sinks. *JEDEC registered values

>1 Semi-Conductors reserves the right to change test conditions, parameter limits and package dimensions without notice Information furnished by N1 Semi-Conductors is believed to be both accurate and reliable at the time of going to press. However \1 Semi-Conductors assumes no responsibility for any errors or omissions discovered in its use. St Semi-Conductors encourages more mers to verify that ablasheets are current before placing orders.

