

HIGH EFFICIENCY FAST RECOVERY RECTIFIER DIODES

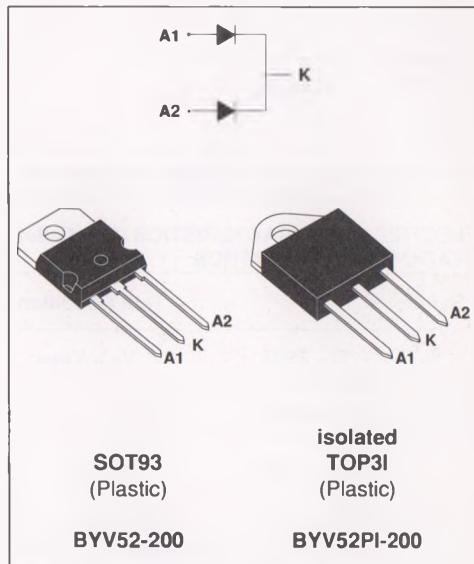
FEATURES

- SUITED FOR SMPS
- VERY LOW FORWARD LOSSES
- NEGLIGIBLE SWITCHING LOSSES
- HIGH SURGE CURRENT CAPABILITY
- HIGH AVALANCHE ENERGY CAPABILITY
- INSULATED VERSION TOP3I :
 - Insulating voltage = 2500 V DC
 - Capacitance = 12 pF

DESCRIPTION

Dual center tap rectifier suited for switchmode power supply and high frequency DC to DC converters.

Packaged in SOT93, or TOP3I this device is intended for use in low voltage, high frequency inverters, free wheeling and polarity protection applications.



ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter				Value	Unit
$I_{F(RMS)}$	RMS forward current			Per diode	50	A
$I_{F(AV)}$	Average forward current $\delta = 0.5$	SOT93	$T_c=110^\circ\text{C}$	Per diode	30	A
		TOP3I	$T_c=90^\circ\text{C}$	Per diode	30	
I_{FSM}	Surge non repetitive forward current		$t_p=10\text{ms}$ sinusoidal	Per diode	500	A
T_{stg} T_j	Storage and junction temperature range				- 40 to + 150	$^\circ\text{C}$
					- 40 to + 150	$^\circ\text{C}$

Symbol	Parameter	BYV52-/PI-				Unit
		50	100	150	200	
V_{RRM}	Repetitive peak reverse voltage	50	100	150	200	V

THERMAL RESISTANCE

Symbol	Parameter			Value	Unit
R _{th} (j-c)	Junction to case	SOT93	Per diode	1.2	°C/W
			Total	0.75	
	TOP3I		Per diode	1.8	
			Total	1.2	
R _{th} (c)	Coupling	SOT93		0.3	°C/W
		TOP3I		0.6	

When the diodes 1 and 2 are used simultaneously :

$$T_j - T_c (\text{diode } 1) = P(\text{diode } 1) \times R_{th(j-c)} (\text{Per diode}) + P(\text{diode } 2) \times R_{th(c)}$$

ELECTRICAL CHARACTERISTICS (Per diode)

STATIC CHARACTERISTICS

Symbol	Test Conditions		Min.	Typ.	Max.	Unit
I _R *	T _j = 25°C	V _R = V _{RRM}			25	µA
	T _j = 100°C				2.5	mA
V _F **	T _j = 125°C	I _F = 20 A			0.85	V
	T _j = 125°C	I _F = 40 A			1.00	
	T _j = 25°C	I _F = 40 A			1.15	

Pulse test : * tp = 5 ms, duty cycle < 2 %

** tp = 380 µs, duty cycle < 2 %

To evaluate the conduction losses use the following equation :

$$P = 0.7 \times I_{F(AV)} + 0.0075 \times I_F^2 (\text{RMS})$$

RECOVERY CHARACTERISTICS

Symbol	Test Conditions		Min.	Typ.	Max.	Unit
trr	T _j = 25°C	I _F = 0.5A I _R = 1A	I _{rr} = 0.25A		35	ns
		I _F = 1A V _R = 30V	dI _F /dt = -50A/µs		50	
tfr	T _j = 25°C	I _F = 1A V _{FR} = 1.1 x V _F	tr = 5 ns		10	ns
V _{FP}	T _j = 25°C	I _F = 1A	tr = 5 ns		1.5	V

Fig.1 : Average forward power dissipation versus average forward current.

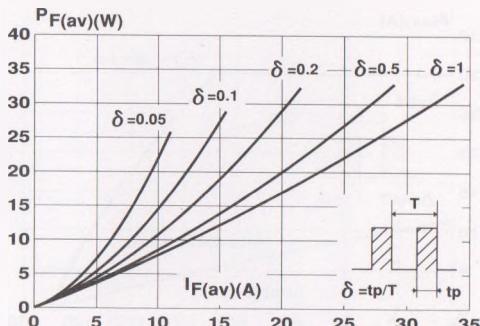


Fig.3 : Forward voltage drop versus forward current (maximum values).

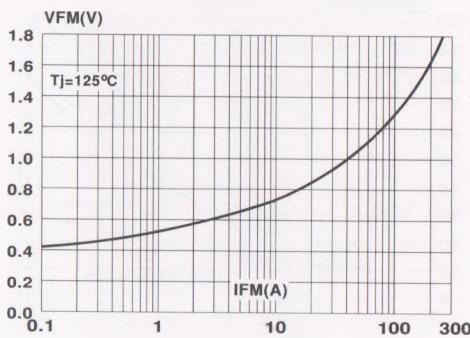


Fig.5 : Non repetitive surge peak forward current versus overload duration. (SOD93)

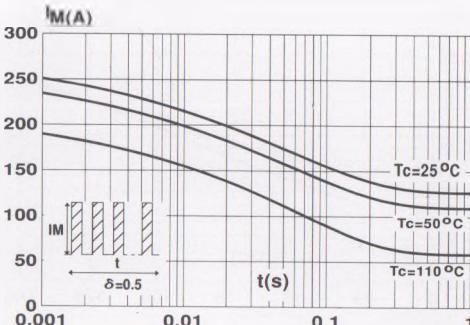


Fig.2 : Peak current versus form factor.

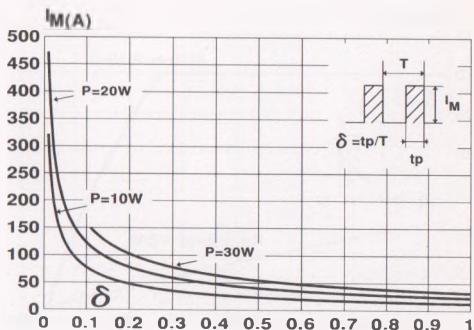


Fig.4 : Relative variation of thermal impedance junction to case versus pulse duration.

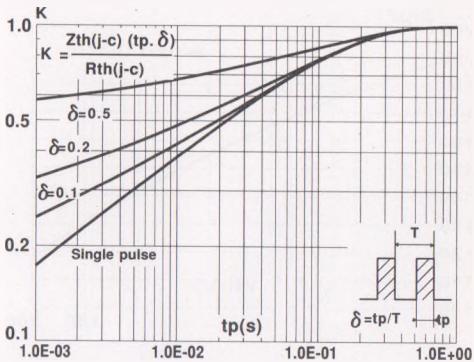


Fig.6 : Non repetitive surge peak forward current versus overload duration. (TOP3I)

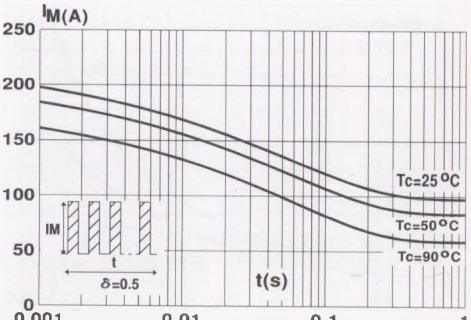


Fig.7 : Average current versus ambient temperature.
(duty cycle : 0.5) (SOD93)

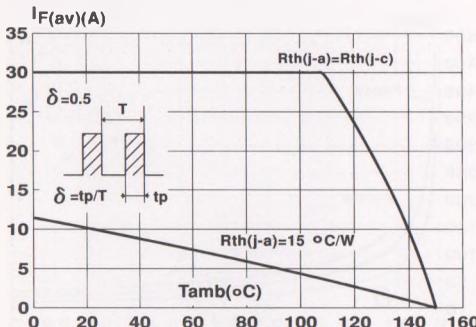


Fig.9 : Junction capacitance versus reverse voltage applied (Typical values).

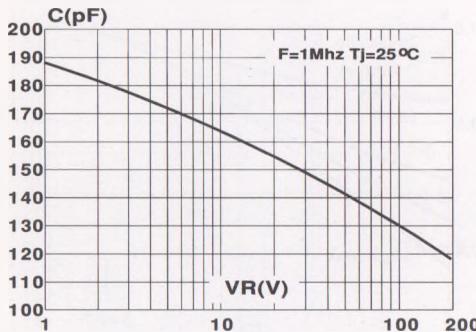


Fig.11 : Peak reverse current versus dIF/dt.

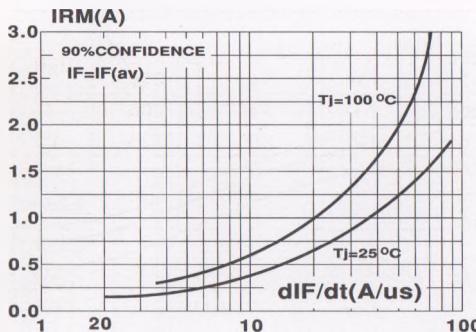


Fig.8 : Average current versus ambient temperature.
(duty cycle : 0.5) (TOP3I)

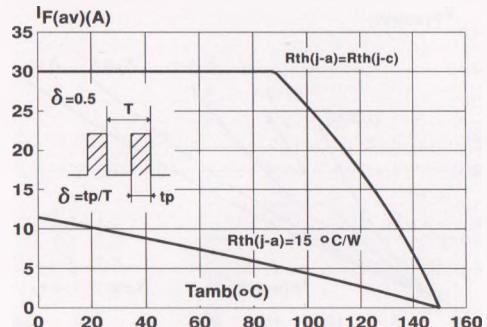


Fig.10 : Recovery charges versus dIF/dt.

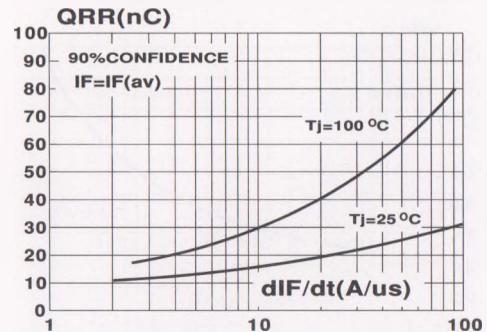


Fig.12 : Dynamic parameters versus junction temperature.

