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FD700/FDLL700 FD777/FDLL777

Ultra Fast Diodes

• C...1.0 pF (MAX) \oplus V_R = 0, 1 = 1.0 MHz (FD 700) • t_{fT} ...700 ps (MAX) \oplus t_{f} = 10 mA, R_L = 100 Ω (FD 700) • CONTROLLED FORWARD CONDUCTANCE

ABSOLUTE MAXIMUM RATINGS (Note 1)

| Temperatures | | FD700 | FD777 |
|----------------|------------------------------|--------------|-----------------|
| Storage Tem | perature Range65 | °C to +200°C | -65°C to +200°C |
| Max Junction | Operating Temperature | +175°C | +175°C |
| Lead Temper | | +260°C | +260°C |
| Power Dissipa | tion | | |
| Maximum Tot | al Dissipation at 25°C | | |
| Ambient | · | 250 mW | 250 mW |
| Linear Derati | ng Factor (from 25°C) | 1.67 mW/°C | 1.67 mW/°C |
| Maximum Volt | eges and Currents | | |
| WIV | Working Inverse Voltage | 20 V | 8.0 V |
| lo | Average Rectified Current | 50 mA | . 50 mA |
| | Forward Current Steady State | dc 150 mA | 150 mA |
| i F | Recurrent Peak Forward Curre | ent 150 mA | 150 mA |
| if (surge) | Peak Forward Surge Current | | |
| , ,041,847 | Pulse Width = 1.0 s | 250 mA | . 250 mA |

PACKAGES

DO-7 FD700 FD777 DO-7 LL-34 FDLL700 FDLL777 LL-34

If you need this device in the SOT package, an electical equivalent is available. See FDSO1700 family.

| SYMBOL | | FD700 | | FD777 | | UNITS | TEST CONDITIONS |
|-----------------|--------------------------------|-------|------|-------|------|-------|--|
| | CHARACTERISTIC | MIN | MAX | MIN | MAX | UNITS | TEST CONDITIONS |
| ٧F | Forward Voltage | 0.89 | 1,10 | 0.89 | 1.35 | v | }r = 60 mA |
| | , Olivera Actually | 0.81 | 0.95 | 0.81 | 1.00 | V | IF = 20 mA |
| | | 0.76 | 0.88 | 0.76 | 0.94 | V | IF = 10 mA |
| | | 0.64 | 0.74 | 0.64 | 0,79 | V | == 1.0 mA |
| | | 0.52 | 0.61 | 0.52 | 0.64 | V | l _F = 0.1 mA |
| | | 0.42 | 0.50 | 0.42 | 0.53 | ٧. | IF = 0.01 mA |
| BV | Breakdown Voltage | 30 | | 15 | | V | i _R = 5.0 μA |
| I _R | Reverse Current | | 50 | | | nΑ | V _R = 20 V |
| | 11010100 00110111 | | 1 | | 100 | nΑ | V _R = 8.0 V |
| | | | 50 | | 1 | μA | VR = 20 V, TA = 150°C |
| | | | | | 50 | μΑ | V _R = 8.0 V, T _A = 150°C |
| τ | Minority Carrier Lifetime | Ţ | 450 | | 450 | ps | (see Note 2) |
| t _{rr} | Reverse Recovery Time (Note 3) | | 700 | | 750 | ps | if = ir = 10 mA, RL = 100 Ω |
| C | Capacitance | | 1.0 | | 1,3 | pF | VR = 0, f = 1.0 MHz |

ROTES:

1. The maximum ratings are limiting values above which life or satisfactory performance may be impaired.

2. Measured as suggested by S. M. Krakauer, IRE Proceedings, Volume 80, July 1962, pp. 1674 - 1876.

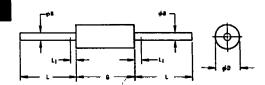
3. Recovery to 0.1 [q.,

4. For product family characteristic curves, refer to Chapter 4, D3.



NJ Semi-Conductors reserves the right to change test conditions, parameter limits and package dimensions without notice. Information furnished by NJ Semi-Conductors is believed to be both accurate and reliable at the time of going to press. However, NJ Semi-Conductors assumes no responsibility for any errors or omissions discovered in its use. NJ Semi-Conductors encourages customers to verify that datasheets are current before placing orders.





NOTES:

- 1. PACKAGE CONTOUR OPTIONAL WITHIN CYLINDER OF DIAMETER 8D AND LENGTH G. SLUGS, IF ANY, SMALL BE INCLUDED WITHIN THIS CYLINDER BUT SHALL NOT BE SUBJECT TO THE HINIMUM LIMIT OF 8D.

 2. LEAD DIAMETER NOT CONTROLLED IN THIS ZONE TO ALLOW FOR FLASH, LEAD FINISH BUILD-UP, AND MINOR TRREGULARITIES OTHER THAN SLUGS.

MILLIMETER DIMENSIONS ARE DERIVED FROM ORIGINAL INCH DIMENSIONS

| SYMBOL | INC. | MAX. | MILLII | NOTES | |
|----------------|-------|------|--------|-------|---|
| #8 | .018 | .022 | 0.458 | 0.558 | - |
| ₫Đ | .085 | .107 | 2.16 | 2.71 | 1 |
| G | .230 | .300 | 5.85 | 7.62 | 1 |
| L | 1.000 | - | 25.40 | - | |
| L ₁ | - | .050 | - | 1.27 | 2 |