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November 2013

ISL9R860P2, ISL9R860S3ST 8 A, 600 V, STEALTH™ Diode

Features

- Stealth Recovery trr = 28 ns (@ IF = 8 A)
- Max Forward Voltage, VF = 2.4 V (@ TC = 25°C)
- 600 V Reverse Voltage and High Reliability
- Avalanche Energy Rated
- RoHS Compliant

Applications

- SMPS FWD
- · Hard Switched PFC Boost Diode
- · UPS Free Wheeling Diode
- Motor Drive FWD
- · Snubber Diode

Description

The ISL9R860P2, ISL9R860S3ST is a STEALTH™ diode optimized for low loss performance in high frequency hard switched applications. The STEALTH™ family exhibits low reverse recovery current (I_{RR}) and exceptionally soft recovery under typical operating conditions. This device is intended for use as a free wheeling or boost diode in power supplies and other power switching applications. The low I_{RR} and short ta phase reduce loss in switching transistors. The soft recovery minimizes ringing, expanding the range of conditions under which the diode may be operated without the use of additional snubber circuitry. Consider using the STEALTH™ diode with an SMPS IGBT to provide the most efficient and highest power density design at lower cost.

Package JEDEC TO-220AC-2L JEDEC TO-263AB(D²-PAK) CATHODE (FLANGE) N/C ANODE N/C ANODE

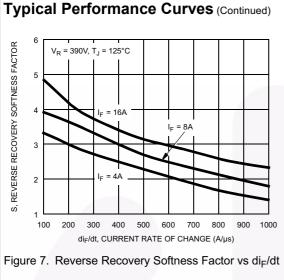
Device Maximum Ratings T_C= 25°C unless otherwise noted

Symbol	Parameter	Ratings	Unit
V_{RRM}	Peak Repetitive Reverse Voltage	600	V
V _{RWM}	Working Peak Reverse Voltage	600	V
V _R	DC Blocking Voltage	600	V
I _{F(AV)}	Average Rectified Forward Current (T _C = 147°C)	8	Α
I _{FRM}	Repetitive Peak Surge Current (20kHz Square Wave)	16	Α
I _{FSM}	Nonrepetitive Peak Surge Current (Halfwave 1 Phase 60Hz)	100	Α
P _D	Power Dissipation	85	W
E _{AVL}	Avalanche Energy (1 A, 40 mH)	20	mJ
T _J , T _{STG}	Operating and Storage Temperature Range	-55 to 175	°C
T _L T _{PKG}	Maximum Temperature for Soldering Leads at 0.063in (1.6mm) from Case for 10s Package Body for 10s, See Techbrief TB334	300 260	°C

CAUTION: Stresses above those listed in "Absolute Maximum Ratings" may cause permanent damage to the device. This is a stress only rating and operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied.

Part Num	ber	Top Mark	Package	Packing Met	hod	Reel Size	Ta	oe Wic	lth C	Quantit	
ISL9R860P2		ISL9R860P2	TO-220AC-2L	Tube		N/A		N/A		50	
SL9R860S3ST ISL9R860		ISL9R860S3ST	TO-263AB(D ² -PAK	Reel		13" Dia		24mm		800	
Electric	al (Characteris	Stics T _C = 25°C u	nless otherwise n	oted						
Symbol			Test Conditions			Min	Тур	Max	Unit		
Off State	Ch	aracteristics				<u> </u>				- I	
I _R		Instantaneous Reverse Current		$V_R = 600 \text{ V}$ T_C	To =	= 25°C	_	-	100	μА	
'R	11130				T _C =	= 125°C		-	1.0	mΑ	
					1.0			1		1	
		aracteristics							•		
V _F	Inst	antaneous Forwa	rd Voltage	I _F = 8 A	T _C =	= 25°C	-	2.0	2.4	V	
					T _C =	= 125°C	-	1.6	2.0	V	
Dynamic	Ch	aracteristics									
CJ	Jun	ction Capacitance	9	V _R = 10 V, I _F = 0 A	١		-	30	-	pF	
Switchin	a C	haracteristic	8								
t _{rr}		erse Recovery T		I _F = 1 A, di _F /dt = 1	00 A/u:	s. Vp = 30 V	_	18	25	ns	
- 11	The verse receivery rame			$I_F = 8 \text{ A}, \text{ dig/dt} = 100 \text{ A/µs}, \text{ V}_R = 30 \text{ V}$			-	21	30	ns	
t _{rr}	Rev	everse Recovery Time		$I_F = 8 \text{ A},$ $di_F/dt = 200 \text{ A/}\mu\text{s},$ $V_R = 390 \text{ V}, T_C = 25^{\circ}\text{C}$			-	28	-	ns	
I _{rr}	Rev	everse Recovery Current					-	3.2	-	Α	
Q _{rr}	_	verse Recovery Charge					-	50	-	nC	
t _{rr}	Rev	verse Recovery Time		I _F = 8 A,			-	77	-	ns	
S	Soft	ftness Factor (t _b /t _a)		$di_F/dt = 200 A/\mu s$,		-	3.7	-			
l _{rr}	Rev	everse Recovery Current		$V_R = 390 \text{ V},$		-	3.4	-	Α		
Q _{rr}	Rev	erse Recovery C	harge	T _C = 125°C		Ť	-	150	-	nC	
t _{rr}	Rev	erse Recovery T	me	I _F = 8 A,		-	53	-	ns		
S	Soft	Softness Factor (t _b /t _a) Reverse Recovery Current		di _F /dt = 600 A/μs, V _R = 390 V,		-	2.5	-			
Irr	_					-	6.5	-	Α		
Q _{rr}	Rev	erse Recovery C	harge	T _C = 125°C			195	-	nC		
dl _M /dt	Max	imum di/dt during	ı t _b				-	500	-	A/µs	
	•		_								
		racteristics	lumatian ta Oaa						4 75	90/11/	
R _{0JC}	+	hermal Resistance Junction to Case		TO 220		-	-	1.75	°C/W		
R _{θJA}		Thermal Resistance Junction to Ambient Thermal Resistance Junction to Ambient				-	-	62	°C/W		
R_{\thetaJA}	ııne	rmai kesistance	JUNCTION TO AMBIENT	TO-263					62	°C/W	

Typical Performance Curves 175°C 150°C REVERSE CURRENT (µA) FORWARD CURRENT (A) 12 10 125°C 8 100°C 100°C 6 2 0.5 0.75 1.25 1.5 1.75 V_F, FORWARD VOLTAGE (V) V_R, REVERSE VOLTAGE (V) Figure 1. Forward Current vs Forward Voltage Figure 2. Reverse Current vs Reverse Voltage V_R = 390V, T_J = 125°C V_R = 390V, T_J = 125°C t_b AT $d_F/dt = 200A/\mu s$, 500A/ μs , 800A/ μs 60 t, RECOVERY TIMES (ns) t, RECOVERY TIMES (ns) 60 50 40 30 30 20 10 10 t_a AT $di_F/dt = 200A/\mu s$, $500A/\mu s$, $800A/\mu s$ 300 400 500 600 700 800 900 1000 di_F/dt, CURRENT RATE OF CHANGE (A/µs) 100 I_F, FORWARD CURRENT (A) Figure 4. t_a and t_b Curves vs di_F/dt Figure 3. t_a and t_b Curves vs Forward Current V_R = 390V, T_J = 125°C $V_R = 390V, T_J = 125^{\circ}C$ $di_F/dt = 800A/\mu s$ € MAX REVERSE RECOVERY CURRENT (A) 10 MAX REVERSE RECOVERY CURRENT 12 9 10 8 $di_F/dt = 500A/\mu s$ 7 6 6 5 $di_F/dt = 200A/\mu s$ 0 16 I_F, FORWARD CURRENT (A) di_F/dt, CURRENT RATE OF CHANGE (A/μs) Figure 5. Maximum Reverse Recovery Current Figure 6. Maximum Reverse Recovery Current vs Forward Current vs di_F/dt



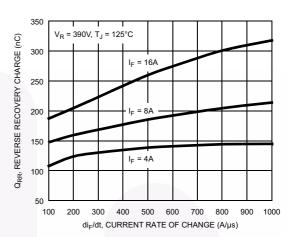


Figure 8. Reverse Recovery Charge vs di_F/dt

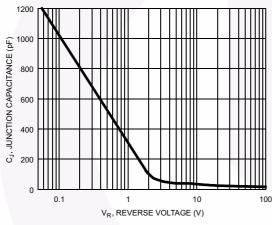


Figure 9. Junction Capacitance vs Reverse Voltage

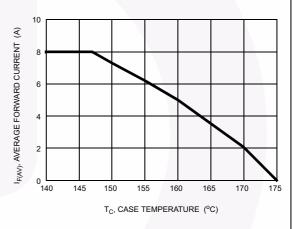


Figure 10. DC Current Derating Curve

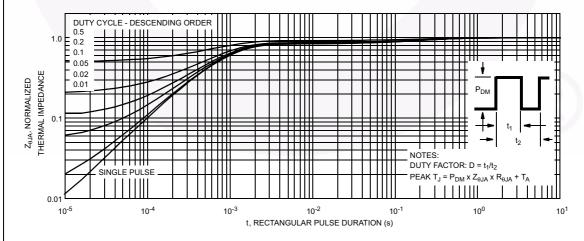
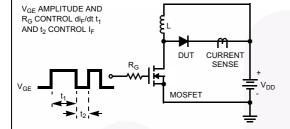


Figure 11. Normalized Maximum Transient Thermal Impedance

Test Circuits and Waveforms



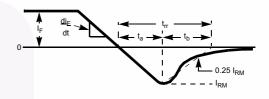
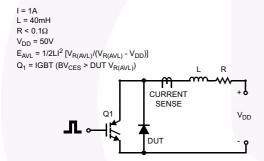


Figure 12. t_{rr} Test Circuit

Figure 13. t_{rr} Waveforms and Definitions



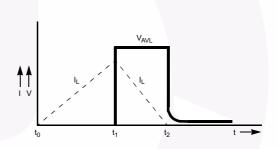


Figure 14. Avalanche Energy Test Circuit

Figure 15. Avalanche Current and Voltage Waveforms

→ 0.36M B AM 10.67 9.65 3.43 2.54 13.40 12.19 16.51 9.40 2 1.78 MAX 6.35 14.73 0.61 (1.91)2.54 ◆ 0.38M B AM 5.08 NOTES: UNLESS OTHERWISE SPECIFIED A) REFERENCE JEDEC, TO—220, ISSUE K, VARIATION AC, DATED APRIL 2002. B) ALL DIMENSIONS ARE IN MILLIMETERS. DIMENSIONS ARE INCLUSIVE OF BURRS, MOLD FLASH, AND TIE BAR EXTRUSIONS. C) _ DIMENSIONING AND TOLERANCING PER ANSI Y14.5 - 1973 D)

Figure 16. TO-220 2L - 2LD,TO220,JEDEC TO-220 VARIATION AC

E)

IS OPTIONAL

PRESENCE OF TRIMMED CENTER LEAD

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Mechanical Dimensions

Package Dimensions 9.45 10.00 (6.40)1.78 MAX 3.80 (2.12)5.08 LAND PATTERN RECOMMENDATION UNLESS NOTED, ALL DIMS TYPICAL 5.08 → 0.25 M B AM 6.22 MIN 6.86 MIN 15.88 14.61 SEE DETA|L A NOTES: UNLESS OTHERWISE SPECIFIED A) ALL DIMENSIONS ARE IN MILLIMETERS. B) REFERENCE JEDEC, TO-263, VARIATION AB. C) DIMENSIONING AND TOLERANCING PER ANSI Y14,5M - 1994. D) LOCATION OF THE PIN HOLE MAY VARY GAGE PLANE (LOWER LEFT CORNER, LOWER CENTER AND CENTER OF THE PACKAGE). E) LANDPATTERN RECOMMENDATION PER IPC 0.25 TO254P1524X482-3N F) FILENAME: TO263A02REV6 O.10 B 0.25 MAX (5.38)

Figure 17. TO-263 2L (D2PAK) - 2LD,TO263, SURFACE MOUNT

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