

SANYO Semiconductors DATA SHEET

An ON Semiconductor Company

SB05-05P — Schottky Barrier Diode

50V, 500mA Rectifier

Applications

· High frequency rectification (switching regulators, converters, choppers)

Features

- Low forward voltage (VF max=0.55V)
- · Low switching noise
- · Low leakage current and high reliability due to highly reliable planar structure

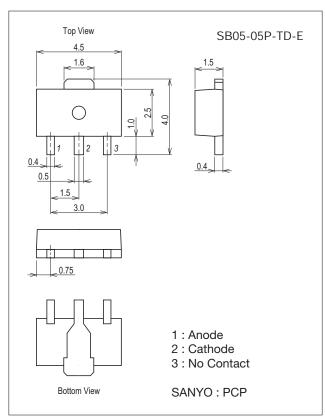
Specifications

Absolute Maximum Ratings at Ta=25°C

| Parameter | Symbol | Conditions | Ratings | Unit |
|--|------------------|-------------------------|-------------|------|
| Repetitive Peak Reverse Voltage | VRRM | | 50 | V |
| Nonrepetitive Peak Reverse Surge Voltage | VRSM | | 55 | V |
| Average Output Current | IO | | 500 | mA |
| Surge Forward Current | I _{FSM} | 50Hz sine wave, 1 cycle | 5 | Α |
| Junction Temperature | Tj | | -55 to +125 | °C |
| Storage Temperature | Tstg | | -55 to +125 | °C |

Package Dimensions

unit : mm (typ) 7007B-001



Product & Package Information

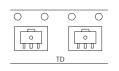
• Package : PCP

• JEITA, JEDEC : SC-62, SOT-89, TO-243

• Fast reverse recovery time (trr max=10ns)

• Minimum Packing Quantity: 1,000 pcs./reel

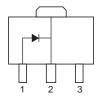
Packing Type: TD



Marking



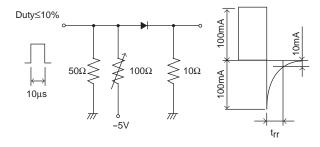
Electrical Connection



Electrical Characteristics at Ta=25°C

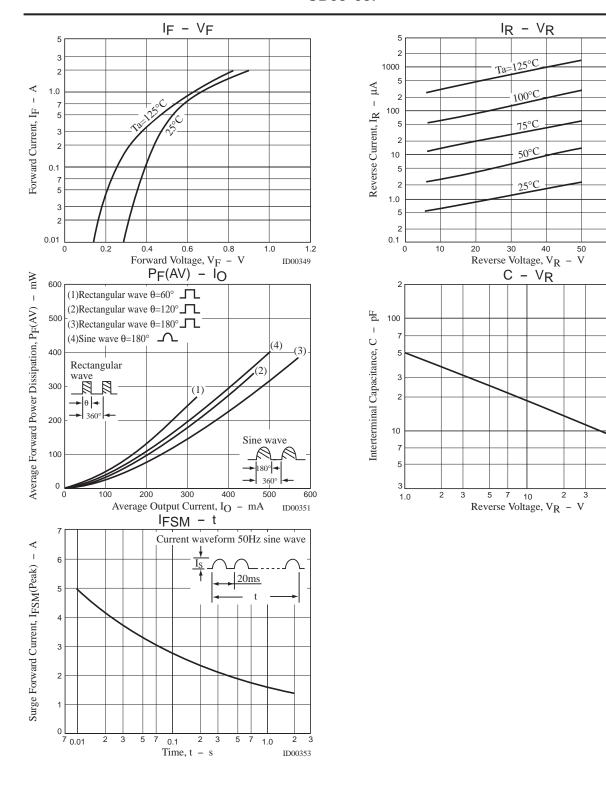
| Parameter | Symbol | Conditions | Ratings | | | Unit |
|---------------------------|-----------------|---|---------|-----|------|-------|
| | | Conditions | min | typ | max | UTIIL |
| Reverse Voltage | VR | I _R =200μA | 50 | | | V |
| Forward Voltage | VF | I _F =500mA | | | 0.55 | V |
| Reverse Current | IR | V _R =25V | | | 50 | μΑ |
| Interterminal Capacitance | С | V _R =10V, f=1MHz | | 18 | | pF |
| Reverse Recovery Time | t _{rr} | IF=IR=100mA, See specified Test Circuit. | | | 10 | ns |
| Thermal Resistance | Rth(j-a)1 | | | 310 | | °C/W |
| | Rth(j-a)2 | When mounted on ceramic substrate (250mm ² ×0.8mm) | | 120 | | °C/W |

t_{rr} Test Circuit



Ordering Information

| Device | Package | Shipping | memo | |
|---------------|---------|----------|---------|--|
| SB05-05P-TD-E | | | Pb Free | |



ID00350

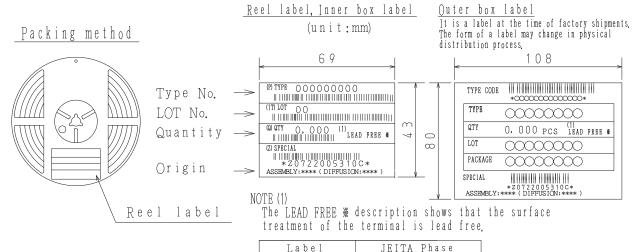
f=1MHz

ID00352

Embossed Taping Specification SB05-05P-TD-E

1. Packing Format

| Package Name | Carrier Tape | Maximum Number of devices contained (pcs) | | | Packing | format | | |
|--------------|--------------|--|-----------|-----------|--------------------------|--------------------------|--|--|
| | Туре | Reel | Inner box | Outer box | Inner BOX (C-1) | Outer BOX (A-7) | | |
| PCP | PCP | 1, 000 | 4,000 | 24,000 | 4 reels contained | 6 inner boxes contained | | |
| | | | | | Dimensions:mm (external) | Dimensions:mm (external) | | |
| | | | | | 183×72×185 | 440×195×210 | | |



LEAD FREE 3

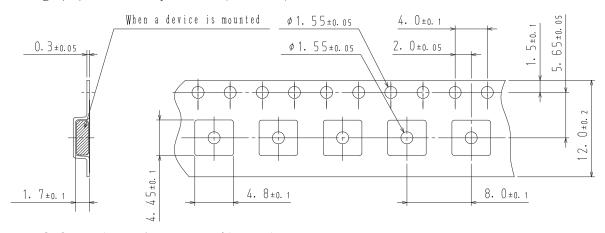
LEAD FREE 4

JEITA Phase 3A

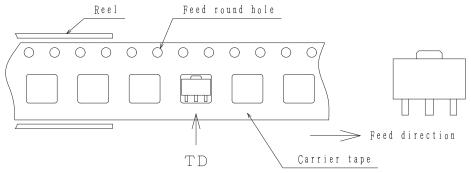
JEITA Phase 3

2. Taping configuration

2-1. Carrier tape size (unit:mm)



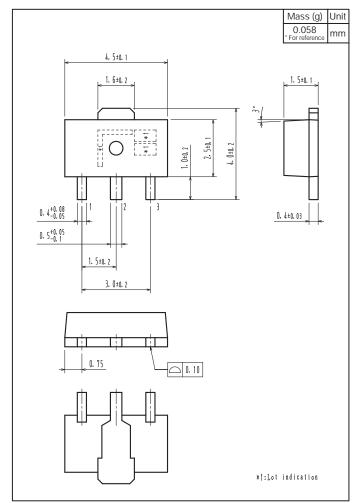
2-2. Device placement direction



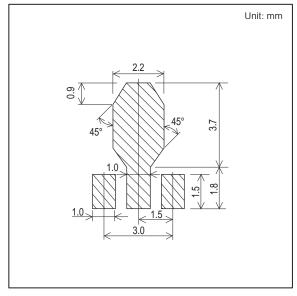
Those with pin 1 index on the feed hole side·····TD

Outline Drawing

SB05-05P-TD-E



Land Pattern Example



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