

POWER SCHOTTKY RECTIFIER

MAIN PRODUCT CHARACTERISTICS

I _{F(AV)}	2 x 15 A
V _{RRM}	45 V
V _F	0.57 V

FEATURES AND BENEFITS

- VERY SMALL CONDUCTION LOSSES
- NEGLIGIBLE SWITCHING LOSSES
- HIGH AVALANCHE CAPABILITY
- HIGH DISSIPATION MINIATURE PACKAGE
- SURFACE MOUNT TECHNOLOGY COMPATIBLE

DESCRIPTION

Dual schottky rectifier suited for switchmode power supply and high frequency DC to DC converters.

Packaged in a high performance surface mount package PSO-10, this device is intended for use in low voltage, high frequency inverters, free wheeling and polarity protection applications.



ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter			Value	Unit
V _{RRM}	Repetitive Peak Reverse Voltage			45	V
I _{F(RMS)}	RMS Forward Current	All pins connected	Per diode	27	A
I _{F(AV)}	Average Forward Current $\delta = 0.5$	T _c = 135°C	Per diode	15	A
			Per device	30	
I _{FSM}	Surge Non Repetitive Forward Current	tp = 10 ms Sinusoidal All pins connected	Per diode	220	A
I _{IRRM}	Repetitive Peak Reverse Current	tp = 2 µs F = 1KHz	Per diode	1	A
T _{stg} T _j	Storage and Junction Temperature Range			- 65 to + 150	°C
dV/dt	Critical Rate of Rise of Reverse Voltage			1000	V/µs

TM : PowerSO-10 is a trademark of SGS-THOMSON Microelectronics.

THERMAL RESISTANCES

Symbol	Parameter	Value	Unit
$R_{TH(j-c)}$	Junction to Case Thermal Resistance	1.6 0.85	°C/W
$R_{TH(c)}$	Coupling Thermal Resistance	0.1	°C/W

STATIC ELECTRICAL CHARACTERISTICS (Per diode)

Symbol	Tests Conditions	Tests Conditions		Min.	Typ.	Max.	Unit
I_R *	Reverse leakage Current	$T_j = 25^\circ\text{C}$	$V_R = V_{RRM}$			200	μA
		$T_j = 125^\circ\text{C}$				40	mA
V_F **	Forward Voltage drop	$T_j = 125^\circ\text{C}$	$I_F = 30 \text{ A}$			0.72	V
		$T_j = 125^\circ\text{C}$	$I_F = 15 \text{ A}$			0.57	
		$T_j = 25^\circ\text{C}$	$I_F = 30 \text{ A}$			0.84	

Pulse test : * $t_p = 5 \text{ ms}$, duty cycle < 2 %** $t_p = 380 \mu\text{s}$, duty cycle < 2%

To evaluate the conduction losses use the following equation :

$$P = 0.42 \times I_{F(AV)} + 0.010 I_F^2(\text{RMS})$$

PIN OUT configuration in PowerSO-10 :

Anode 1 = pin 1 to 5

Anode 2 = pin 6 to 10

Cathodes = connected to base tab

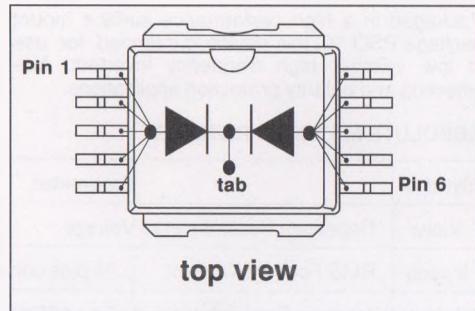


Fig. 1 : Average forward power dissipation versus average forward current. (Per diode)

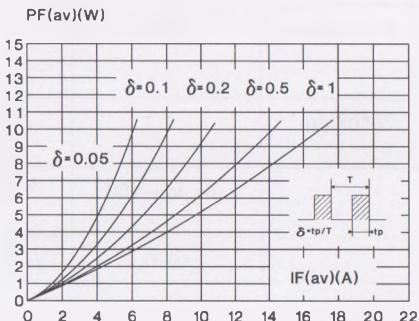


Fig. 2 : Average current versus ambient temperature.

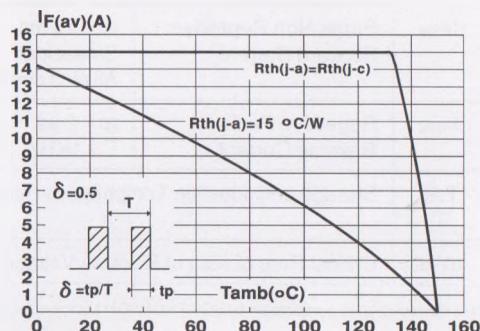


Fig. 3 : Non repetitive surge peak forward current versus overload duration.
(Maximum values) (Per diode)

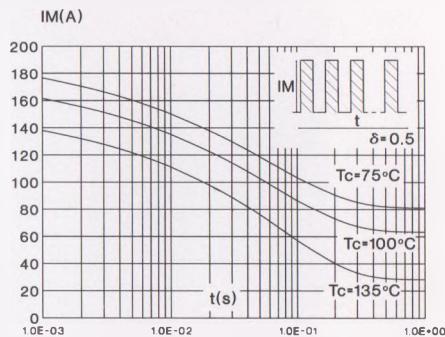


Fig. 5 : Reverse leakage current versus reverse voltage applied. (Typical values) (Per diode)

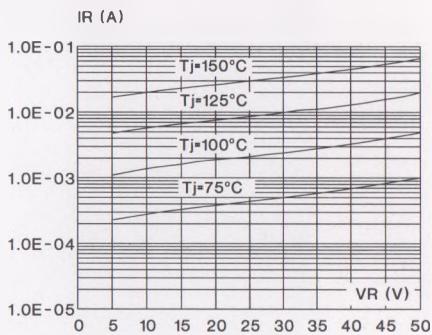


Fig. 7 : Forward voltage drop versus forward current. (Maximum values) (Per diode)

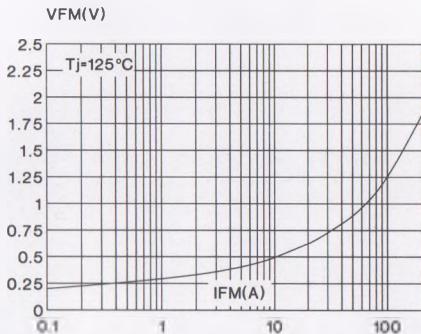


Fig. 4 : Relative variation of thermal transient impedance junction to case versus pulse duration.

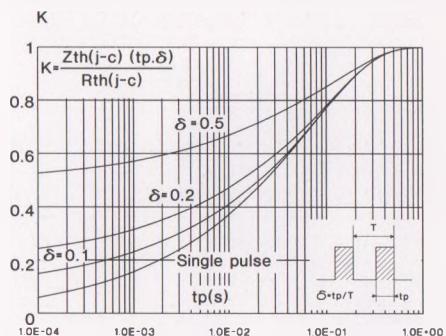


Fig. 6 : Junction capacitance versus reverse voltage applied. (Typical values) (Per diode)

