

New Jersey Semi-Conductor Products, Inc.

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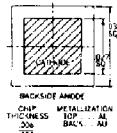
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RECTIFIERS High Efficiency, 2A

UES1104
UES1105
UES1106

FEATURES

- Very Low Forward Voltage (1.15V)
- Very Fast Recovery Times (50nSec)
- Small Size
- Convenient Package



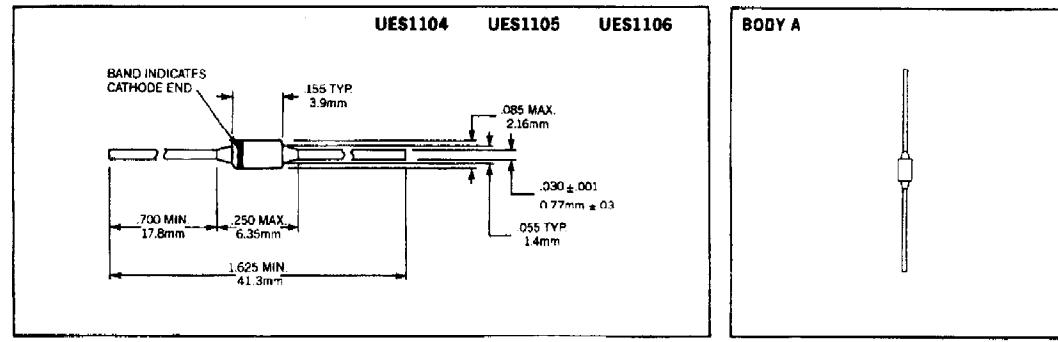
DESCRIPTION

The UES1104 series is specifically designed for operation in power switching circuits operating at frequencies of at least 20 KHz.

ABSOLUTE MAXIMUM RATINGS

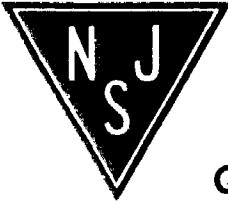
Peak Inverse Voltage, UES1104	200V
Peak Inverse Voltage, UES1105	300V
Peak Inverse Voltage, UES1106	400V
Maximum Average DC Output Current, I_O	
@ $T_A = 25^\circ\text{C}$ (Free Air)	1A
@ $T_L = 50^\circ\text{C}$, $L = \frac{3}{4}''$	2A
Surge Current, 8.3mSec	20A
Thermal Resistance @ $L = \frac{3}{4}''$.38°C/W
Operating and Storage Temperature Range	-55°C to +150°C

MECHANICAL SPECIFICATIONS



NJ Semi-Conductors reserves the right to change test conditions, parameter limits and package dimensions without notice. Information furnished by NJ Semi-Conductors is believed to be both accurate and reliable at the time of going to press. However, NJ Semi-Conductors assumes no responsibility for any errors or omissions discovered in its use. NJ Semi-Conductors encourages customers to verify that datasheets are current before placing orders.

Quality Semi-Conductors

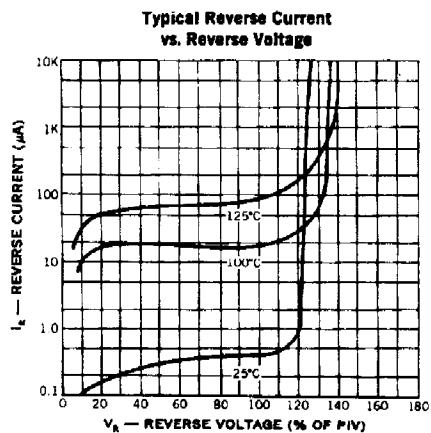
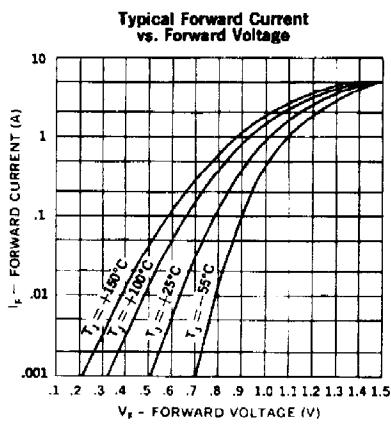
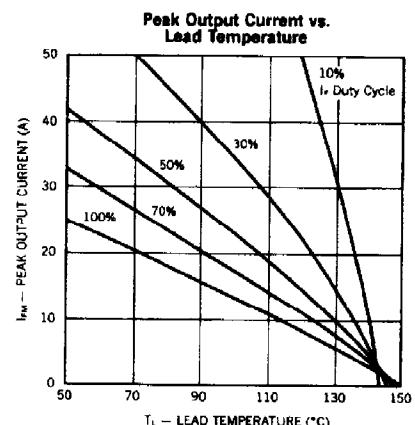
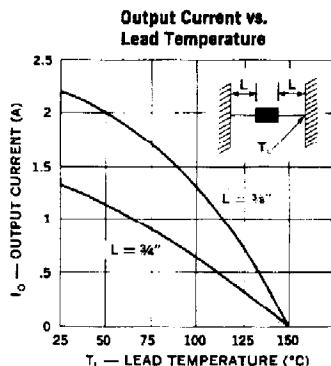


UES1104 UES1105 UFS1106

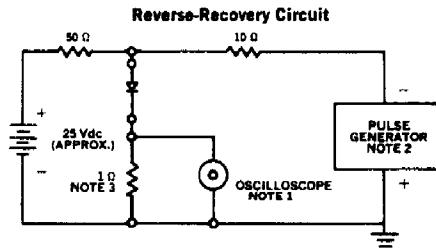
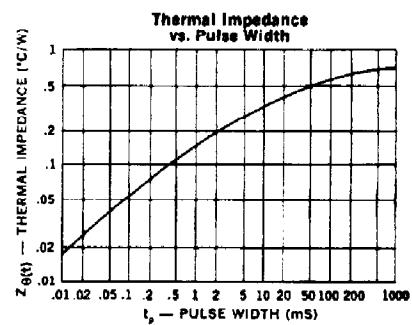
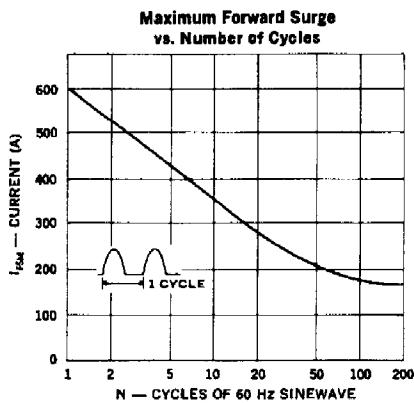
ELECTRICAL SPECIFICATIONS

Type	PIV	Maximum Forward Voltage		Maximum Reverse Current		Maximum Reverse Recovery Time*
		$T_J = 25^\circ\text{C}$	$T_J = 100^\circ\text{C}$	@ PIV, $T_J = 25^\circ\text{C}$	$T_J = 100^\circ\text{C}$	
UES1104/1104HR	200V	1.25V	1.15V			
UES1105/1105HR	300V	@ 1A	@ 1A			
UES1106/1106HR	400V	tp = 300μS	tp = 300μS	10μA	200μA	50nS

* Measured in circuit $I_F = 0.5\text{A}$, $I_R = 1\text{A}$, $I_{REC} = 0.25\text{A}$



UES804 UES805 UES806
UES804HR2 UES805HR2 UES806HR2



NOTES:

1. Oscilloscope: Rise time \leq 3ns; input impedance = 500.
2. Pulse Generator: Rise time \leq 8ns; source impedance 100.
3. Current viewing resistor, non-inductive, coaxial recommended.

OPTIONAL HIGH RELIABILITY (HR2) SCREENING

The following tests are performed on 100% of the devices specified UES804HR2, 5HR2, 6HR2.

SCREEN	MIL-STD-750 METHOD	CONDITIONS
1. High Temperature	1032	24 Hours @ T _A = 150°C
2. Temperature Cycle	1051	F, 20 Cycles, -55 to +150°C. No dwell required @ 25°C, t > 10 min. @ extremes
3. Hermetic Seal a. Fine Leak b. Gross Leak	1071	H, Helium C, Liquid
4. Thermal Impedance		Sage Test
5. Interim Electrical Parameters	GO/NO GO	V _F and I _R @ 25°C
6. High Temperature Reverse Blocking	Similar to Method 1040	½ Sine Reverse, t = 48 Hours, T _C = 125°C, VRW _M = rating, F = 50-60 Hz, I _O = 0A
7. Final Electrical Parameters	GO/NO GO	V _F + I _R @ 25°C PDA = 10% (Final Electricals)