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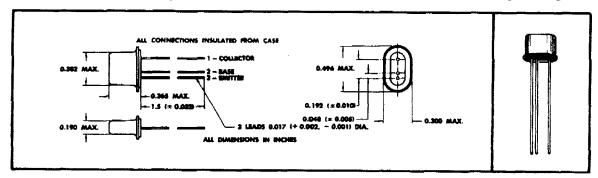
-65°C to +175°C

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## TYPE 2N119 N-P-N GROWN-JUNCTION SILICON TRANSISTOR

## 36 to 86 beta spread Specifically designed for high gain at high temperatures

mechanical data
Welded case with glass-to-metal hermetic seal between case and leads. Approximate weight is 1.7 grams.



absolute maximum ratings at 25°C ambient (except where advanced temperatures are indicated)

Collector Voltage											
Emitter Voltage	Referre	d to	Base	, ,							1 V
Collector Current											
Emitter Current											-25  mA
Collector Dissipati	on }										150 mW
at 100	°C }										100 mW
at 150	°C }										50 mW
junction temperature											

common base design characteristics at Tj = 25°C (except where advanced temperatures are indicated)

		test' con	ditions	min.	design conter	mex.	wait
BVcmo	Collector Breakdown Voltage	Ic = 50µA	JE = 0	45			Volt
Iceo	Collector Cutoff Current	Vcs - 30V	IE = 0	ĺ	l	2	μA
ŀ	at 100° C}	Vcs - 5V	IE = 0	)	Ì	10	μÅ
	at 150° C}	$V_{CB} = 5V$	1g = 0	}	•	50	μA
hib	Input Impedance	Vcs - 5V	Ig = -1mA	30	42	80	Ohm
h <sub>ob</sub>	Output Admittance	Vcs - 5V	ie = -lmA	0	0.4	1.2	μπho
h <sub>rb</sub>	Feedback Voltage Ratio	VcB - 5V	le = -imA	50	400	1000	X10-6
hrp	Current Transfer Ratio	VCB - 5V	IE ImA	-0.9735	-0.98	0.989	1
PG.	Power Gain*†	VCE - 20V	IE = -2mA		42		db
NF .	Noise Figure*‡	VCE = 5V	IE = -1mA		20		db
fab	Frequency Cutoff	Vcs - 5V	IE1mA	}	6		mc
Cab	Output Capacitance (1mc)	Vcs = 5V	$I_E = -1mA$	1	7		ابيبر
Res	Saturation Resistance*	$l_B = 2.2mA$	ic - 5mA	l	100	200	Ohm

\*Common Emitte

Maximum Range

 $tR_{\rm E}=1k$ ;  $R_{\rm L}=200$ 

2 Conventional Noise—Compared to 1000 above resistor, 1000 apa and 1 cycle band width



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