# SILICON POWER TRANSISTOR 2SC4550

## NPN SILICON EPITAXIAL TRANSISTOR FOR HIGH-SPEED SWITCHING

The 2SC4550 is a power transistor developed for high-speed switching and features low  $V_{CE(sat)}$  and high here. This transistor is ideal for use in drivers such as DC/DC converters and actuators.

In addition, a small resin-molded insulation type package contributes to high-density mounting and reduction of mounting cost.

#### FEATURES

NEC

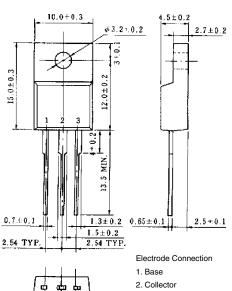
- High hFE and low VCE(sat): hFE  $\geq$  100 (VCE = 2 V, IC = 1.5 A) VCE(sat)  $\leq$  0.3 V (IC = 4 A, IB = 0.2 A)
- Mold package that does not require an insulating board or insulation bushing

Parameter	Symbol	Ratings	Unit
Falailletei	Symbol	natings	Unit
Collector to base voltage	Vсво	100	V
Collector to emitter voltage	VCEO	60	V
Emitter to base voltage	Vebo	7.0	V
Collector current (DC)	IC(DC)	7.0	А
Collector current (pulse)	C(pulse)*	14	А
Base current (DC)	B(DC)	3.5	А
Total power dissipation	P⊤ (Tc = 25°C)	30	W
Total power dissipation	P⊤ (Ta = 25°C)	2.0	W
Junction temperature	Tj	150	°C
Storage temperature	Tstg	–55 to +150	°C

#### ABSOLUTE MAXIMUM RATINGS (Ta = 25°C)

#### \* PW $\leq$ 300 $\mu$ s, duty cycle $\leq$ 10%

### PACKAGE DRAWING (UNIT: mm)



3. Emitter

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#### ELECTRICAL CHARACTERISTICS (Ta = 25°C)

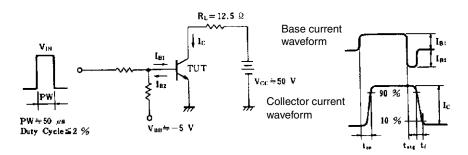
Parameter	Symbol	Conditions MIN. TYP		TYP.	MAX.	Unit
Collector to emitter voltage	VCEO(SUS)	Ic = 4.0 A, I <sub>B</sub> = 0.4 A, L = 1 mH				V
Collector to emitter voltage	VCEX(SUS)	Ic = 4.0 A, I <sub>B1</sub> = $-I_{B2}$ = 0.4 A, V <sub>BE(OFF)</sub> = $-1.5$ V, L = 180 $\mu$ H, clamped	60			V
Collector cutoff current	Ісво	$V_{CB} = 60 V, I_E = 0$			10	μA
Collector cutoff current	ICER	$V_{CE} = 60 \text{ V}, \text{ R}_{BE} = 50 \Omega, \text{ Ta} = 125^{\circ}\text{C}$			1.0	mA
Collector cutoff current	ICEX1	Vce = 60 V, Vbe(off) = -1.5 V			10	μA
Collector cutoff current	ICEX2	$V_{CE} = 60 \text{ V}, \text{ V}_{BE(OFF)} = -1.5 \text{ V},$ Ta = 125°C			1.0	mA
Emitter cutoff current	Іево	V <sub>EB</sub> = 5.0 V, Ic = 0			10	μA
DC current gain	hfe1*	Vce = 2.0 V, Ic = 0.7 A	100			
DC current gain	hfe2*	Vce = 2.0 V, Ic = 1.5 A	100	200	400	
DC current gain	hfe3*	Vce = 2.0 V, Ic = 4.0 A	60			
Collector saturation voltage	V <sub>CE(sat)1</sub> *	Ic = 4.0 A, I <sub>B</sub> = 0.2 A			0.3	V
Collector saturation voltage	VCE(sat)2*	Ic = 6.0 A, I <sub>B</sub> = 0.3 A			0.5	V
Base saturation voltage	V <sub>BE(sat)1</sub> *	Ic = 4.0 A, I <sub>B</sub> = 0.2 A			1.2	V
Base saturation voltage	VBE(sat)2*	Ic = 6.0 A, I <sub>B</sub> = 0.3 A			1.5	V
Collector capacitance	Cob	$V_{CB} = 10 V, I_E = 0, f = 1.0 MHz$		100		pF
Gain bandwidth product	f⊤	Vce = 10 V, Ic = 1.0 A		150		MHz
Turn-on time	ton	$I_{C} = 4.0 \text{ A}, \text{ R}_{L} = 12.5 \Omega,$		0.1	0.3	μs
Storage time	tstg	$I_{B1} = -I_{B2} = 0.2 \text{ A}, \text{ Vcc} \cong 50 \text{ V}$ Refer to the test circuit.		1.0	1.5	μs
Fall time	tr			0.1	0.3	μs

\* Pulse test PW  $\leq$  350  $\mu$ s, duty cycle  $\leq$  2%

#### **hfe CLASSIFICATION**

Marking	М	L	к
hFE2	100 to 200	150 to 300	200 to 400

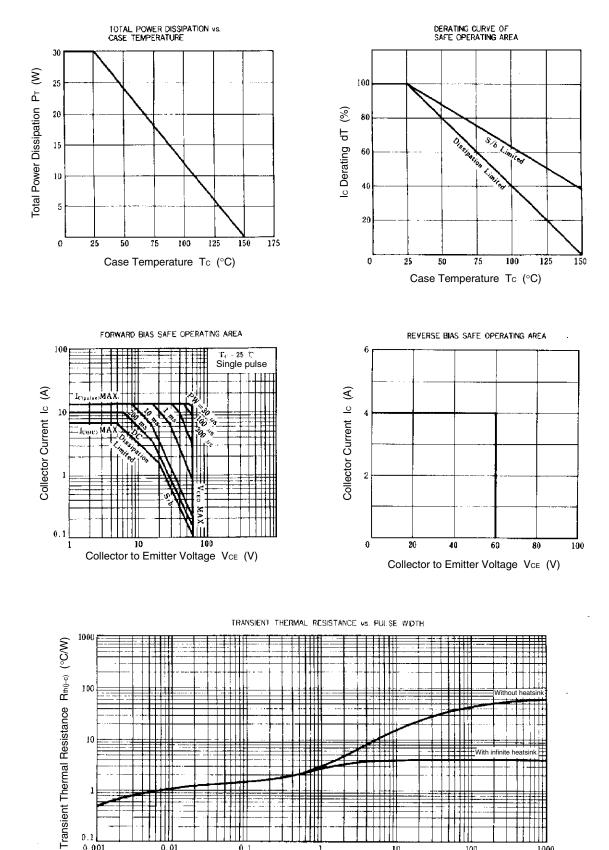
SWITCHING TIME (ton, tstg, tf) TEST CIRCUIT



TYPICAL CHARACTERISTICS (Ta = 25°C)

0.001

0.01



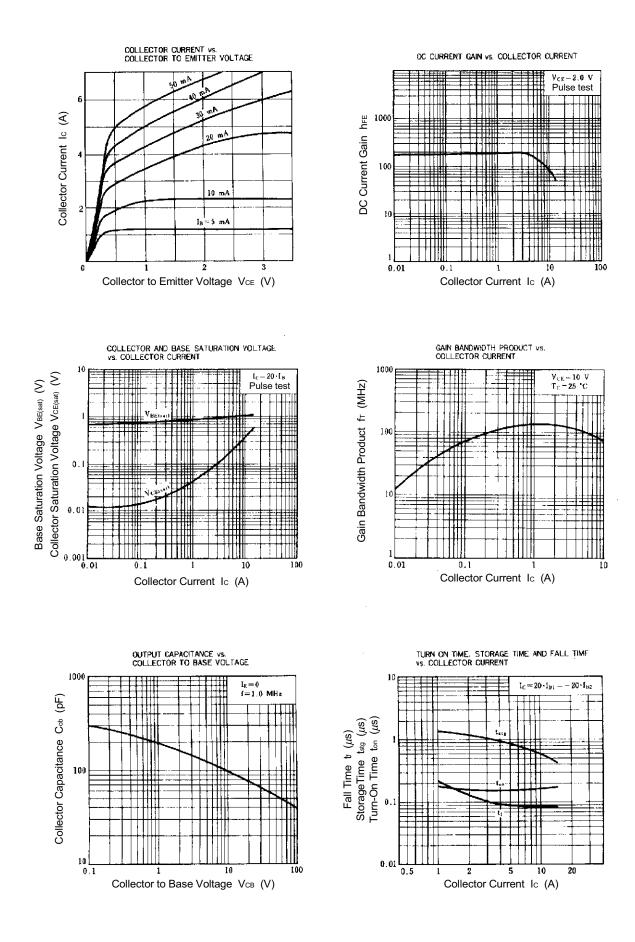
0.1 Pulse Width PW (s)

10

100

1000

Data Sheet D15596EJ2V0DS



[MEMO]

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