

SANYO Semiconductors

DATA SHEET

An ON Semiconductor Company



NPN Epitaxial Planar Silicon Transistor High-Current Switching Applications

Applications

· DC / DC converter, relay drivers, lamp drivers, motor drivers, inverter

Features

- · Adoption of FBET, MBIT process
- · Large current capacity
- High-speed switching
- Low collector-to-emitter saturation voltageHigh allowable power dissipation

Specifications

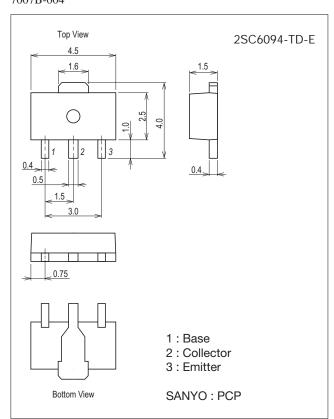
Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Collector-to-Base Voltage	VCBO		100	V
Collector-to-Emitter Voltage	VCES		100	V
	VCEO		60	V
Emitter-to-Base Voltage	VEBO		6.5	V
Collector Current	IC		3	A
Collector Current (Pulse)	ICP		5	A

Continued on next page.

Package Dimensions

unit : mm (typ) 7007B-004

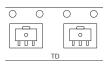


Product & Package Information

- Package : PCP
- JEITA, JEDEC : SC-62, SOT-89, TO-243
- Minimum Packing Quantity : 1,000 pcs./reel

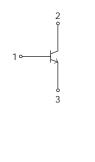
Packing Type: TD

Marking





Electrical Connection



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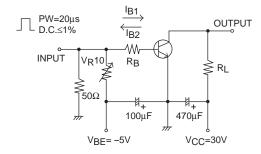
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	1			
Parameter	Symbol	Conditions	Ratings	Unit
Base Current	IB		600	mA
Collector Dissipation	PC	When mounted on ceramic substrate (250mm ² ×0.8mm)	1.3	W
		Tc=25°C	3.5	W
Junction Temperature	Тј		150	°C
Storage Temperature	Tstg		-55 to +150	°C

Electrical Characteristics at Ta=25°C

Parameter	Symbol	Conditions		Unit			
Parameter	Symbol	Conditions	min	typ	max	UTIIL	
Collector Cutoff Current	ICBO	V _{CB} =50V, I _E =0A			1	μΑ	
Emitter Cutoff Current	IEBO	V _{EB} =4V, I _C =0A			1	μΑ	
DC Current Gain	hFE	V _{CE} =2V, I _C =100mA	300		600		
Gain-Bandwidth Product	fŢ	VCE=10V, IC=500mA		390		MHz	
Output Capacitance	Cob	V _{CB} =10V, f=1MHz		18		рF	
Collector to Emitter Saturation Voltage	V _{CE} (sat)1	IC=1A, IB=50mA		90	135	mV	
Collector-to-Emitter Saturation Voltage	V _{CE} (sat)2	IC=1A, IB=100mA		80	120	mV	
Base-to-Emitter Saturation Voltage	VBE(sat)	IC=1A, IB=100mA		0.84	1.2	V	
Collector-to-Base Breakdown Voltage	V(BR)CBO	I _C =10μΑ, I _E =0Α	100			V	
Collector-to-Emitter Breakdown Voltage	V(BR)CES	I _C =100μA, R _{BE} =0Ω	100			V	
	V(BR)CEO	IC=1mA, RBE=∞	60			V	
Emitter-to-Base Breakdown Voltage	V(BR)EBO	IE=10μA, IC=0A	6.5			V	
Turn-ON Time	ton			35		ns	
Storage Time	t _{stg}	See specified Test Circuit.		680		ns	
Fall Time	tf			24		ns	

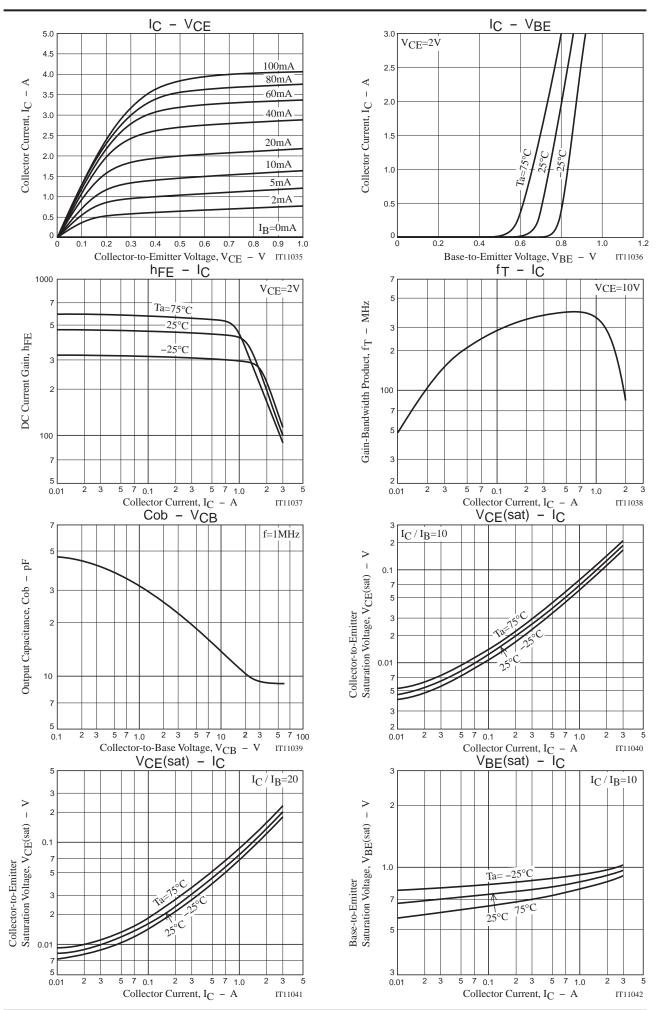
Switching Time Test Circuit

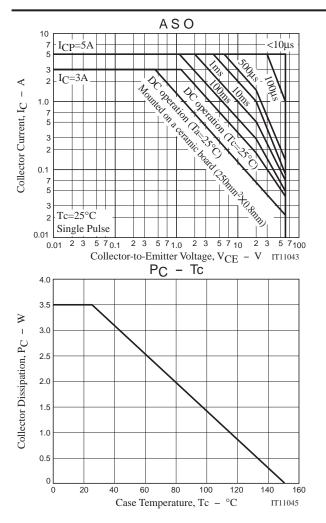


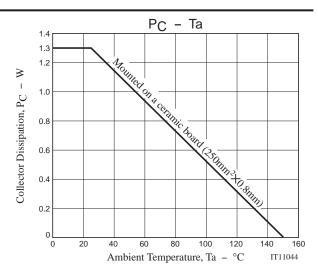
 $IC=10I_{B1}=-10I_{B2}=0.5A$

Ordering Information

Device Package		Shipping	memo		
2SC6094-TD-E	5094-TD-E PCP		Pb Free		







Embossed Taping Specification 2SC6094-TD-E

1. Packing Format

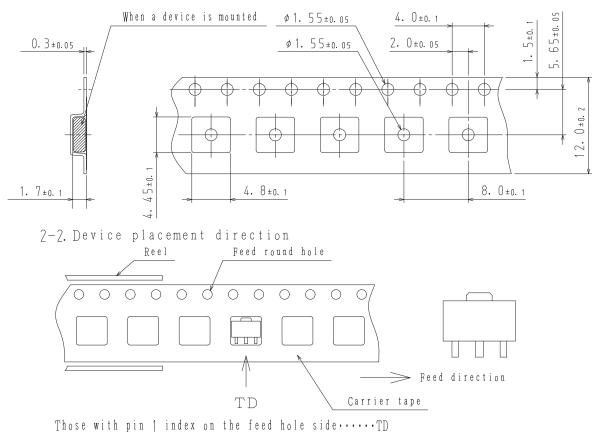
Package Name	Carrier Tape	Maximum Number of devices contained (pcs)		Packing format				
	Туре	Reel	Inner box	Outer box	Inner BOX (C-1)		(C-1)	Outer BOX (A-7)
РСР	PCP	1,000	4,000	24,000	4 reels contained		d	6 inner boxes contained
					Dimensions:mm (external)		external)	Dimensions:mm (external)
					18	$3 \times 72 \times$	185	440×195×210
			Reel	label,]	nner	box label		box label
Packing met	hod			(u 1	nit:r	nm)	The for	label at the time of factory shipments. n of a label may change in physical ution process.
°	\geq		<	6	59	>	<	108
	Type LOT Quan Orig Reel la	No. tity in	-> (1' -> (2' (Z) (Z) (Z) (Z) (Z) (Z) (Z) (Z) (Z) (Z)	11 111111 1111111 17 LOT OO 11 111111 1111111 10 OTY OO, OO 11 11111111111111111111111111111111	IIIIIIIIIIIIIIIIIIIIIIIIIIIIIIIIIIIIII		C SP	YPE CODE ************************************
				Label			Phase	
				LEAD FRE	EE 3	JEITA P	hase 3A	

LEAD FREE 4

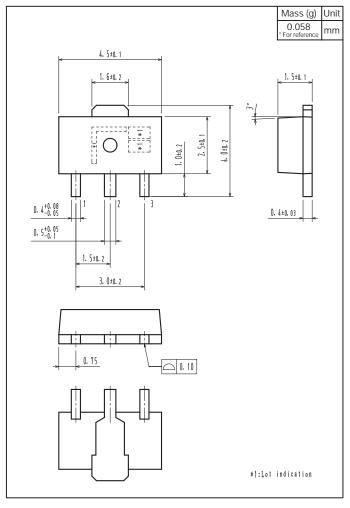
JEITA Phase 3

2. Taping configuration

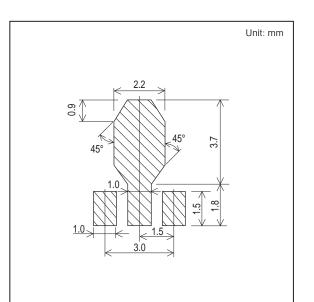
2-1. Carrier tape size (unit:mm)



Outline Drawing 2SC6094-TD-E



Land Pattern Example



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