## Silicon N-Channel MOS FET

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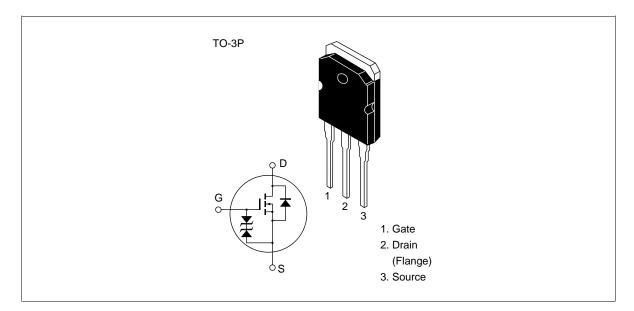
### Application

High speed power switching

#### Features

- Low on-resistance
- High speed switching
- Low drive current
- No secondary breakdown
- Suitable for switching regulator and DC-DC converter

### Outline





## Absolute Maximum Ratings (Ta = $25^{\circ}$ C)

Item		Symbol	Ratings	Unit
Drain to source voltage	2SK1163	V <sub>DSS</sub>	450	V
	2SK1164		500	
Gate to source voltage		V <sub>GSS</sub>	±30	V
Drain current		I <sub>D</sub>	11	А
Drain peak current		L *1 D(pulse)	40	A
Body to drain diode reverse drain current		I <sub>DR</sub>	11	A
Channel dissipation		Pch*2	100	W
Channel temperature		Tch	150	°C
Storage temperature		Tstg	-55 to +150	°C

Notes: 1. PW 10 µs, duty cycle 1%

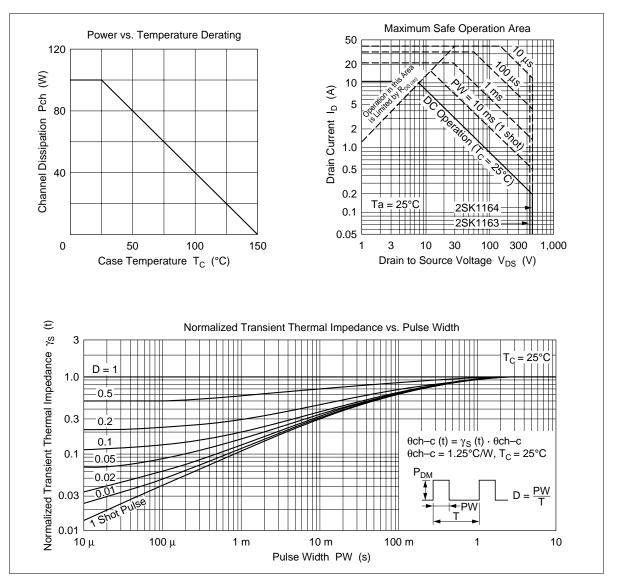
2. Value at  $T_c = 25^{\circ}C$ 

### **Electrical Characteristics** (Ta = 25°C)

ltem		Symbol	Min	Тур	Мах	Unit	Test conditions
Drain to source	2SK1163	$V_{(\text{BR})\text{DSS}}$	450	_	_	V	$I_{\rm D} = 10 \text{ mA}, V_{\rm GS} = 0$
breakdown voltage	2SK1164		500				
Gate to source breakdown voltage		$V_{(BR)GSS}$	±30		_	V	$I_{G} = \pm 100 \ \mu A, \ V_{DS} = 0$
Gate to source leak current		I <sub>GSS</sub>	_	—	±10	μA	$V_{GS} = \pm 25 \text{ V}, V_{DS} = 0$
Zero gate voltage	2SK1163	I <sub>DSS</sub>	_		250	μA	$V_{\rm DS} = 360 \text{ V}, \text{ V}_{\rm GS} = 0$
drain current	2SK1164						$V_{\rm DS} = 400 \text{ V}, \text{ V}_{\rm GS} = 0$
Gate to source cutoff	voltage	$V_{GS(off)}$	2.0	—	3.0	V	$I_{\rm D} = 1 \text{ mA}, V_{\rm DS} = 10 \text{ V}$
Static Drain to source	2SK1163		_	0.55	0.7		$I_{\rm D} = 5 \text{ A}, V_{\rm GS} = 10 \text{ V}^{*1}$
on state resistance	2SK1164		—	0.60	0.8	_	
Forward transfer admi	ittance	yfs	5.0	8.0	—	S	$I_{\rm D} = 5 \text{ A}, V_{\rm DS} = 10 \text{ V}^{*1}$
Input capacitance		Ciss	_	1150	—	pF	$V_{DS} = 10 \text{ V}, \text{ V}_{GS} = 0,$
Output capacitance		Coss	_	340	—	pF	f = 1 MHz
Reverse transfer capacitance		Crss	_	55	—	pF	_
Turn-on delay time		t <sub>d(on)</sub>	_	17	—	ns	$I_{\rm D} = 5 \text{ A}, V_{\rm GS} = 10 \text{ V},$
Rise time		t,	_	60	_	ns	$R_{L} = 6$
Turn-off delay time		t <sub>d(off)</sub>		95	—	ns	_
Fall time		t <sub>f</sub>	_	50	_	ns	_
Body to drain diode forward voltage		$V_{DF}$	_	1.0	—	V	$I_{F} = 11 \text{ A}, V_{GS} = 0$
Body to drain diode reverse recovery time		t <sub>rr</sub>	—	400	—	ns	I <sub>F</sub> = 11 A, V <sub>GS</sub> = 0, di <sub>F</sub> /dt = 100 A/μs

Note: 1. Pulse test

See characteristic curves of 2SK1159, 2SK1160.



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