

---

# 2SK1334

Silicon N-Channel MOS FET

# HITACHI

---

## Application

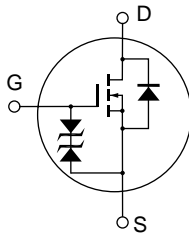
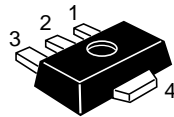
High speed power switching

## Features

- Low on-resistance
- High speed switching
- Low drive current
- No secondary Breakdown
- Suitable for switching regulator and DC-DC converter

## Outline

UPAK



1. Gate
2. Drain
3. Source
4. Drain

**Absolute Maximum Ratings** ( $T_a = 25^\circ\text{C}$ )

<b>Item</b>	<b>Symbol</b>	<b>Ratings</b>	<b>Unit</b>
Drain to source voltage	$V_{\text{DSS}}$	200	V
Gate to source voltage	$V_{\text{GSS}}$	$\pm 20$	V
Drain current	$I_{\text{D}}$	1	A
Drain peak current	$I_{\text{D(pulse)}}^{*1}$	2	A
Body to drain diode reverse drain current	$I_{\text{DR}}$	1	A
Channel dissipation	$P_{\text{ch}}^{*2}$	1	W
Channel temperature	$T_{\text{ch}}$	150	$^\circ\text{C}$
Storage temperature	$T_{\text{stg}}$	-55 to +150	$^\circ\text{C}$

Notes: 1.  $PW = 10 \mu\text{s}$ , duty cycle = 1%

2. When using the alumina ceramic board ( $12.5 \times 20 \times 0.7 \text{ mm}$ )

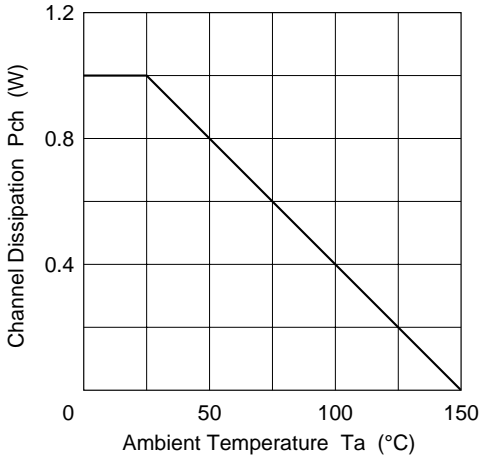
## Electrical Characteristics (Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	200	—	—	V	$I_D = 10 \text{ mA}, V_{GS} = 0$
Gate to source breakdown voltage	$V_{(BR)GSS}$	$\pm 20$	—	—	V	$I_G = \pm 100 \text{ }\mu\text{A}, V_{DS} = 0$
Gate to source leak current	$I_{GSS}$	—	—	$\pm 10$	$\mu\text{A}$	$V_{GS} = \pm 16 \text{ V}, V_{DS} = 0$
Zero gate voltage drain current	$I_{DSS}$	—	—	50	$\mu\text{A}$	$V_{DS} = 160 \text{ V}, V_{GS} = 0$
Gate to source cutoff voltage	$V_{GS(off)}$	2.0	—	4.0	V	$I_D = 1 \text{ mA}, V_{DS} = 10 \text{ V}$
Static drain to source on state resistance	$R_{DS(on)}$	—	2.5	3.8		$I_D = 0.5 \text{ A}, V_{GS} = 10 \text{ V}^{*1}$
		—	4.5	7.0		$I_D = 2 \text{ A}, V_{GS} = 10 \text{ V}^{*1}$
Forward transfer admittance	$ y_{fs} $	0.4	0.6	—	S	$I_D = 0.5 \text{ A}, V_{DS} = 10 \text{ V}^{*1}$
Input capacitance	$C_{iss}$	—	80	—	pF	$V_{DS} = 10 \text{ V}, V_{GS} = 0,$
Output capacitance	$C_{oss}$	—	40	—	pF	$f = 1 \text{ MHz}$
Reverse transfer capacitance	$C_{rss}$	—	7	—	pF	
Turn-on delay time	$t_{d(on)}$	—	5	—	ns	$I_D = 0.5 \text{ A}, V_{GS} = 10 \text{ V},$
Rise time	$t_r$	—	8	—	ns	$R_L = 60$
Turn-off delay time	$t_{d(off)}$	—	10	—	ns	
Fall time	$t_f$	—	7	—	ns	
Body to drain diode forward voltage	$V_{DF}$	—	1.0	—	V	$I_F = 1 \text{ A}, V_{GS} = 0$
Body to drain diode reverse recovery time	$t_{rr}$	—	75	—	ns	$I_F = 1 \text{ A}, V_{GS} = 0,$ $di_F/dt = 50 \text{ A}/\mu\text{s}$

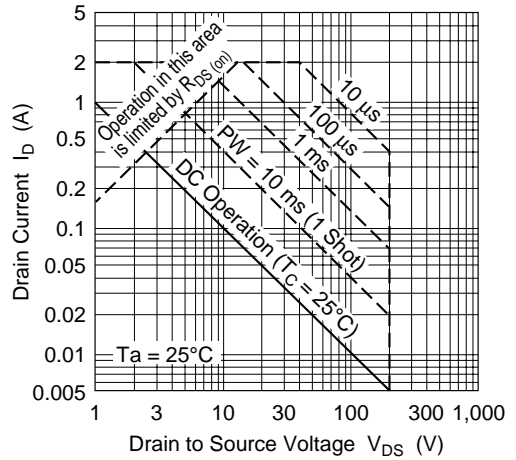
Notes: 1. Pulse test

2. Marking for 2SK1334 is "BY".

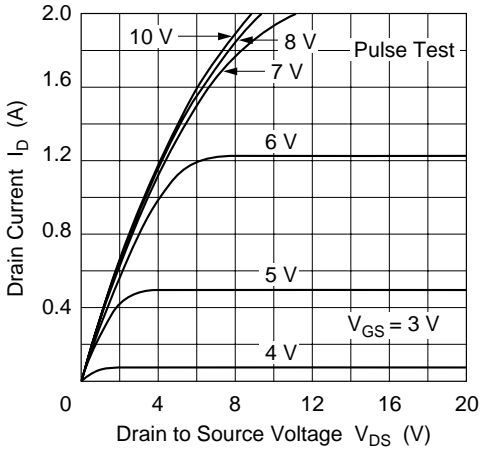
Power vs. Temperature Derating



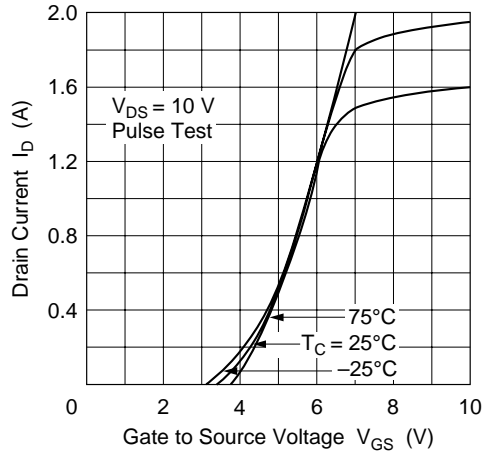
Maximum Safe Operation Area

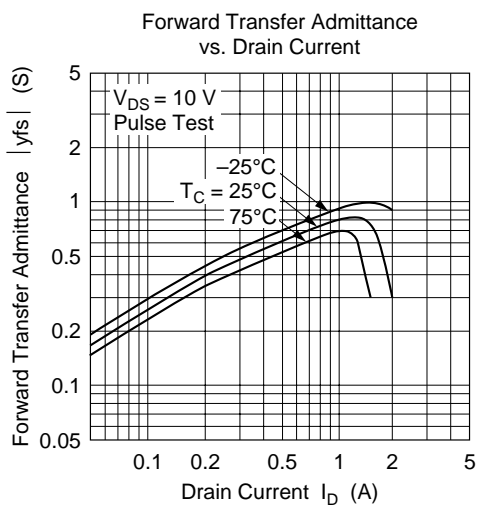
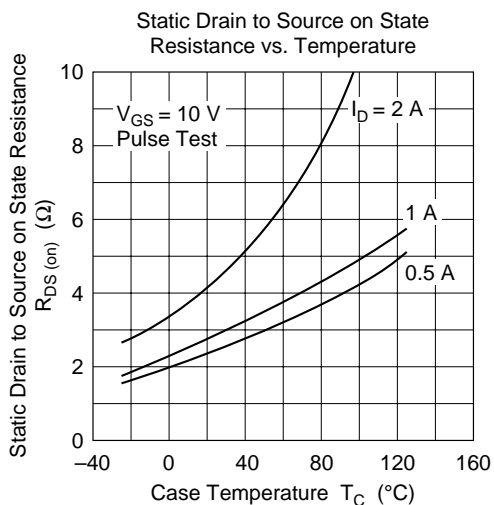
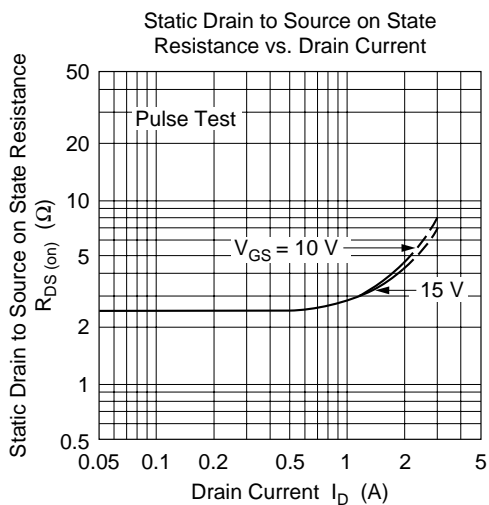
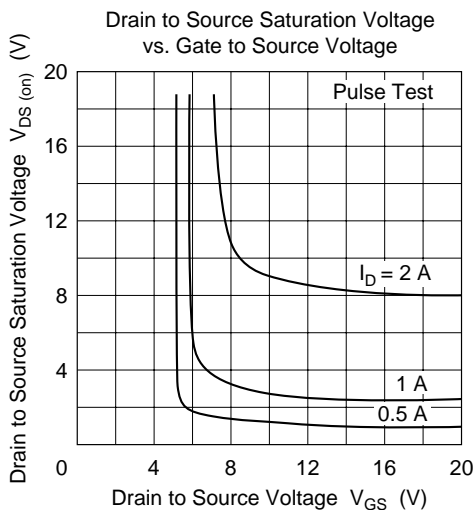


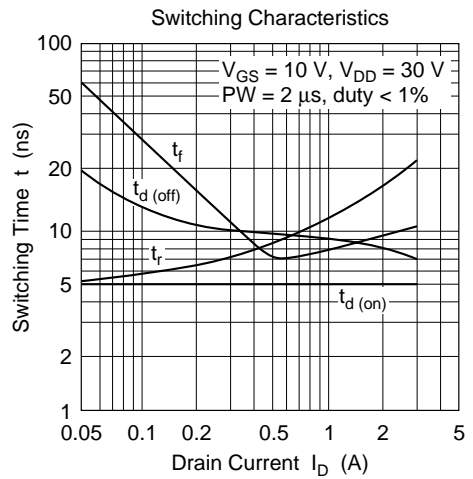
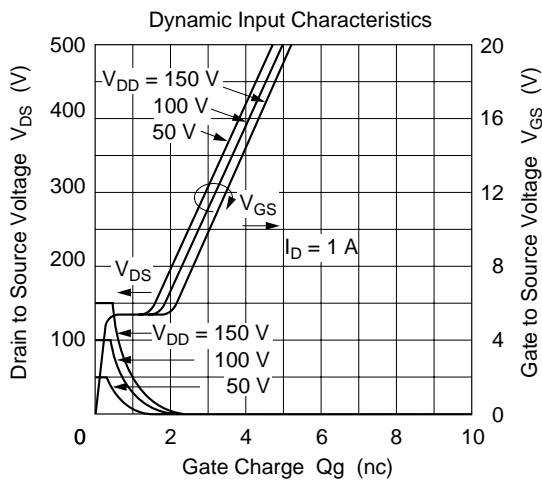
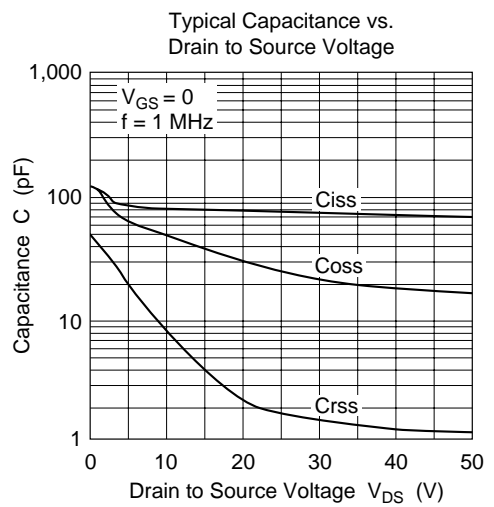
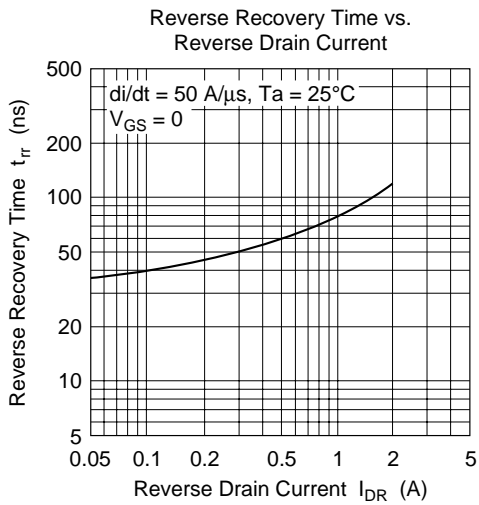
Typical Output Characteristics

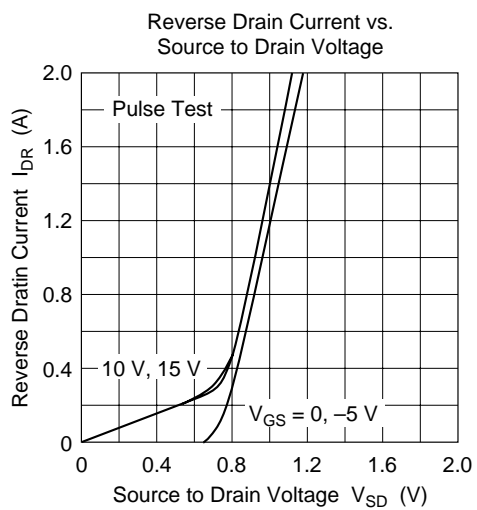


Typical Transfer Characteristics









When using this document, keep the following in mind:

1. This document may, wholly or partially, be subject to change without notice.
2. All rights are reserved: No one is permitted to reproduce or duplicate, in any form, the whole or part of this document without Hitachi's permission.
3. Hitachi will not be held responsible for any damage to the user that may result from accidents or any other reasons during operation of the user's unit according to this document.
4. Circuitry and other examples described herein are meant merely to indicate the characteristics and performance of Hitachi's semiconductor products. Hitachi assumes no responsibility for any intellectual property claims or other problems that may result from applications based on the examples described herein.
5. No license is granted by implication or otherwise under any patents or other rights of any third party or Hitachi, Ltd.
6. **MEDICAL APPLICATIONS:** Hitachi's products are not authorized for use in **MEDICAL APPLICATIONS** without the written consent of the appropriate officer of Hitachi's sales company. Such use includes, but is not limited to, use in life support systems. Buyers of Hitachi's products are requested to notify the relevant Hitachi sales offices when planning to use the products in **MEDICAL APPLICATIONS**.

---

---

# HITACHI

## **Hitachi, Ltd.**

Semiconductor & IC Div.  
Nippon Bldg., 2-6-2, Ohte-machi, Chiyoda-ku, Tokyo 100, Japan  
Tel: Tokyo (03) 3270-2111  
Fax: (03) 3270-5109

### **For further information write to:**

Hitachi America, Ltd.  
Semiconductor & IC Div.  
2000 Sierra Point Parkway  
Brisbane, CA. 94005-1835  
U S A  
Tel: 415-589-8300  
Fax: 415-583-4207

Hitachi Europe GmbH  
Electronic Components Group  
Continental Europe  
Dornacher Straße 3  
D-85622 Feldkirchen  
München  
Tel: 089-9 91 80-0  
Fax: 089-9 29 30 00

Hitachi Europe Ltd.  
Electronic Components Div.  
Northern Europe Headquarters  
Whitebrook Park  
Lower Cookham Road  
Maidenhead  
Berkshire SL6 8YA  
United Kingdom  
Tel: 0628-585000  
Fax: 0628-778322

Hitachi Asia Pte. Ltd.  
16 Collyer Quay #20-00  
Hitachi Tower  
Singapore 0104  
Tel: 535-2100  
Fax: 535-1533

Hitachi Asia (Hong Kong) Ltd.  
Unit 706, North Tower,  
World Finance Centre,  
Harbour City, Canton Road  
Tsim Sha Tsui, Kowloon  
Hong Kong  
Tel: 27359218  
Fax: 27306071

**HITACHI**