2SK1762

Silicon N-Channel MOS FET

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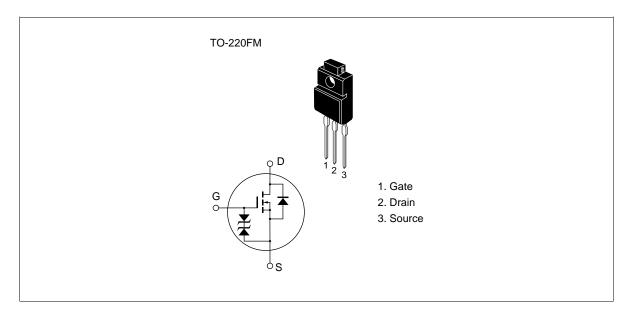
Application

High speed power switching

Features

- Low on-resistance
- High speed switching
- Low drive current
- No secondary breakdown
- Suitable for switchingregulator, DC-DC converter

Outline





2SK1762

Absolute Maximum Ratings (Ta = 25° C)

| Item | Symbol | Ratings | Unit |
|---|--------------------------|-------------|------|
| Drain to source voltage | V _{DSS} | 250 | V |
| Gate to source voltage | V _{GSS} | ±30 | V |
| Drain current | I _D | 12 | А |
| Drain peak current | L _{D(pulse)} *1 | 48 | А |
| Body to drain diode reverse drain current | I _{DR} | 12 | А |
| Channel dissipation | Pch*2 | 35 | W |
| Channel temperature | Tch | 150 | °C |
| Storage temperature | Tstg | -55 to +150 | °C |

Notes 1. PW 10 µs, duty cycle 1 %

2. Value at Tc = 25 °C

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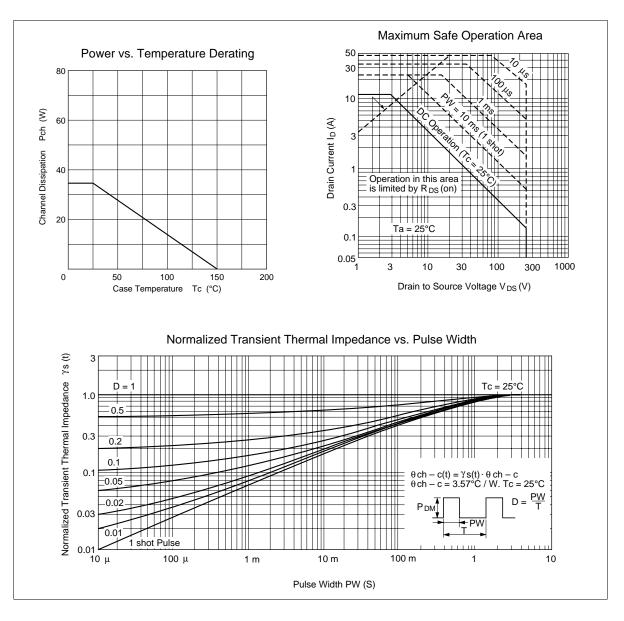
Electrical Characteristics (Ta = 25° C)

| Item | Symbol | Min | Тур | Max | Unit | Test Conditions |
|--|-----------------------------|-----|------|------|------|---|
| Drain to source breakdown voltage | $V_{(\text{BR})\text{DSS}}$ | 250 | — | _ | V | $I_{\rm D} = 10 \text{ mA}, V_{\rm GS} = 0$ |
| Gate to source breakdown voltage | $V_{(BR)GSS}$ | ±30 | — | — | V | $I_{G} = \pm 100 \ \mu A, V_{DS} = 0$ |
| Gate to source leak current | I _{GSS} | _ | _ | ±10 | μA | $V_{GS} = \pm 25 \text{ V}, V_{DS} = 0$ |
| Zero gate voltage drain current | I _{DSS} | _ | _ | 250 | μA | $V_{\rm DS} = 200 \text{ V}, \text{ V}_{\rm GS} = 0$ |
| Gate to source cutoff voltage | $V_{GS(off)}$ | 2.0 | | 3.0 | V | $I_{\rm D} = 1 \text{ mA}, V_{\rm DS} = 10 \text{ V}$ |
| Static drain to source on state resistance | $R_{\text{DS(on)}}$ | _ | 0.23 | 0.35 | | $I_{\rm D} = 6 \text{ A}$ $V_{\rm GS} = 10 \text{ V}^{*1}$ |
| Forward transfer admittance | y _{fs} | 5.0 | 8.0 | — | S | $I_{\rm D} = 6 \text{ A}$ $V_{\rm DS} = 10 \text{ V}^{*1}$ |
| Input capacitance | Ciss | _ | 1100 | _ | pF | V _{DS} = 10 V |
| Output capacitance | Coss | _ | 440 | _ | pF | $V_{GS} = 0$ |
| Reverse transfer capacitance | Crss | _ | 68 | _ | pF | f = 1 MHz |
| Turn-on delay time | t _{d(on)} | _ | 20 | _ | ns | I _D = 6 A |
| Rise time | t, | _ | 65 | _ | ns | V _{GS} = 10 V |
| Turn-off delay time | t _{d(off)} | _ | 100 | _ | ns | R _L = 5 |
| Fall time | t _f | _ | 44 | _ | ns | |
| Body to drain diode forward voltage | V_{DF} | — | 1.0 | — | V | $I_F = 12 \text{ A}, V_{GS} = 0$ |
| Body to drain diode reverse recovery time | t _{rr} | — | 200 | — | ns | $I_F = 12 \text{ A}, V_{GS} = 0,$ $di_F / dt = 100 \text{ A} / \mu \text{s}$ |

Note 1. Pulse Test

See characteristic curves of 2SK1761.

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Hitachi, Ltd.

Semiconductor & IC Div. Nippon Bldg., 2-6-2, Ohte-machi, Chiyoda-ku, Tokyo 100, Japan Tel: Tokyo (03) 3270-2111 Fax: (03) 3270-5109

For further information write to:

Hitachi America, Ltd. Semiconductor & IC Div. 2000 Sierra Point Parkway Brisbane, CA. 94005-1835 U S A Tel: 415-589-8300 Fax: 415-583-4207 Hitachi Europe GmbH Electronic Components Group Continental Europe Dornacher Straße 3 D-85622 Feldkirchen München Tel: 089-9 91 80-0 Fax: 089-9 29 30 00 Hitachi Europe Ltd. Electronic Components Div. Northern Europe Headquarters Whitebrook Park Lower Cookham Road Maidenhead Berkshire SL6 8YA United Kingdom Tel: 0628-585000 Fax: 0628-778322 Hitachi Asia Pte. Ltd. 16 Collyer Quay #20-00 Hitachi Tower Singapore 0104 Tel: 535-2100 Fax: 535-1533

Hitachi Asia (Hong Kong) Ltd. Unit 706, North Tower, World Finance Centre, Harbour City, Canton Road Tsim Sha Tsui, Kowloon Hong Kong Tel: 27359218 Fax: 27306071

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