Silicon N-Channel MOS FET

HITACHI

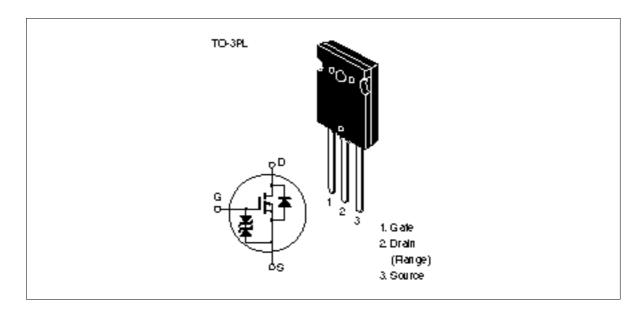
Application

High speed power switching

Features

- Low on-resistance
- High speed switching
- Low drive current
- No Secondary Breakdown
- Suitable for Switching regulator, DC DC converter, Motor Control

Outline





Absolute Maximum Ratings $(Ta = 25^{\circ}C)$

Item	Symbol	Ratings	Unit
Drain to source voltage	V _{DSS}	500	V
Gate to source voltage	V_{GSS}	±30	V
Drain current	I _D	35	А
Drain peak current	I _{D(pulse)} *1	140	А
Body to drain diode reverse drain current	I _{DR}	35	A
Channel dissipation	Pch*2	200	W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

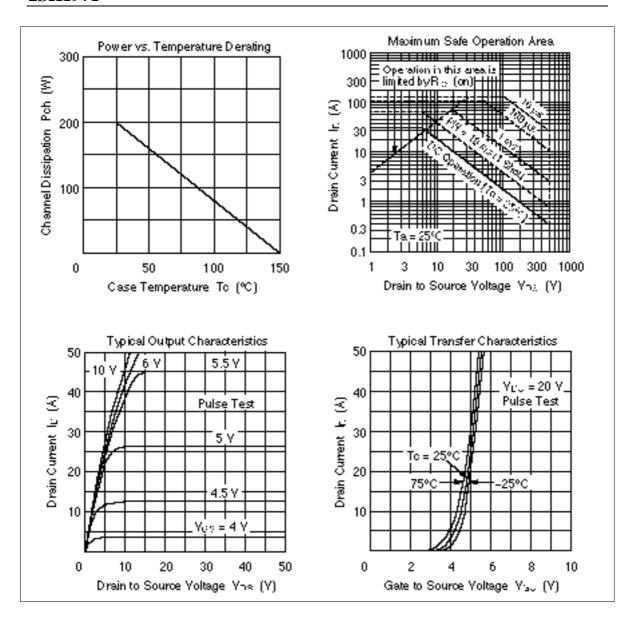
Notes 1. PW 10 µs, duty cycle 1 %

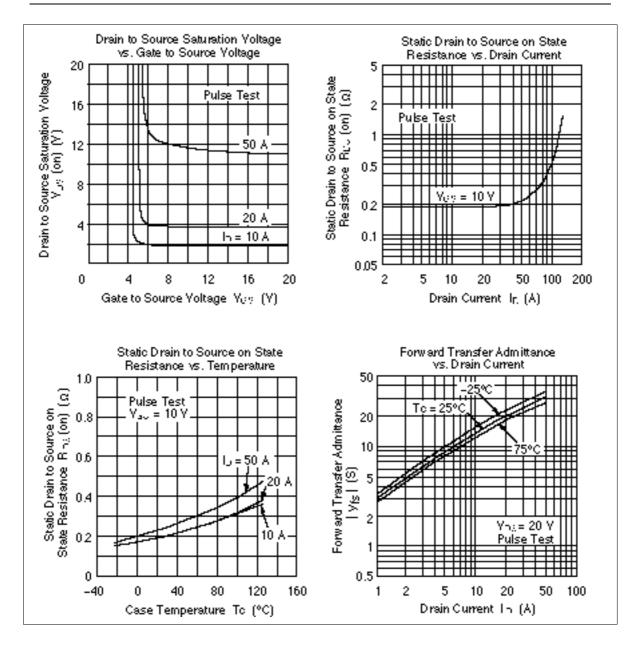
2. Value at Tc = 25°C

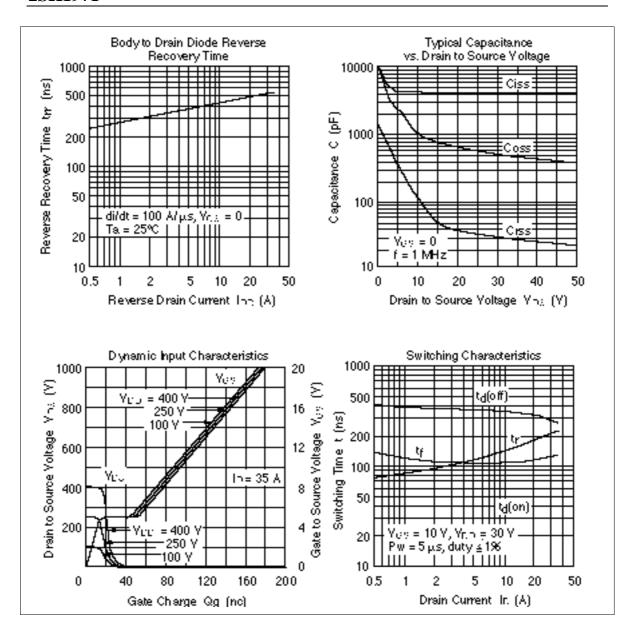
Electrical Characteristics ($Ta = 25^{\circ}C$)

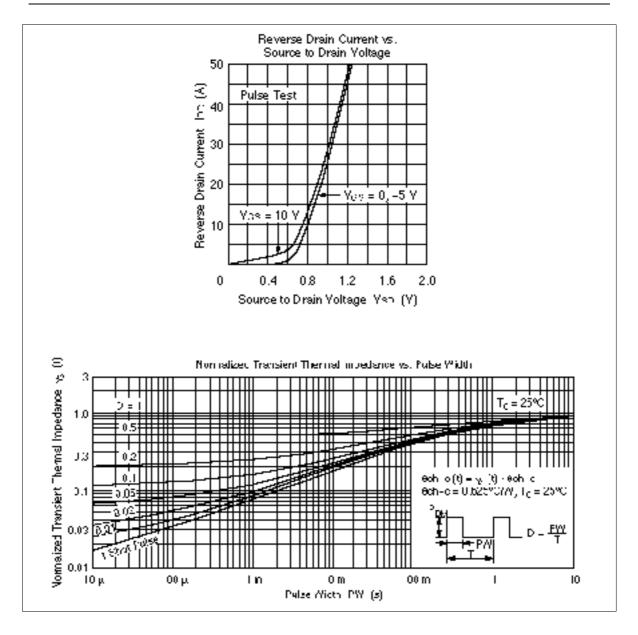
Item	Symbol	Min	Тур	Max	Unit	Test conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	500	_	_	V	$I_D = 10 \text{ mA}, V_{GS} = 0$
Gate to source breakdown voltage	$V_{(BR)GSS}$	±20	_	_	V	$I_G = \pm 100 \ \mu A, \ V_{DS} = 0$
Gate to source leak current	I _{GSS}		_	±10	μΑ	$V_{GS} = \pm 25 \text{ V}, V_{DS} = 0$
Zero gate voltage drain current	I _{DSS}	_		250	μΑ	$V_{DS} = 400 \text{ V}, V_{GS} = 0$
Gate to source cutoff voltage	$V_{GS(off)}$	2.0		3.0	V	$I_D = 1 \text{ mA}, V_{DS} = 10 \text{ V}$
Static drain to source on state resistance	R _{DS(on)}	_	0.19	0.23		$I_D = 18 \text{ A}$ $V_{GS} = 10 \text{ V}^{*1}$
Forward transfer admittance	y _{fs}	16	24	_	S	$I_D = 18 \text{ A}$ $V_{DS} = 10 \text{ V}^{*1}$
Input capacitance	Ciss	_	4320	_	pF	V _{DS} = 10 V
Output capacitance	Coss	_	1120	_	pF	$V_{GS} = 0$
Reverse transfer capacitance	Crss	_	130	_	pF	f = 1 MHz
Turn-on delay time	t _{d(on)}	_	50	_	ns	I _D = 18A
Rise time	t _r	_	170	_	ns	V _{GS} = 10 V
Turn-off delay time	t _{d(off)}	_	320	_	ns	$R_L = 1.67$
Fall time	t _f	_	130	_	ns	
Body to drain diode forward voltage	V_{DF}	_	1.1	_	V	I _F =35 A, V _{GS} = 0
Body to drain diode reverse recovery time	t _{rr}		530		ns	$I_F = 35 \text{ A}, V_{GS} = 0,$ $di_F / dt = 100 \text{ A} / \mu \text{s}$

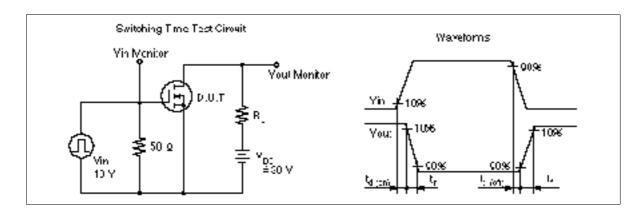
Note 1. Pulse Test











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