Silicon N-Channel MOS FET

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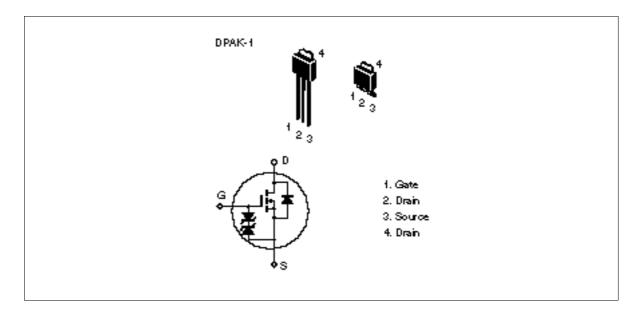
Application

High speed power switching

Features

- Low on-resistance
- High speed switching
- No Secondary Breakdown
- Suitable for Switching regulator, DC DC converter

Outline





Absolute Maximum Ratings $(Ta = 25^{\circ}C)$

Item	Symbol	Ratings	Unit
Drain to source voltage	V _{DSS}	600	V
Gate to source voltage	V _{GSS}	±30	V
Drain current	I _D	3	A
Drain peak current	I _{D(pulse)} *1	6	A
Body to drain diode reverse drain current	I_{DR}	3	A
Channel dissipation	Pch*2	20	W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

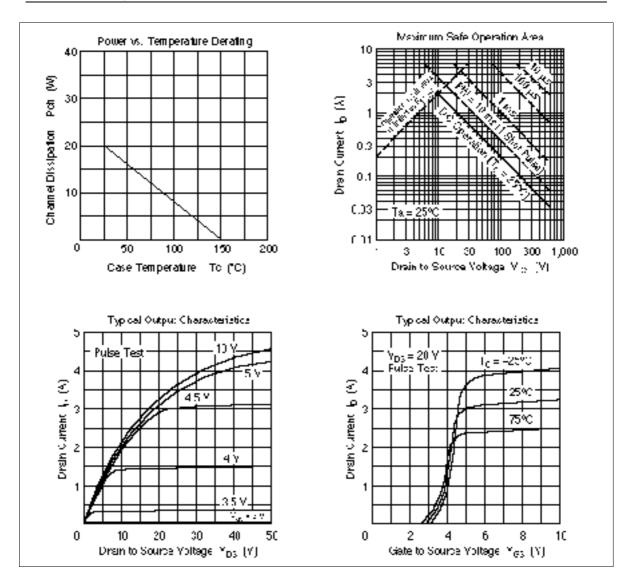
Notes 1. PW 10 µs, duty cycle 1 %

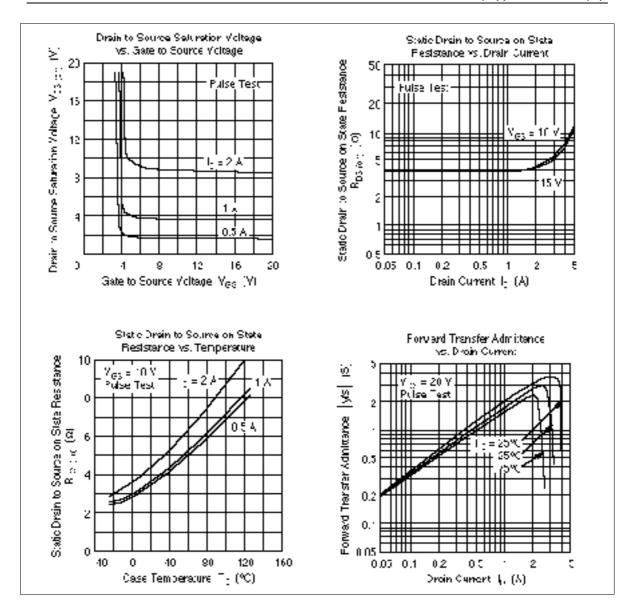
2. Value at Tc = 25°C

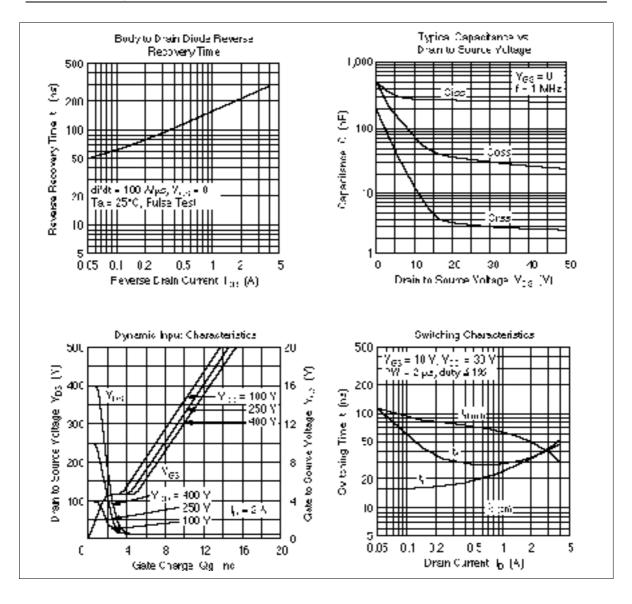
Electrical Characteristics ($Ta = 25^{\circ}C$)

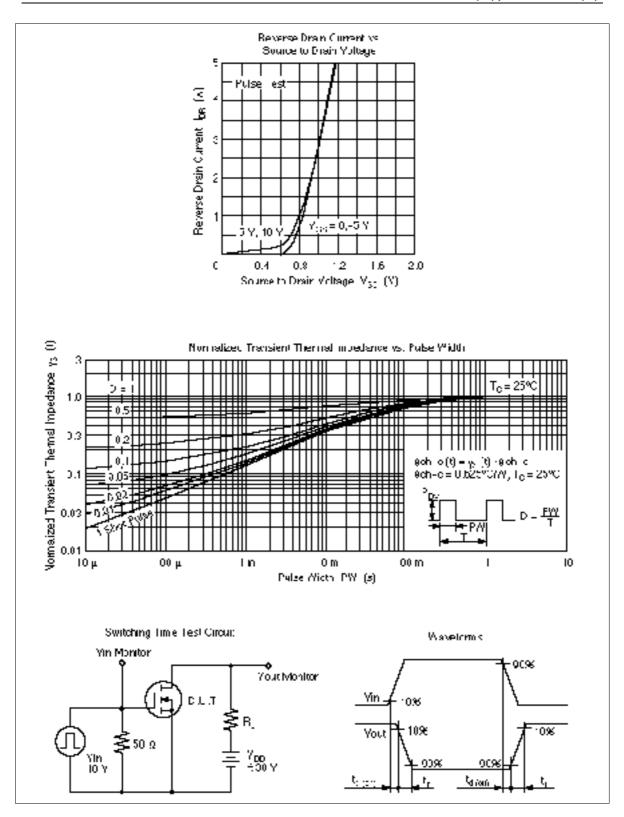
Item	Symbol	Min	Тур	Max	Unit	Test conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	600	_	_	V	$I_D = 10 \text{ mA}, V_{GS} = 0$
Gate to source breakdown voltage	$V_{(BR)GSS}$	±30	_	_	V	$I_G = \pm 100 \ \mu A, \ V_{DS} = 0$
Gate to source leak current	I _{GSS}	_	_	±10	μA	$V_{GS} = \pm 25 \text{ V}, V_{DS} = 0$
Zero gate voltage drain current	I _{DSS}	_	_	100	μΑ	V _{DS} =500 V, V _{GS} = 0
Gate to source cutoff voltage	V _{GS(off)}	2.0	_	3.0	V	I _D = 1 mA, V _{DS} = 10 V
Static drain to source on state resistance	R _{DS(on)}	_	3.8	5.0		$I_D = 1A$ $V_{GS} = 10 \text{ V}^{*1}$
Forward transfer admittance	y _{fs}	1.2	2.0	_	S	$I_D = 1A$ $V_{DS} = 10 \text{ V}^{*1}$
Input capacitance	Ciss	_	295		pF	V _{DS} = 10 V
Output capacitance	Coss	_	70		pF	$V_{GS} = 0$
Reverse transfer capacitance	Crss	_	12		pF	f = 1 MHz
Turn-on delay time	t _{d(on)}		8	_	ns	I _D = 1A
Rise time	t _r	_	25	_	ns	V_{GS} = 10 V
Turn-off delay time	t _{d(off)}	_	65	_	ns	$R_L = 30$
Fall time	t _f	_	30	_	ns	_
Body to drain diode forward voltage	V_{DF}	_	0.9	_	V	I _F =3 A, V _{GS} = 0
Body to drain diode reverse recovery time	t _{rr}	_	220	_	ns	$I_F = 3A$, $V_{GS} = 0$, $di_F / dt = 100 A / \mu s$

Note 1. Pulse Test









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