TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (L^2 - π -MOSV)

2SK2615

DC-DC Converter, Relay Drive and Motor Drive Applications

 $\begin{array}{ll} \bullet & Low\ drain-source\ ON\ resistance & : R_{DS}\ (ON) = 0.23\ \Omega\ (typ.) \\ \bullet & High\ forward\ transfer\ admittance & : |Y_{fs}| = 2.0\ S\ (typ.) \\ \bullet & Low\ leakage\ current & : I_{DSS} = 100\ \mu A\ (max)\ (V_{DS} = 60\ V) \\ \bullet & Enhancement-mode & : V_{th} = 0.8 \\ \sim 2.0\ V\ (V_{DS} = 10\ V,\ I_{D} = 1\ mA) \end{array}$

Maximum Ratings (Ta = 25°C)

Characteris	stics	Symbol	Rating	Unit	
Drain-source voltage		V_{DSS}	60	V	
Drain-gate voltage (R _{GS} = 20 kΩ)		V_{DGR}	60	V	
Gate-source voltage		V _{GSS}	±20	V	
Drain current	DC (Note 1)	I _D	2	Α	
	Pulse (Note 1)	I _{DP}	6	A	
Drain power dissipation	n	P _D	0.5	W	
Drain power dissipation (Note 2)		P _D	1.5	W	
Channel temperature		T _{ch}	150	°C	
Storage temperature range		T _{stg}	-55~150	°C	

Note 1: Please use devices on condition that the channel temperature is below 150°C.

Note 2: Mounted on ceramic substrate (25.4 mm × 25.4 mm × 0.8 mm)

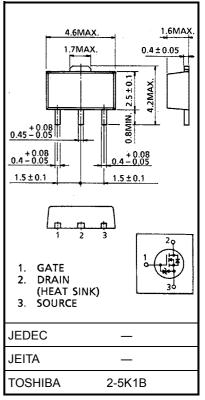
Thermal Characteristics

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to ambient	R _{th (ch-a)}	250	°C/W

This transistor is an electrostatic sensitive device.

Please handle with caution.

Unit: mm



Weight: 0.05 g (typ.)

Marking



(The two digits represent the part number.)



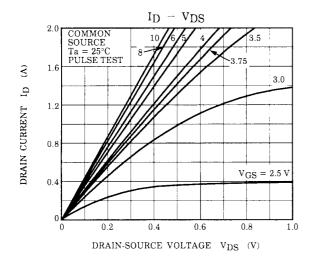
Electrical Characteristics (Ta = 25°C)

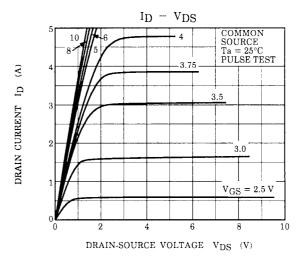
Charac	teristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	rrent	I _{GSS}	V _{GS} = ±16 V, V _{DS} = 0 V	_	_	±10	μΑ
Drain cut-off cu	rent	I _{DSS}	V _{DS} = 60 V, V _{GS} = 0 V	_	_	100	μΑ
Drain-source br	eakdown voltage	V (BR) DSS	I _D = 10 mA, V _{GS} = 0 V	60	_	_	V
Gate threshold v	roltage	V_{th}	V _{DS} = 10 V, I _D = 1 mA	8.0	_	2.0	V
Drain-source ON resistance		R _{DS (ON)}	VGS = 4 V, ID = 1 A	_	0.33	0.44	
			VGS = 10 V, ID = 1 A	_	0.23	0.30	Ω
Forward transfer	admittance	Y _{fs}	V _{DS} = 10 V, I _D = 1 A	1.0	2.0	_	S
Input capacitano	е	C _{iss}		_	150	_	
Reverse transfer capacitance		C _{rss}	V _{DS} = 10 V, V _{GS} = 0 V, f = 1 MHz	_	25	_	pF
Output capacita	put capacitance C _{oss}		_	70	_		
Switching time	Rise time	t _r	V_{GS} V_{OV} V_{OUt} V_{OUt} V_{OUt} V_{OUT} V_{OUT} V_{OUT} V_{OUT} V_{OUT}	_	25	_	ns ns
	Turn-on time	t _{on}		_	30	_	
	Fall time	t _f		1	50	1	
	Turn-off time	t _{off}	Duty $\leq 1\%$, $t_{\mathbf{w}} = 10 \mu \mathbf{s}$		150	-	
Total gate charge (gate-source plus gate-drain)				6.0			
Gate-source charge		Q _{gs}	$V_{DD} \approx 48 \text{ V}, V_{GS} = 10 \text{ V}, I_{D} = 2 \text{ A}$	_	4.6		nC
Gate-drain ("miller") Charge		Q _{gd}]		1.4	_	

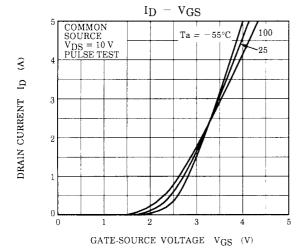
Source-Drain Ratings and Characteristics (Ta = 25°C)

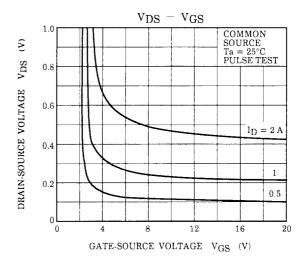
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I _{DR}	_	_	_	2	Α
Pulse drain reverse current (Note 1)	I _{DRP}	-	_	_	6	Α
Forward voltage (diode)	V _{DSF}	I _{DR} = 2 A, V _{GS} = 0 V	_	_	-1.5	V
Reverse recovery time	t _{rr}	I _{DR} = 2 A, V _{GS} = 0 V		100	_	ns
Reverse recovery charge	Q _{rr}	dI_{DR} / $dt = 50 A / \mu s$	_	40	_	nC

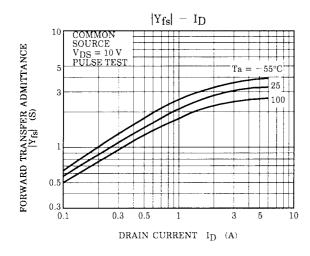
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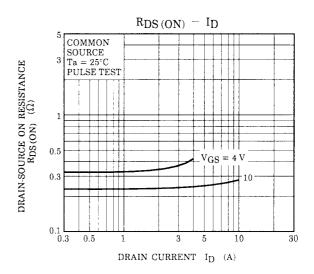




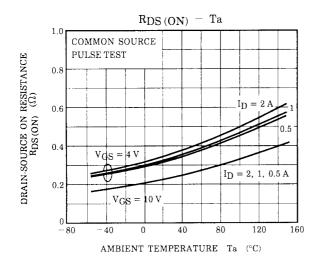


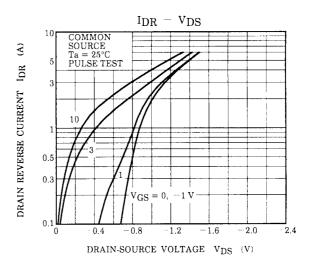


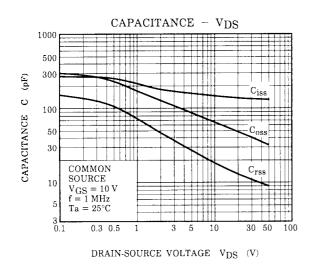


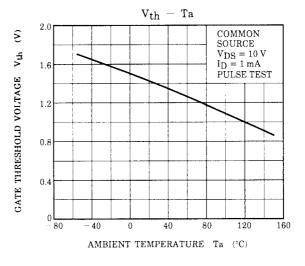


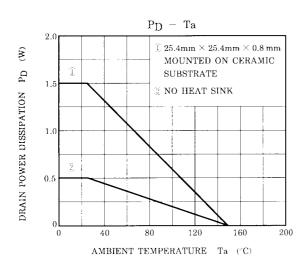
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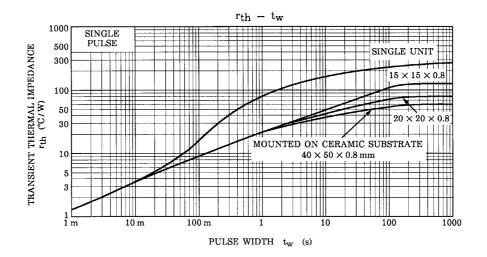






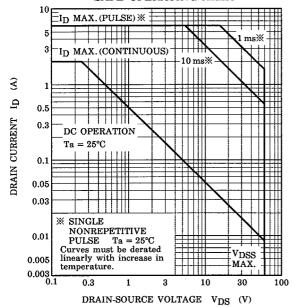


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SAFE OPERATING AREA



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