Silicon N Channel MOS FET High Speed Power Switching

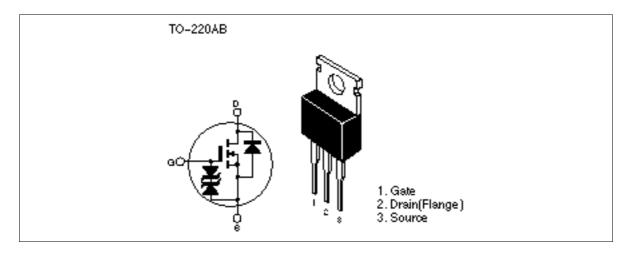


ADE-208-550 Target Specification 1st. Edition

### Features

- Low on-resistance  $R_{DS} = 0.055 \ \Omega$  typ.
- High speed switching
- 4V gate drive device can be driven from 5V source

### Outline





## Absolute Maximum Ratings ( $Ta = 25^{\circ}C$ )

Symbol	Ratings	Unit	
V <sub>DSS</sub>	60	V	
V <sub>GSS</sub>	±20	V	
I <sub>D</sub>	10	А	
I <sub>D(pulse)</sub> * <sup>1</sup>	40	А	
I <sub>DR</sub>	10	А	
I <sub>AP</sub> * <sup>3</sup>	10	А	
${\sf E}_{\sf AR}^{*3}$	8.5	mJ	
Pch* <sup>2</sup>	30	W	
Tch	150	°C	
Tstg	-55 to +150	°C	
	VDSS   VGSS   ID   ID(pulse)*1   IDR   IAP*3   EAR*3   Pch*2   Tch	VDSS 60   VGSS $\pm 20$ ID 10   ID(pulse)*1 40   IDR 10   IAP*3 10   EAR*3 8.5   Pch*2 30   Tch 150	VDSS 60 V   VGSS $\pm 20$ V   ID 10 A   ID(pulse)*1 40 A   IDR 10 A   IDR 10 A   IDR 10 A   IDR 30 W   Pch*2 30 °C

Notes: 1.  $PW \le 10\mu s$ , duty cycle  $\le 1 \%$ 

2. Value at Tc = 25°C

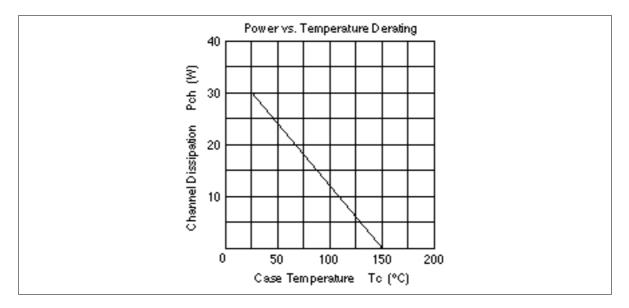
3. Value at Tch =  $25^{\circ}$ C, Rg  $50\Omega$ 

Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	60	_		V	$I_D = 10mA, V_{GS} = 0$
Gate to source breakdown voltage	$V_{(BR)GSS}$	±20	—	_	V	$I_{G} = \pm 100 \mu A, V_{DS} = 0$
Gate to source leak current	I <sub>GSS</sub>	_	_	±10	μA	$V_{GS} = \pm 16V, V_{DS} = 0$
Zero gate voltege drain current	I <sub>DSS</sub>		—	10	μA	$V_{DS} = 60 \text{ V}, V_{GS} = 0$
Gate to source cutoff voltage	$V_{\text{GS(off)}}$	1.5	—	2.5	V	$I_D = 1mA$ , $V_{DS} = 10V$
Static drain to source on state	R <sub>DS(on)</sub>	—	0.055	0.075	Ω	$I_D = 5A, V_{GS} = 10V^{*1}$
resistance	R <sub>DS(on)</sub>	—	0.090	0.150	Ω	$I_D = 5A, V_{GS} = 4V^{*1}$
Forward transfer admittance	y <sub>fs</sub>	5	8	_	S	$I_D = 5A, V_{DS} = 10V^{*1}$
Input capacitance	Ciss	_	350	_	pF	$V_{DS} = 10V$
Output capacitance	Coss	_	190	_	pF	$V_{GS} = 0$
Reverse transfer capacitance	Crss	_	70	_	pF	f = 1MHz
Turn-on delay time	t <sub>d(on)</sub>	_	10	_	ns	$I_D = 5A, V_{GS} = 10V$
Rise time	tr	—	55	_	ns	$R_L = 6\Omega$
Turn-off delay time	t <sub>d(off)</sub>	—	60	_	ns	
Fall time	t <sub>f</sub>	—	70	_	ns	
Body to drain diode forward voltage	$V_{DF}$	_	0.9	_	V	$I_F = 10A, V_{GS} = 0$
Body to drain diode reverse recovery time	t <sub>rr</sub>	_	50		ns	I <sub>F</sub> = 10A, V <sub>GS</sub> = 0 diF/ dt = 50A/μs
Noto: 1 Dulas test						

## **Electrical Characteristics** (Ta = 25°C)

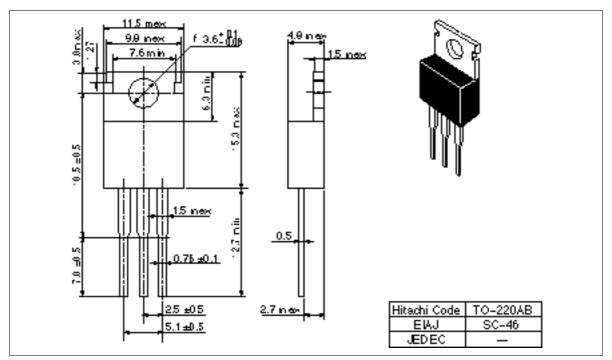
Note: 1. Pulse test

## **Main Characteristics**



## **Package Dimentions**

Unit: mm



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