Silicon N Channel MOS FET High Speed Power Switching

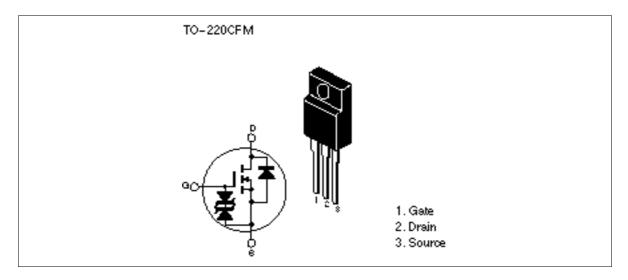


ADE-208-558 Target Specification 1st. Edition

#### Features

- Low on-resistance  $R_{DS} = 0.020 \ \Omega$  typ.
- High speed switching
- 4V gate drive device can be driven from 5V source

#### Outline





## Absolute Maximum Ratings ( $Ta = 25^{\circ}C$ )

Symbol	Ratings	Unit	
V <sub>DSS</sub>	60	V	
V <sub>GSS</sub>	±20	V	
ID	35	A	-
I <sub>D(pulse)</sub> * <sup>1</sup>	140	A	
I <sub>DR</sub>	35	A	
I <sub>AP</sub> * <sup>3</sup>	35	A	
E <sub>AR</sub> * <sup>3</sup>	105	mJ	
Pch* <sup>2</sup>	30	W	
Tch	150	°C	
Tstg	-55 to +150	°C	
	VDSS   VGSS   ID   ID(pulse)*1   IDR   IAP*3   EAR*3   Pch*2   Tch	V GO   VGSS $\pm 20$ ID 35   ID(pulse)*1 140   IDR 35   IAP*3 35   EAR*3 105   Pch*2 30   Tch 150	VDSS 60 V   VGSS ±20 V   ID 35 A   ID(pulse)*1 140 A   IDR 35 A   IAP*3 35 A   EAR*3 105 mJ   Pch*2 30 W   Tch 150 °C

Notes: 1.  $PW \le 10\mu s$ , duty cycle  $\le 1 \%$ 

2. Value at Tc = 25°C

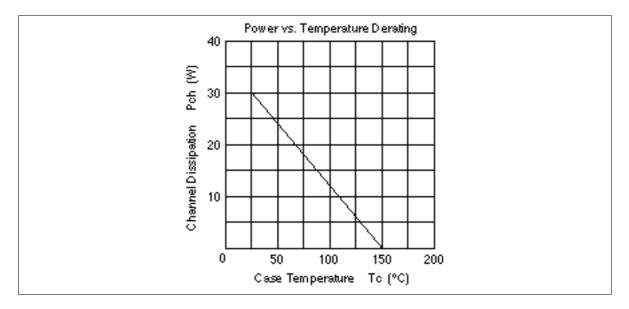
3. Value at Tch =  $25^{\circ}$ C, Rg  $50\Omega$ 

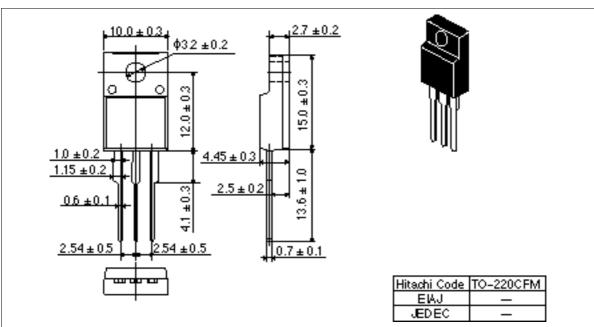
### **Electrical Characteristics** (Ta = 25°C)

Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	60			V	$I_D = 10 \text{mA}, V_{GS} = 0$
Gate to source breakdown voltage	$V_{(BR)GSS}$	±20			V	$I_{G} = \pm 100 \mu A, V_{DS} = 0$
Gate to source leak current	I <sub>GSS</sub>		—	±10	μA	$V_{GS} = \pm 16V, V_{DS} = 0$
Zero gate voltege drain current	I <sub>DSS</sub>		_	10	μA	$V_{\text{DS}} = 60 \text{ V},  V_{\text{GS}} = 0$
Gate to source cutoff voltage	V <sub>GS(off)</sub>	1.5	_	2.5	V	$I_D = 1mA$ , $V_{DS} = 10V$
Static drain to source on state	RDS(on)		0.020	0.026	Ω	$I_D = 15A, V_{GS} = 10V^{*1}$
resistance	R <sub>DS(on)</sub>		0.032	0.050	Ω	$I_D = 15A, V_{GS} = 4V^{*1}$
Forward transfer admittance	y <sub>fs</sub>	14	23	—	S	$I_D = 15A, V_{DS} = 10V^{*1}$
Input capacitance	Ciss		1100	—	pF	$V_{DS} = 10V$
Output capacitance	Coss		540	—	pF	$V_{GS} = 0$
Reverse transfer capacitance	Crss		200	—	pF	f = 1MHz
Turn-on delay time	t <sub>d(on)</sub>		15	—	ns	$I_D = 15A, V_{GS} = 10V$
Rise time	tr		180	—	ns	$R_L = 2\Omega$
Turn-off delay time	t <sub>d(off)</sub>		175	—	ns	
Fall time	t <sub>f</sub>	—	195	—	ns	
Body to drain diode forward voltage	$V_{DF}$		0.95		V	$I_F = 35A, V_{GS} = 0$
Body to drain diode reverse recovery time	t <sub>rr</sub>		40	—	ns	I <sub>F</sub> = 35A, V <sub>GS</sub> = 0 diF/ dt = 50A/μs
Note: 1 Pulse test						

Note: 1. Pulse test

### **Main Characteristics**





## **Package Dimentions**

Unit: mm

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