# DATA SHEET



# **MOS FIELD EFFECT TRANSISTORS**

# 2SK2941

## SWITCHING N-CHANNEL POWER MOS FET INDUSTRIAL USE

#### DESCRIPTION

This product is n-Chanel MOS Field Effect Transistor designed high current switching application.

#### FEATURE

- Low On-Resistance
  - $R_{DS(on)1} = 14 \text{ m}\Omega \text{ Typ.} (V_{GS} = 10 \text{ V}, \text{ Id} = 18 \text{ A})$
- $R_{DS(on)2} = 22 \text{ m}\Omega \text{ Typ.} (V_{GS} = 4 \text{ V}, \text{ ID} = 18 \text{ A})$
- Low C<sub>iss</sub> C<sub>iss</sub> = 1250 pF Typ.
- Built-in G-S Protection Diode

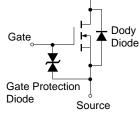
#### ABSOLUTE MAXIMUM RATINGS (T<sub>A</sub> = 25 $^{\circ}$ C)

rrents

Drain to Source Voltage	Vdss	30	V
Gate to Source Voltage	Vgss	±20	V
Drain Current (DC)	D(DC)	±35	А
Drain Current (Pulse)*	D(Pulse)	±140	А
Maximum Power Dissipation			
Total Power Dissipation (T <sub>A</sub> = 25 °C)	Рт	1.5	W
Total Power Dissipation (Tc = 25 °C)	Р⊤	60	W
Maximum Temperature			
Channel Temperature	Tch	150	°C
Storage Temperature	Tstg	-55 to + 125	°C

\* PW  $\leq$  10  $\mu$ s, Duty Cycle  $\leq$  1%

PACKAGE DIMENSIONS inmillimeters 3.0±0.3 4.8 MAX. 10.6 MAX 3.6±0.2 1.3±0.2 10.0 ШN 15.5 MAX 5.9 | 12.7 MIN. 6.0 MAX. 0.5±0.2 1.3±0 0.75±0.1 2.8±0.2 2.54 2.54 1. Gate dda 2. Drain 3. Source 4. Fin (Drain) JEDEC: TO-220AB MP-25 (TO-220) Drain



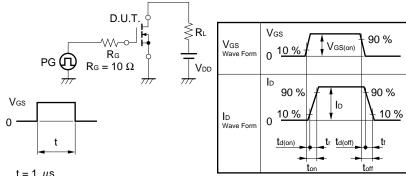
The diode connected between the gate and source of the transistor serves as a protector against ESD. When this device acutally used, an additional protection circuit is externally required if voltage exceeding the rated voltage may be applied to this device.

The information in this document is subject to change without notice.

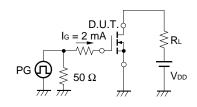
#### ELECTRICAL CHARACTERISTICS (TA = 25 $^{\circ}$ C)

CHARACTERISTIC	SYMBLO	MIN.	TYP.	MAX.	UNIT	TEST CONDITION
Drain to Source On-State	RDS(on)1		14	20	mΩ	Vgs = 10 V, Id = 18 A
Resistance	RDS(on)2		22	33	mΩ	Vgs = 4 V, Id = 18 A
Gate to Source Cutoff Voltage	VGS(off)	1.0	1.5	2.0	V	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 mA
Forward Transfer Admittance	I y <sub>fs</sub> I	8.0	25		S	Vds = 10 V, Id = 18 A
Drain Leakage Current	Idds			10	μA	Vds = 30 V, Vgs = 0
Gate to Source Leakage Current	lgss			±10	μA	$V_{GS} = \pm 20 \text{ V}, \text{ V}_{DS} = 0$
Input Capacitance	Ciss		1250		pF	VDS = 10 V, VGS = 0, f =1 MHz
Output Capacitance	Coss		900		pF	
Reverse Transfer Capacitance	Crss		460		pF	
Turn-on Delay Time	td(on)		40		ns	$I_{D} = 18 \text{ A}, \text{ V}_{GS(on)} = 10 \text{ V}$ $V_{DD} = 15 \text{ V}, \text{ R}_{G} = 10 \Omega$
Rise Time	tr		430		ns	
Turn-off Delay Time	td(off)		160		ns	
Fall Time	tr		220		ns	
Total Gate Charge	QG		50		nC	ID = 35 A, VDD = 24 V, VGS = 10 V
Gate to Source Charge	Q <sub>GS</sub>		4.5		nC	
Gate to Drain Charge	Qgd		21		nC	
Body Diode Forward Voltage	VF(S-D)		1.0		V	IF = 35 A, VGS = 0
Reverse Recovery Time	trr		65		ns	IF = 35 A, VGs = 0, di/dt = 100 A/μs
Reverse Recovery Charge	Qrr		90		nC	

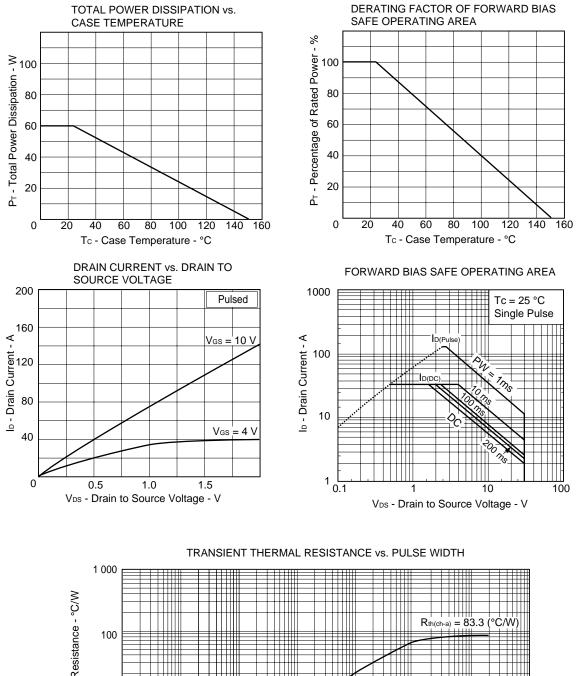
#### Test Circuit 1 Switching Time



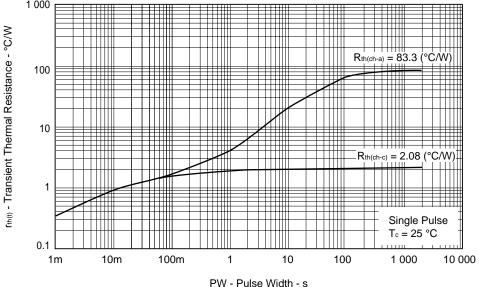
#### Test Circuit 2 Gate Charge

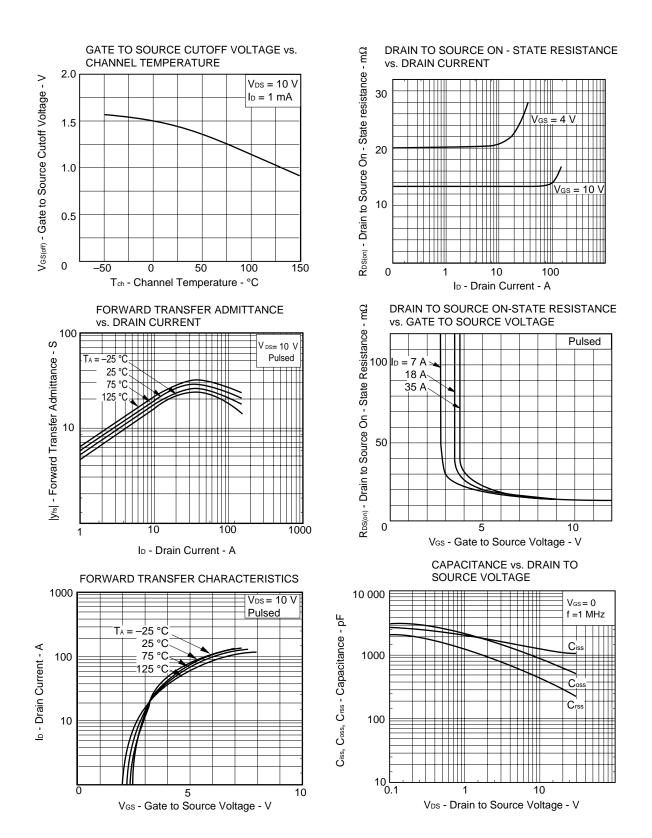


t = 1  $\mu$ s Duty Cycle  $\leq$  1 %



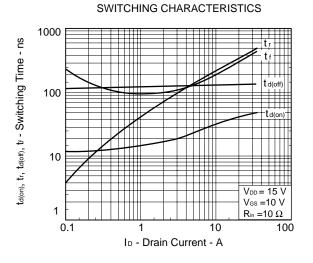
ELECTRICAL CHARACTERISTICS (T<sub>A</sub> = 25  $^{\circ}$ C)



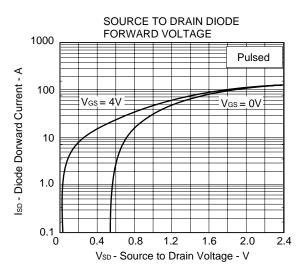


 $R_{DS(on)}$  - Drain to Source On - State Resistance - m $\Omega$ 

-50



DRAIN TO SOURCE ON-RESISTANCE vs.

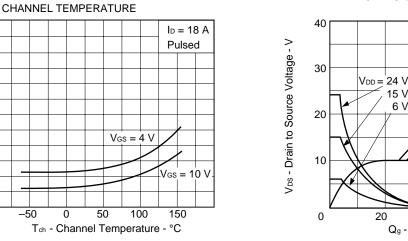


DYNAMIC INPUT/OUTPUT CHARACTERISTICS

15 V 6 V

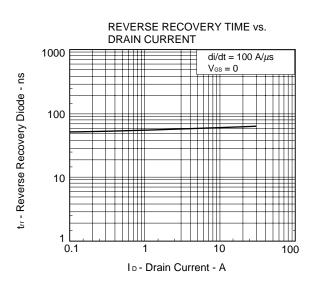
Vos

Qg - Gate Charge - nC



V<sub>GS</sub> - Gate to Source Voltage - V

ID = 35 A



### ELECTRICAL REFERENCE (TA = 25 $^{\circ}$ C)

Ducument Name	Ducument No.
NEC semiconductor device reliability/quality control system	C11745E
Quality grade on NEC semiconductor devices	C11531E
Semiconductor device mounting technology manual	C10535E
Semiconductor device package manual	C10943X
Guide to quality assurance for semiconductor devices	MEI-1202
Application circuits using Power MOS FET	TEA-1035
Safe operating area of Power MOS FET	TEA-1037

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Anti-radioactive design is not implemented in this product.