
2SK2956

Silicon N Channel MOS FET
High Speed Power Switching

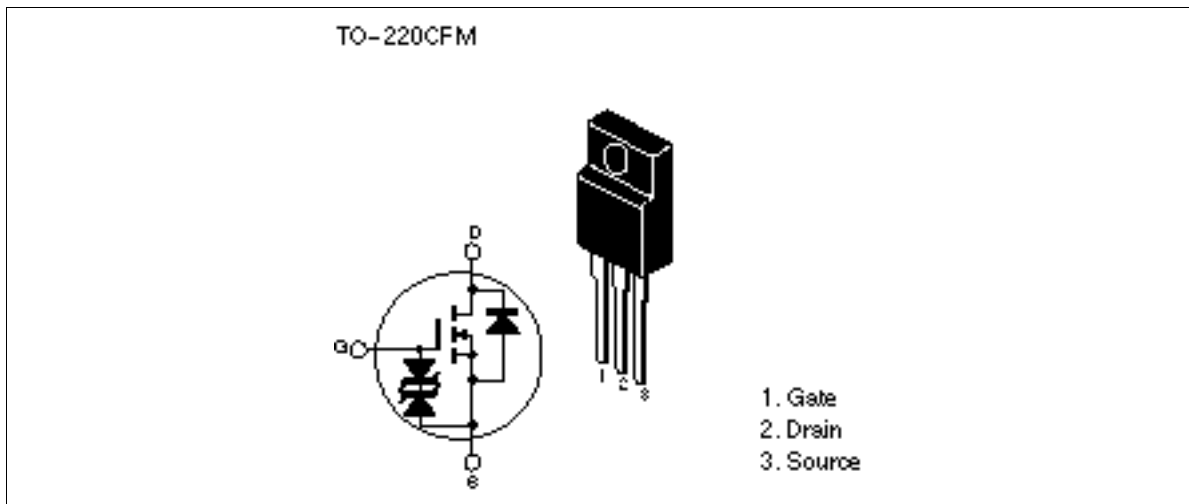
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ADE-208-566
Target specification 1st. Edition

Features

- Low on-resistance
 $R_{DS(on)} = 7m\Omega$ typ.
- 4V gate drive devices.
- High speed switching

Outline



2SK2956

Absolute Maximum Ratings (Ta = 25°C)

Item	Symbol	Ratings	Unit
Drain to source voltage	V _{DSS}	30	V
Gate to source voltage	V _{GSS}	±20	V
Drain current	I _D	50	A
Drain peak current	I _{D(pulse)} * ¹	200	A
Body to drain diode reverse drain current	I _{DR}	50	A
Channel dissipation	P _{ch} * ²	35	W
Channel temperature	T _{ch}	150	°C
Storage temperature	T _{stg}	-55 to +150	°C

Notes: 1. PW ≤ 10μs, duty cycle ≤ 1 %

2. Value at T_c = 25°C

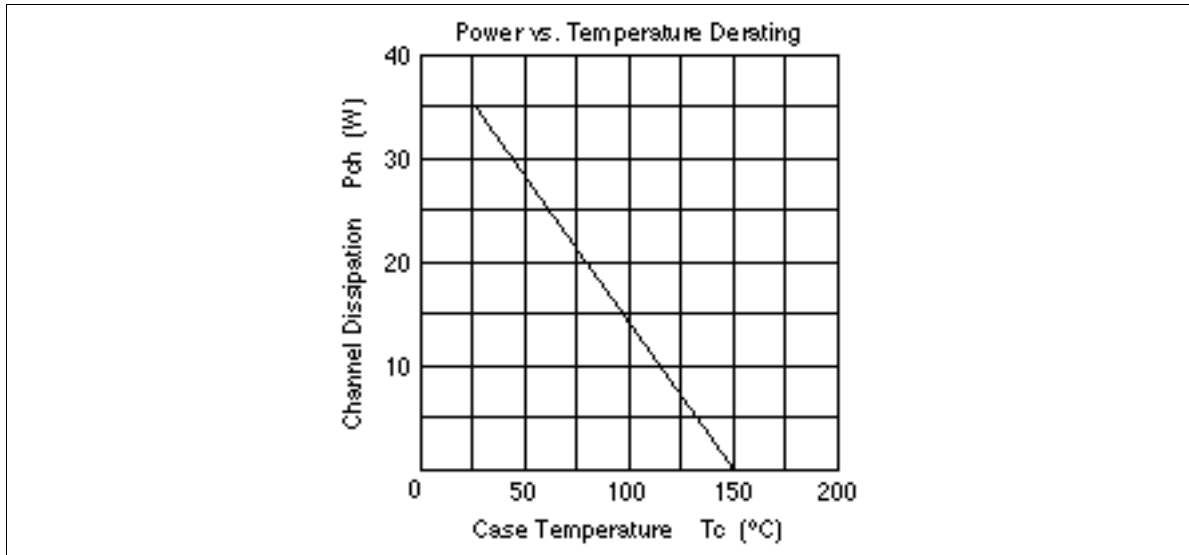
Electrical Characteristics (Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test Conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	30	—	—	V	$I_D = 10\text{mA}$, $V_{GS} = 0$
Gate to source breakdown voltage	$V_{(BR)GSS}$	± 20	—	—	V	$I_G = \pm 100\mu\text{A}$, $V_{DS} = 0$
Zero gate voltage drain current	I_{DSS}	—	—	10	μA	$V_{DS} = 30\text{V}$, $V_{GS} = 0$
Gate to source leak current	I_{GSS}	—	—	± 10	μA	$V_{GS} = \pm 16\text{V}$, $V_{DS} = 0$
Gate to source cutoff voltage	$V_{GS(off)}$	1.0	—	2.0	V	$I_D = 1\text{mA}$, $V_{DS} = 10\text{V}$
Static drain to source on state resistance	$R_{DS(on)}$	—	7.0	10	$\text{m}\Omega$	$I_D = 25\text{A}$, $V_{GS} = 10\text{V}^{*1}$
	$R_{DS(on)}$	—	12	18	$\text{m}\Omega$	$I_D = 25\text{A}$, $V_{GS} = 4\text{V}^{*1}$
Forward transfer admittance	$ y_{fs} $	25	45	—	S	$I_D = 25\text{A}$, $V_{DS} = 10\text{V}^{*1}$
Input capacitance	C_{iss}	—	2000	—	pF	$V_{DS} = 10\text{V}$
Output capacitance	C_{oss}	—	1500	—	pF	$V_{GS} = 0$
Reverse transfer capacitance	C_{rss}	—	350	—	pF	$f = 1\text{MHz}$
Turn-on delay time	$t_{d(on)}$	—	20	—	ns	$V_{GS} = 10\text{V}$, $I_D = 25\text{A}$
Rise time	t_r	—	330	—	ns	$R_L = 0.4\Omega$
Turn-off delay time	$t_{d(off)}$	—	190	—	ns	
Fall time	t_f	—	190	—	ns	
Body to drain diode forward voltage	V_{DF}	—	0.95	—	V	$I_D = 50\text{A}$, $V_{GS} = 0$
Body to drain diode reverse recovery time	t_{rr}	—	140	—	ns	$I_F = 50\text{A}$, $V_{GS} = 0$ $di_F/dt = 50\text{A}/\mu\text{s}$

Note: 1. Pulse test

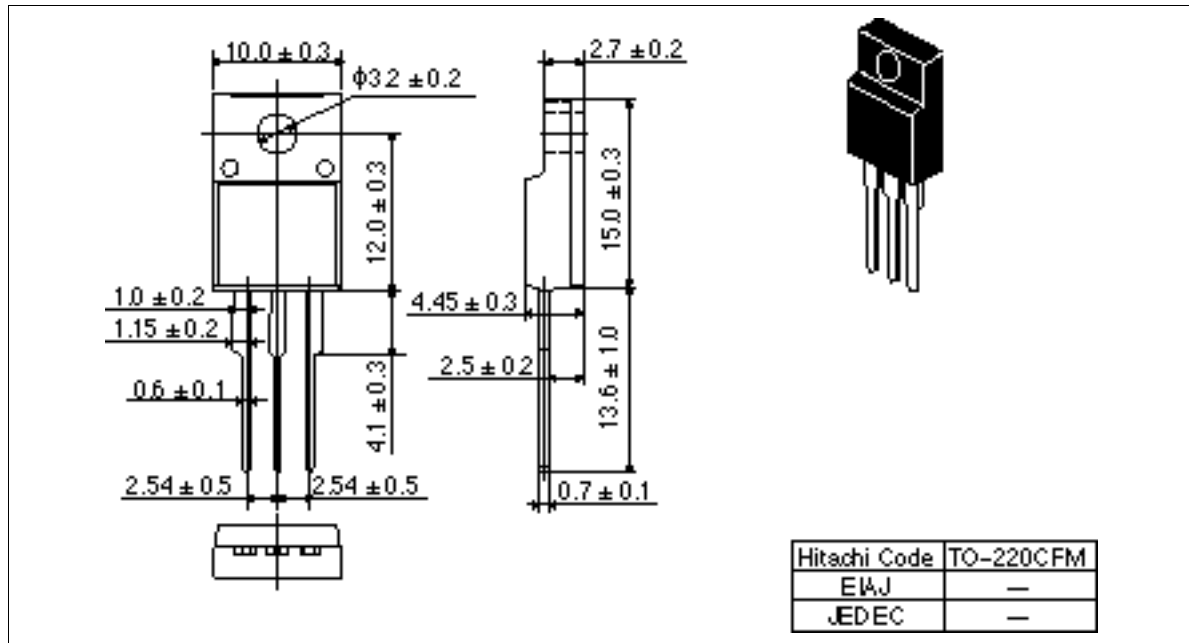
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Main Characteristics



Package Dimensions

Unit: mm



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