# SANMW 2SK3291 

## Features

- Low ON resistance.
- Ultrahigh-speed switching.
. 4V drive.


## Package Dimensions

unit:mm
2062A


## Specifications

Absolute Maximum Ratings at $\mathrm{Ta}=25^{\circ} \mathrm{C}$

| Parameter | Symbol | Conditions | Ratings | Unit |
| :---: | :---: | :---: | :---: | :---: |
| Drain-to-Source Voltage | $\mathrm{V}_{\text {DSS }}$ |  | 60 | V |
| Gate-to-Source Voltage | $\mathrm{V}_{\text {GSS }}$ |  | $\pm 20$ | V |
| Drain Current (DC) | ID |  | 1.6 | A |
| Drain Current (Pulse) | IDP | $\mathrm{PW} \leq 10 \mu \mathrm{~s}$, duty cycle $\leq 1 \%$ | 6.4 | A |
| Allowable Power Dissipation | $P_{D}$ | Mounted on a ceramic board ( $250 \mathrm{~mm}^{2} \times 0.8 \mathrm{~mm}$ ) | 1.3 | W |
|  |  | Tc= $25^{\circ} \mathrm{C}$ | 3.5 | W |
| Channel Temperature | Tch |  | 150 | ${ }^{\circ} \mathrm{C}$ |
| Storage Temperature | Tstg |  | -55 to +150 | ${ }^{\circ} \mathrm{C}$ |

Electrical Characteristics at $\mathrm{Ta}=25^{\circ} \mathrm{C}$

| Parameter | Symbol | Conditions | Ratings |  |  | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  | min | typ | max |  |
| Drain-to-Source Breakdown Voltage | $\mathrm{V}_{(\mathrm{BR}) \mathrm{DSS}}$ | ${ }^{1} \mathrm{D}=1 \mathrm{~mA}, \mathrm{~V}_{\mathrm{GS}}=0$ | 60 |  |  | V |
| Zero Gate Voltage Drain Current | IDSS | $\mathrm{V}_{\mathrm{DS}}=60 \mathrm{~V}, \mathrm{~V}_{\mathrm{GS}}=0$ |  |  | 10 | $\mu \mathrm{A}$ |
| Gate-to-Source Leakage Current | IGSS | $\mathrm{V}_{\mathrm{GS}}= \pm 16 \mathrm{~V}, \mathrm{~V}_{\mathrm{DS}}=0$ |  |  | $\pm 10$ | $\mu \mathrm{A}$ |
| Cutoff Voltage | $\mathrm{V}_{\mathrm{GS}}(\mathrm{off})$ | $\mathrm{V}_{\mathrm{DS}}=10 \mathrm{~V}, \mathrm{I}_{\mathrm{D}}=1 \mathrm{~mA}$ | 1.0 |  | 2.4 | V |
| Forward Transfer Admittance | \| yfs | | $\mathrm{V}_{\mathrm{DS}}=10 \mathrm{~V}, \mathrm{I}_{\mathrm{D}}=0.8 \mathrm{~A}$ | 1.2 | 1.7 |  | S |
| Static Drain-to-Source On-State Resistance | $\mathrm{R}_{\mathrm{DS} \text { (on) }}{ }^{1}$ | $\mathrm{I}_{\mathrm{D}}=0.8 \mathrm{~A}, \mathrm{~V}_{\mathrm{GS}}=10 \mathrm{~V}$ |  | 380 | 500 | $\mathrm{m} \Omega$ |
|  | $\mathrm{R}_{\mathrm{DS}}(\mathrm{on})^{2}$ | ${ }^{\mathrm{I}}=0.8 \mathrm{~A}, \mathrm{~V}_{\mathrm{GS}}=4 \mathrm{~V}$ |  | 500 | 680 | $\mathrm{m} \Omega$ |

Marking : KX
Continued on next page.

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| Parameter | Symbol | Conditions | Ratings |  |  | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  | min | typ | max |  |
| Input Capacitance | Ciss | $\mathrm{V}_{\mathrm{DS}}=20 \mathrm{~V}, \mathrm{f}=1 \mathrm{MHz}$ |  | 70 |  | pF |
| Output Capacitance | Coss | $\mathrm{V}_{\mathrm{DS}}=20 \mathrm{~V}, \mathrm{f}=1 \mathrm{MHz}$ |  | 20 |  | pF |
| Reverse Transfer Capacitance | Crss | $\mathrm{V}_{\mathrm{DS}}=20 \mathrm{~V}, \mathrm{f}=1 \mathrm{MHz}$ |  | 5 |  | pF |
| Turn-ON Delay Time | ${ }_{\mathrm{d}}(\mathrm{on})$ | See specified Test Circuit |  | 5 |  | ns |
| Rise Time | $\mathrm{t}_{\mathrm{r}}$ | See specified Test Circuit |  | 4 |  | ns |
| Turn-OFF Delay Time | $\mathrm{t}_{\mathrm{d} \text { (off) }}$ | See specified Test Circuit |  | 18 |  | ns |
| Fall Time | $\mathrm{t}_{\mathrm{f}}$ | See specified Test Circuit |  | 5 |  | ns |
| Total Gate Charge | Qg | $\mathrm{V}_{\mathrm{DS}}=10 \mathrm{~V}, \mathrm{~V}_{\mathrm{GS}}=10 \mathrm{~V}, \mathrm{I}_{\mathrm{D}}=1.6 \mathrm{~A}$ |  | 3.6 |  | nC |
| Gate-to-Source Charge | Qgs |  |  | 0.6 |  | nC |
| Gate-to-Drain "Miller" Charge | Qgd |  |  | 0.5 |  | nC |
| Diode Forward Voltage | $\mathrm{V}_{\text {SD }}$ | $\mathrm{I}_{\mathrm{S}}=1.6 \mathrm{~A}, \mathrm{~V}_{\mathrm{GS}}=0$ |  | 0.85 | 1.2 | V |

Switching Time Test Circuit


2SK3291


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