BCW66GLT1G, SBCW66GLT1G

General Purpose Transistor NPN Silicon

Features

- AEC-Q101 Qualified and PPAP Capable
- S Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements
- These Devices are Pb-Free, Halogen Free/BFR Free and are RoHS Compliant*



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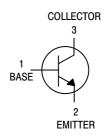


SOT-23 (TO-236) CASE 318-08 STYLE 6

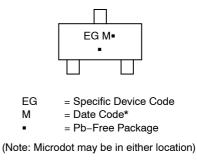
MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector – Emitter Voltage	V _{CEO}	45	Vdc
Collector – Base Voltage	V _{CBO}	75	Vdc
Emitter – Base Voltage	V _{EBO}	5.0	Vdc
Collector Current – Continuous	Ι _C	800	mAdc

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.



MARKING DIAGRAM



*Date Code orientation and/or overbar may vary depending upon manufacturing location.

ORDERING INFORMATION

Device	Package	Shipping [†]
BCW66GLT1G	SOT-23 (Pb-Free)	3,000/Tape & Reel
SBCW66GLT1G	SOT-23 (Pb-Free)	3,000/Tape & Reel
BCW66GLT3G	SOT-23 (Pb-Free)	10,000/Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

*For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Total Device Dissipation FR-5 Board (Note 1), T _A = 25°C Derate above 25°C	P _D	225 1.8	mW mW/°C
Thermal Resistance, Junction-to-Ambient	$R_{ hetaJA}$	556	°C/W
Total Device Dissipation Alumina Substrate, (Note 2) T _A = 25°C Derate above 25°C	P _D	300 2.4	mW mW/°C
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	417	°C/W
Junction and Storage Temperature	T _J , T _{stg}	-55 to +150	°C

1. FR-5 = $1.0 \times 0.75 \times 0.062$ in.

2. Alumina = 0.4 \times 0.3 \times 0.024 in 99.5% alumina.

BCW66GLT1G, SBCW66GLT1G

ELECTRICAL CHARACTERISTICS ($T_A = 25^{\circ}C$ unless otherwise noted)

Characteristic	Symbol	Min	Тур	Max	Unit
OFF CHARACTERISTICS				1	
Collector – Emitter Breakdown Voltage ($I_C = 10 \text{ mAdc}, I_B = 0$)	V _{(BR)CEO}	45	-	-	Vdc
Collector – Emitter Breakdown Voltage (I _C = 10 μ Adc, V _{EB} = 0)	V _{(BR)CES}	75	-	-	Vdc
Emitter – Base Breakdown Voltage (I _E = 10 μ Adc, I _C = 0)	V _{(BR)EBO}	5.0	-	-	Vdc
Collector Cutoff Current ($V_{CE} = 45 \text{ Vdc}, I_E = 0$) ($V_{CE} = 45 \text{ Vdc}, I_E = 0, T_A = 150^{\circ}\text{C}$)	I _{CES}			20 20	nAdc µAdc
Emitter Cutoff Current (V_{EB} = 4.0 Vdc, I_C = 0)	I _{EBO}	-	-	20	nAdc
ON CHARACTERISTICS					
DC Current Gain ($I_C = 100 \mu Adc, V_{CE} = 10 Vdc$) ($I_C = 10 mAdc, V_{CE} = 1.0 Vdc$) ($I_C = 100 mAdc, V_{CE} = 1.0 Vdc$) ($I_C = 500 mAdc, V_{CE} = 2.0 Vdc$)	h _{FE}	50 110 160 60		- - 400 -	_
Collector – Emitter Saturation Voltage ($I_C = 500 \text{ mAdc}, I_B = 50 \text{ mAdc}$) ($I_C = 100 \text{ mAdc}, I_B = 10 \text{ mAdc}$)	V _{CE(sat)}			0.7 0.3	Vdc
Base – Emitter Saturation Voltage (I _C = 500 mAdc, I _B = 50 mAdc)	V _{BE(sat)}	_	_	2.0	Vdc
SMALL-SIGNAL CHARACTERISTICS		•		•	•
Current – Gain — Bandwidth Product (I _C = 20 mAdc, V _{CE} = 10 Vdc, f = 100 MHz)	f _T	100	-	-	MHz
Output Capacitance (V _{CB} = 10 Vdc, I _E = 0, f = 1.0 MHz)	C _{obo}	-	-	12	pF
Input Capacitance $(V_{EB} = 0.5 \text{ Vdc}, I_C = 0, f = 1.0 \text{ MHz})$	C _{ibo}	-	-	80	pF
Noise Figure (V _{CE} = 5.0 Vdc, I _C = 0.2 mAdc, R _S = 1.0 k Ω , f = 1.0 kHz, BW = 200 Hz)	NF	-	-	10	dB
SWITCHING CHARACTERISTICS		-	•	*	•
Turn-On Time ($l_{P4} = l_{P2} = 15 \text{ mAdc}$)	tere	_	_	100	ns

Turn–On Time (I _{B1} = I _{B2} = 15 mAdc)	t _{on}	-	-	100	ns
Turn–Off Time (I _C = 150 mAdc, R_L = 150 Ω)	t _{off}		-	400	ns

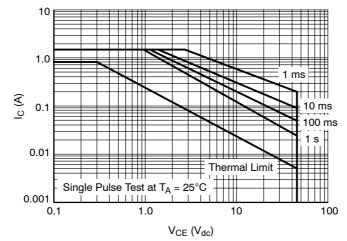
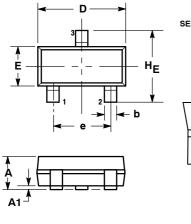
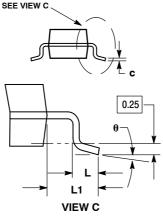


Figure 1. Safe Operating Area

PACKAGE DIMENSIONS

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NOTES:

- 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
- 2. CONTROLLING DIMENSION: INCH. 3. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH
- THICKNESS. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF BASE MATERIAL. 4. DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH, PROTRUSIONS, OR GATE BURRS.
- PROTRUSIONS, OR GATE BURRS.

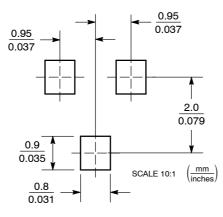
	м	ILLIMETE	ETERS INCHES			
DIM	MIN	NOM	MAX	MIN	NOM	MAX
Α	0.89	1.00	1.11	0.035	0.040	0.044
A1	0.01	0.06	0.10	0.001	0.002	0.004
b	0.37	0.44	0.50	0.015	0.018	0.020
С	0.09	0.13	0.18	0.003	0.005	0.007
D	2.80	2.90	3.04	0.110	0.114	0.120
E	1.20	1.30	1.40	0.047	0.051	0.055
е	1.78	1.90	2.04	0.070	0.075	0.081
L	0.10	0.20	0.30	0.004	0.008	0.012
L1	0.35	0.54	0.69	0.014	0.021	0.029
HE	2.10	2.40	2.64	0.083	0.094	0.104
θ	0°		10°	0°		10°

STYLE 6:

PIN 1. BASE 2. EMITTER

3. COLLECTOR

SOLDERING FOOTPRINT



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