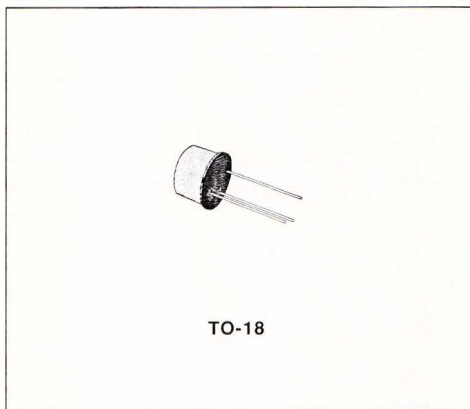
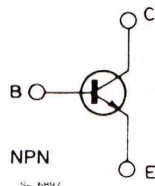


LOW-LEVEL, LOW-NOISE, VERY HIGH GAIN AMPLIFIER
DESCRIPTION

The BFR17 is a silicon planar epitaxial NPN transistor in Jedec TO-18 metal case, designed for use in high performance low level, low noise amplifier applications.


INTERNAL SCHEMATIC DIAGRAM

ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Value	Unit
V_{CES}	Collector-emitter Voltage ($V_{BE} = 0$)	60	V
V_{CEO}	Collector-emitter Voltage ($I_B = 0$)	60	V
V_{EBO}	Emitter-base Voltage ($I_C = 0$)	8	V
I_C	Collector Current	50	mA
P_{tot}	Total Power Dissipation at $T_{amb} = 25\text{ }^\circ\text{C}$ at $T_{case} = 25\text{ }^\circ\text{C}$	0.36	W
		1.2	W
T_{stg}, T_j	Storage and Junction Temperature	- 55 to 200	$^\circ\text{C}$

THERMAL DATA

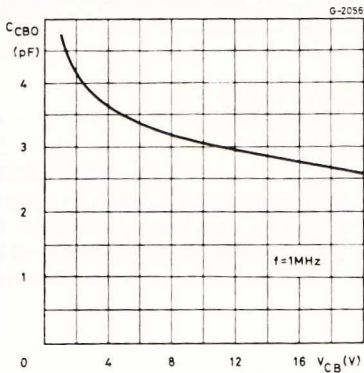
$R_{th\ j-case}$	Thermal Resistance Junction–case	Max	146	$^{\circ}C/W$
$R_{th\ j-amb}$	Thermal Resistance Junction–ambient	Max	486	$^{\circ}C/W$

ELECTRICAL CHARACTERISTICS ($T_{amb} = 25\ ^{\circ}C$ unless otherwise specified)

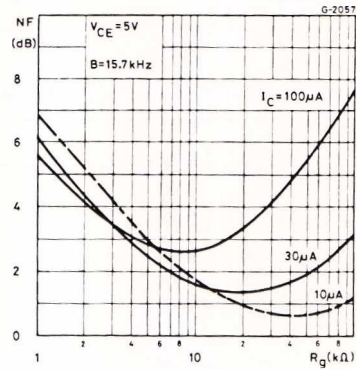
Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
I_{CES}	Collector Cutoff Current ($V_{BE} = 0$)	$V_{CE} = 50\ V$ $V_{CE} = 50\ V$ $T_{amb} = 150\ ^{\circ}C$		0.1 0.1	20 20	nA μA
I_{EBO}	Emitter Cutoff Current ($I_C = 0$)	$V_{EB} = 5\ V$		0.1	20	nA
$V_{(BR)CEO}^*$	Collector-emitter Breakdown Voltage ($I_B = 0$)	$I_C = 10\ mA$	60			V
$V_{(BR)CES}$	Collector-emitter Breakdown Voltage ($V_{BE} = 0$)	$I_C = 10\ \mu A$	60			V
$V_{(BR)EBO}$	Emitter-base Breakdown Voltage ($I_C = 0$)	$I_E = 10\ \mu A$	8			V
$V_{CE(sat)}^*$	Collector-emitter Saturation Voltage	$I_C = 1\ mA$ $I_B = 0.1\ mA$		0.15	0.35	V
V_{BE}^*	Base-emitter Voltage	$I_C = 1\ mA$ $V_{CE} = 5\ V$ $I_C = 100\ \mu A$ $V_{CE} = 5\ V$		0.64 0.58	0.7	V V
h_{FE}^*	DC Current Gain	$I_C = 10\ \mu A$ $V_{CE} = 5\ V$ $I_C = 100\ \mu A$ $V_{CE} = 5\ V$ $I_C = 1\ mA$ $V_{CE} = 5\ V$ $I_C = 10\ mA$ $V_{CE} = 5\ V$	130 220 450	220 300 530 530		
h_{fe}	Small Signal Current Gain	$I_C = 1\ mA$ $f = 20\ kHz$ $V_{CE} = 5\ V$		530		
f_T	Transition Frequency	$I_C = 1\ mA$ $f = 20\ MHz$ $V_{CE} = 5\ V$	70	100		MHz
C_{CBO}	Collector-base Capacitance	$I_E = 0$ $V_{CB} = 5\ V$		3.5	6	pF
C_{EBO}	Emitter-base Capacitance	$I_C = 0$ $V_{EB} = 5\ V$		3.5	6	pF
NF	Noise Figure	$I_C = 10\ \mu A$ $R_g = 10\ k\Omega$ $f = 10\ Hz\ to\ 10\ kHz$ $V_{CE} = 5\ V$ $f = 1\ kHz$ $f = 10\ kHz$		1.8 1 1	4 3 3	dB dB dB
h_{ie}	Input Impedance			10		k Ω
h_{oe}	Output Admittance	$I_C = 1\ mA$ $f = 1\ kHz$ $V_{CE} = 5\ V$		20		μS
h_{re}	Reverse Voltage Ratio			4.5×10^{-4}		

* Pulsed : pulse duration = 300 μs , duty cycle = 1%.

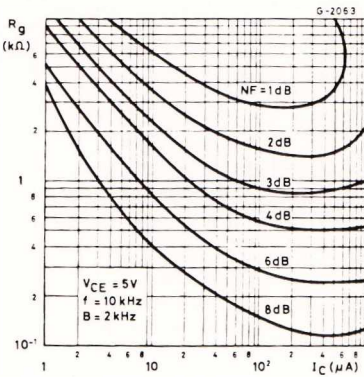
Collector-base Capacitance.



Noise Figure vs. Source Resistance.



Contours of Constant Noise Figure $f = 10\text{kHz}$.



Contours of Constant Noise Figure $f = 1\text{kHz}$.

