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BSR58

N-Channel JFET Low-Frequency Low-Noise Amplifier

 This device is designed for low-power chopper or switching application sourced from process 51



1. Drain 2. Source 3. Gate

Absolute Maximum Ratings $\rm T_{C} = 25^{\circ}C$ unless otherwise noted

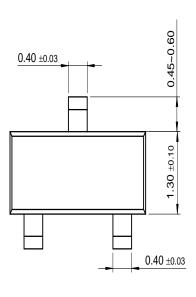
Symbol	Parameter	Value	Units
V_{DGO}	Drain-Gate Voltage	40	V
V_{GSO}	Gate-Source Voltage	- 40	V
I _{GF}	Forward Gate Current	50	mA
P _{tot}	Total Power Dissipation up to T _{amb} =40°C	250	mW
T _{STG}	Storage Temperature Range	- 55 ~ 150	°C
TJ	Junction Temperature	150	°C

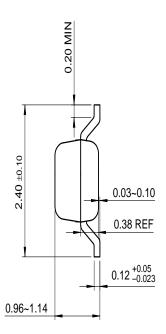
Electrical Characteristics $T_C=25$ °C unless otherwise noted

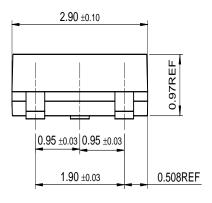
Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Units
BV _{GSS}	Gate-Source Voltage	$V_{DS} = 0V, I_{C} = 1\mu A$	40			V
I _{GSS}	Gate Reverse Current	V _{GS} = 20V			1	nA
I _{DSS}	Zero-Gate Voltage Drain Current	$V_{DS} = 15V, V_{GS} = 0V$	8		80	mA
V _{GS} (off)	Gate-Source Cut-off Voltage	$V_{DS} = 15V, I_{D} = 0.5nA$	0.8		4	V
V _{DS} (on)	Drain-Source On Voltage	$V_{GS} = 0V$, $I_D = 5mA$			0.4	V
r _{ds} (on)	Drain-Source On Reverse	$V_{GS} = 0V, I_{D} = 0$			60	Ω
C _{rss}	Reverse Transfer Capacitance	$V_{DS} = 0V, V_{GS} = 10V$			5	pF
t _d	Delay Time	$V_{DD} = 10V, V_{GS}(on) = 0V$			10	nS
t _r	Rise Time	$I_D = 10 \text{mA}, V_{GS}(\text{off}) = 10 \text{V}$			10	nS
t _{off}	Turn-off Time				100	nS

Package Dimensions

SOT-23







Dimensions in Millimeters

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