

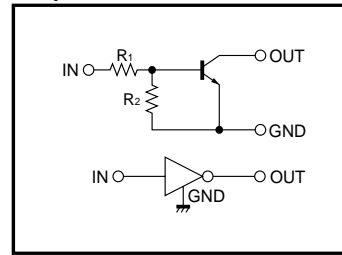
# Digital transistors (built-in resistors)

## DTC115EM / DTC115EE / DTC115EUA DTC115EKA / DTC115ESA

### ●Features

- 1) Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see the equivalent circuit).
- 2) The bias resistors consist of thin-film resistors with complete isolation to allow negative biasing of the input, and parasitic effects are almost completely eliminated.
- 3) Only the on/off conditions need to be set for operation, making device design easy.
- 4) Higher mounting densities can be achieved.

### ●Equivalent circuit



### ●Structure

NPN digital transistor (with built-in resistors)

### ●External dimensions (Units : mm)

<p>DTC115EM</p> <p>ROHM : VMT3</p> <p>Abbreviated symbol : 29</p>	<p>DTC115EE</p> <p>ROHM : EMT3</p> <p>Abbreviated symbol : 29</p>
<p>DTC115EUA</p> <p>ROHM : UMT3 EIAJ : SC-70</p> <p>All terminals have same dimensions</p> <p>Abbreviated symbol : 29</p>	<p>DTC115EKA</p> <p>ROHM : SMT3 EIAJ : SC-59</p> <p>All terminals have same dimensions</p> <p>Abbreviated symbol : 29</p>
<p>DTC115ESA</p> <p>ROHM : SPT EIAJ : SC-72</p>	

# DTC115EM / DTC115EE / DTC115EUA DTC115EKA / DTC115ESA

## Transistors

### ●Absolute maximum ratings (Ta=25°C)

Parameter	Symbol	Limits	Unit
Supply voltage	V <sub>CC</sub>	50	V
Input voltage	V <sub>I</sub>	-10~+40	V
Output current	I <sub>O</sub>	20	mA
	I <sub>C(Max.)</sub>	100	
Power dissipation	DTC115EM / DTC115EE	150	mW
	DTC115EUA / DTC115EKA	200	
	DTC115ESA	300	
Junction temperature	T <sub>J</sub>	150	°C
Storage temperature	T <sub>stg</sub>	-55~+150	°C

### ●Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Input voltage	V <sub>I(off)</sub>	-	-	0.5	V	V <sub>CC</sub> =5V, I <sub>O</sub> =100μA
	V <sub>I(on)</sub>	3	-	-		V <sub>O</sub> =0.3V, I <sub>O</sub> =1mA
Output voltage	V <sub>O(on)</sub>	-	0.1	0.3	V	I <sub>O</sub> =5mA, I <sub>B</sub> =0.25mA
Input current	I <sub>I</sub>	-	-	0.15	mA	V <sub>I</sub> =5V
Output current	I <sub>O(off)</sub>	-	-	0.5	μA	V <sub>CC</sub> =50V, V <sub>I</sub> =0V
DC current gain	G <sub>I</sub>	82	-	-	-	I <sub>O</sub> =5mA, V <sub>O</sub> =5V
Input resistance	R <sub>I</sub>	70	100	130	kΩ	-
Resistance ratio	R <sub>2</sub> /R <sub>1</sub>	0.8	1	1.2	-	-
Transition frequency	f <sub>r</sub>	-	250	-	MHz	V <sub>CE</sub> =10V, I <sub>E</sub> =-5mA, f=100MHz *

\*Transition frequency of the device.

### ●Package, marking, and packaging specifications

Type	DTC115EM	DTC115EE	DTC115EUA	DTC115EKA	DTC115ESA
Package	VMT3	EMT3	UMT3	SMT3	SPT
Marking	29	29	29	29	-
Packaging code	T2L	TL	T106	T146	TP
Basic ordering unit (pieces)	8000	3000	3000	3000	5000