TM

FAIRCHILD

SEMICONDUCTOR®

FCB20N60F 600V N-CHANNEL FRFET

Features

- 650V @T_J = 150°C
- Typ. Rds(on)=0.15Ω
- Fast Recovery Type (t_{rr} = 160ns)
- Ultra low gate charge (typ. Qg=75nC)
- · Low effective output capacitance (typ. Coss.eff=165pF)
- 100% avalanche tested
- RoHS Compliant



Description

SuperFETTM is, Fairchild's proprietary, new generation of high voltage MOSFET family that is utilizing an advanced charge balance mechanism for outstanding low on-resistance and lower gate charge performance.

December 2008

SuperFET

This advanced technology has been tailored to minimize conduction loss, provide superior switching performance, and withstand extreme dv/dt rate and higher avalanche energy. Consequently, SuperFET is very suitable for various AC/DC power conversion in switching mode operation for system miniaturization and higher efficiency.



Go

Absolute Maximum Ratings

| Symbol | Para | imeter | FCB20N60F | Unit | |
|----------------------------------|------------------------------------------------------------------|--------------------------------------------------------------------------------------------|-------------|-----------|--|
| V _{DSS} | Drain-Source Voltage | | 600 | V | |
| I _D | | rain Current - Continuous ($T_C = 25^{\circ}C$) - Continuous ($T_C = 100^{\circ}C$) | | A A | |
| I _{DM} | Drain Current - Pulse | ed (Note | 60 | A | |
| V _{GSS} | Gate-Source voltage | | ± 30 | V | |
| E _{AS} | Single Pulsed Avalanche Energy | | 2) 690 | mJ | |
| I _{AR} | Avalanche Current | | 20 | A | |
| E _{AR} | Repetitive Avalanche Energy | | 20.8 | mJ | |
| dv/dt | Peak Diode Recovery dv/dt (I | | 3) 50 | V/ns | |
| P _D | Power Dissipation (T _C = 25°C) - Derate above 25°C | | 208 1.67 | W W/°C | |
| T _{J,} T _{STG} | Operating and Storage Temp | perature Range | -55 to +150 | °C | |
| Τ _L | Maximum Lead Temperature 1/8" from Case for 5 Seconds | č | 300 | °C | |

Thermal Characteristics

| Symbol | Parameter | FCB20N60F | Unit | | |
|----------------------------------------------------------------|------------------------------------------|-----------|------|--|--|
| $R_{\theta JC}$ | Thermal Resistance, Junction-to-Case | 0.6 | °C/W | | |
| R _{θJA} * | Thermal Resistance, Junction-to-Ambient* | 40 | °C/W | | |
| $R_{\theta JA}$ | Thermal Resistance, Junction-to-Ambient | 62.5 | °C/W | | |
| * When mounted on the minimum pad size recommended (PCB Mount) | | | | | |

| Device Ma | evice Marking Device Pa | | Packa | kage Reel Size Tap | | e Width | | Quantity | | |
|---------------------|----------------------------------------------|----------------------------------|-----------------------|------------------------------------------------------------------------------------------------|----------------------|-------------|------|-----------|----------|-------|
| FCB20N | • | | D2-Pa | -Pak 330mm | | 24m | | 800 | | |
| Electrica | l Chai | racteristics T _c = | = 25°C unless | s otherwise n | oted | | | | | |
| Symbol | | Parameter | | (| Conditions | | Min | Тур | Max | Units |
| Off Characte | eristics | | I | | | | | | 1 | 1 |
| BV _{DSS} | Drain-Source Breakdown Voltage | | e V | $V_{GS} = 0V, I_D = 250\mu A, T_J = 25^{\circ}C$ | | | 600 | | | V |
| | | | V | V_{GS} = 0V, I _D = 250µA, T _J = 150°C | | | | 650 | | V |
| 200 | Breakdown Voltage Temperature Coefficient | | e I _D | $I_D = 250 \mu A$, Referenced to $25^{\circ}C$ | | | | 0.6 | | V/°C |
| 000 | Drain-Source Avalanche Breakdown Voltage | | lown V | V _{GS} = 0V, I _D = 20A | | | | 700 | | V |
| I _{DSS} | Zero Gate Voltage Drain Current | | t V _I V | V _{DS} = 600V, V _{GS} = 0V V _{DS} = 480V, T _C = 125°C | | | | 10 100 | μA μA | |
| I _{GSSF} | Gate-Bod | te-Body Leakage Current, Forward | | $V_{GS} = 30V, V_{DS} = 0V$ | | | | 100 | nA | |
| I _{GSSR} | Gate-Bod | ody Leakage Current, Reverse | | V _{GS} = -30V, V _{DS} = 0V | | | | -100 | nA | |
| On Characte | eristics | | | | | | | | | |
| V _{GS(th)} | Gate Thre | shold Voltage | V | _{DS} = V _{GS} , I _E | ₀ = 250μA | | 3.0 | | 5.0 | V |
| R _{DS(on)} | Static Drain-Source On-Resistance | | V | V _{GS} = 10V, I _D = 10A | | | 0.15 | 0.19 | Ω | |
| g _{FS} I | Forward 1 | ransconductance | V | _{DS} = 40V, I _D | = 10A | (Note 4) | | 17 | | S |
| Dynamic Ch | aracteris | tics | I | | | | | | | 4 |
| C _{iss} | Input Cap | acitance | | V _{DS} = 25V, V _{GS} = 0V, | | | 2370 | 3080 | pF | |
| C _{oss} | Output Capacitance | | f = | f = 1.0MHz | | | 1280 | 1665 | pF | |
| | Reverse | Fransfer Capacitance | | 1 [| | | 95 | | pF | |
| C _{oss} | Output Capacitance | | V | 7 _{DS} = 480V, V _{GS} = 0V, f = 1.0MHz | | MHz | | 65 | 85 | pF |
| | Effective Output Capacitance | | V | $V_{\rm DS}$ = 0V to 400V, $V_{\rm GS}$ = 0V | | | 165 | | pF | |
| Switching C | haracteri | stics | | | | | | | | |
| t _{d(on)} | Turn-On [| Delay Time | | $V_{DD} = 300V, I_D = 20A$ $R_G = 25\Omega$ | | | 62 | 135 | ns | |
| t _r | Turn-On F | Rise Time | R | | | | 140 | 290 | ns | |
| t _{d(off)} | Turn-Off [| Delay Time | | | | | 230 | 470 | ns | |
| t _f | Turn-Off F | all Time | | | | (Note 4, 5) | | 65 | 140 | ns |
| Q _g | Total Gate | e Charge | V | $V_{DS} = 480V, I_D = 20A$ $V_{GS} = 10V$ (Note 4, 5) | | | 75 | 98 | nC | |
| Q _{gs} | Gate-Sou | rce Charge | V | | | | 13.5 | 18 | nC | |
| Q _{gd} | Gate-Drai | n Charge | | | | | 36 | | nC | |
| Drain-Sourc | e Diode (| Characteristics and M | laximum R | atings | | | | | | |
| I _S I | Maximum Continuous Drain-Source Diod | | urce Diode F | le Forward Current | | | | 20 | A | |
| I _{SM} I | Maximum Pulsed Drain-Source Diode Fo | | Diode Forw | prward Current | | | | 60 | A | |
| | Drain-Sou | Irce Diode Forward Vol | Itage V | V _{GS} = 0V, I _S = 20A | | | | 1.4 | V | |
| | Reverse F | Recovery Time | | _{GS} = 0V, I _S | | | | 160 | | ns |
| | Dovorco I | Recovery Charge | dl | dI _F /dt =100A/μs (Note 4) | | | 1.1 | | μC | |

NOTES:

1. Repetitive Rating: Pulse width limited by maximum junction temperature

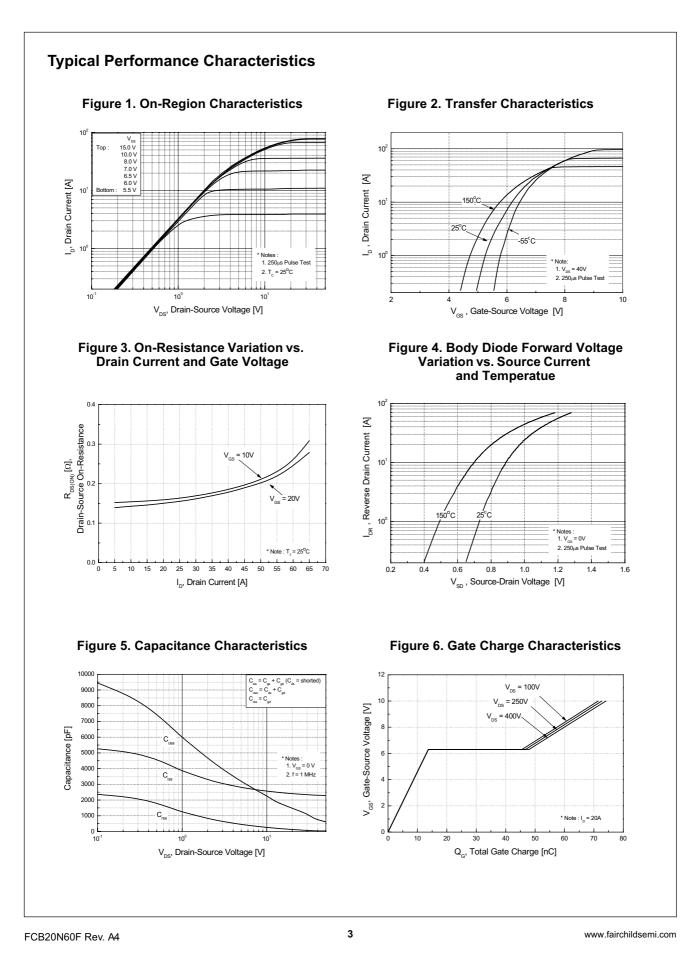
2. I_{AS} = 10A, V_{DD} = 50V, R_G = 25 Ω , Starting T_J = 25°C

3. I_{SD} \leq 20A, di/dt \leq 1200A/µs, V_{DD} \leq BV_{DSS}, Starting T_J = 25°C

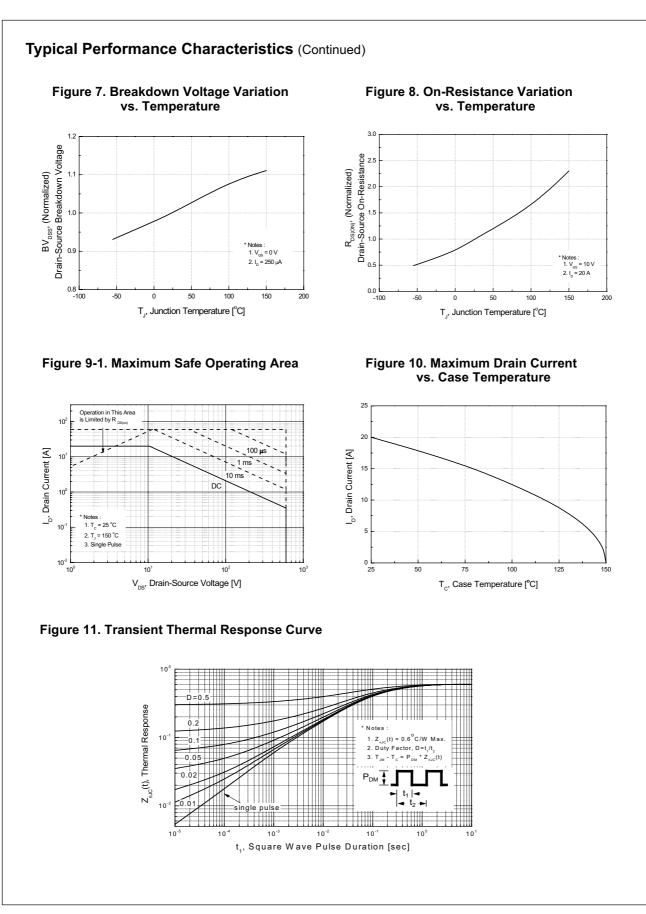
4. Pulse Test: Pulse width $\leq 300 \mu s,$ Duty Cycle $\leq 2\%$

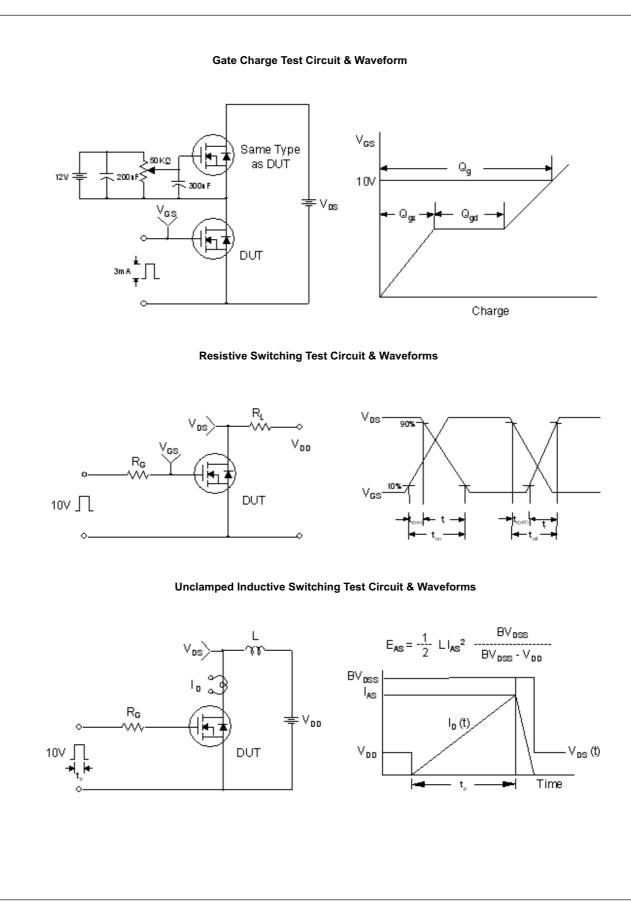
5. Essentially Independent of Operating Temperature Typical Characteristics

FCB20N60F 600V N-CHANNEL FRFET



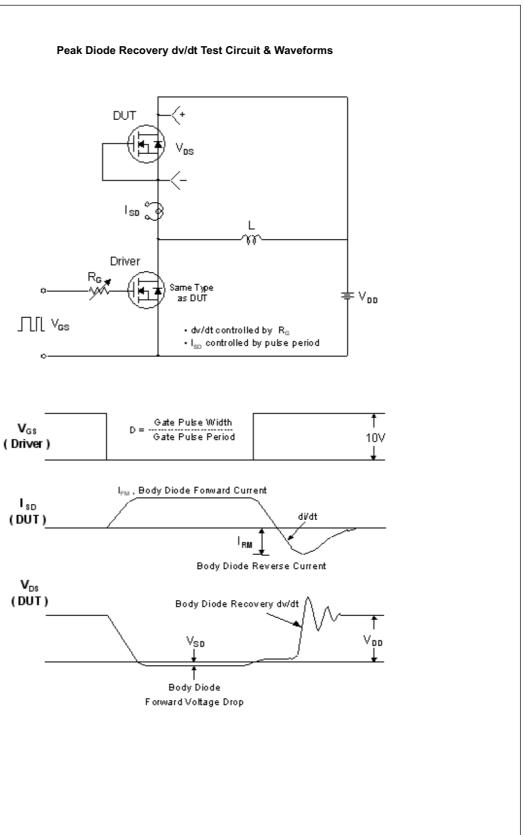
FCB20N60F 600V N-CHANNEL FRFET





FCB20N60F Rev. A4

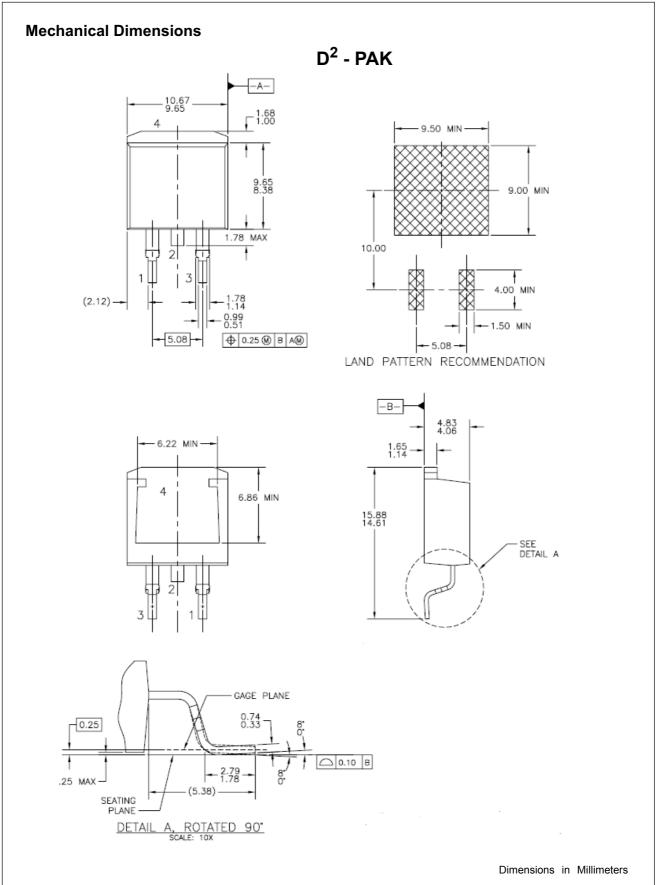
www.fairchildsemi.com



FCB20N60F Rev. A4

I_{SD}

 V_{DS}



www.fairchildsemi.com



SEMICONDUCTOR

TRADEMARKS

The following includes registered and unregistered trademarks and service marks, owned by Fairchild Semiconductor and/or its global subsidiaries, and is not intended to be an exhaustive list of all such trademarks.

FRFFT® Build it Now™ Global Power ResourceSM CorePLUS™ CorePOWER™ Green FPS™ Green FPS™ e-Series™ CROSSVOLT™ CTI ™ GTO™ IntelliMAX™ Current Transfer Logic™ EcoSPARK[®] ISOPI ANARTM EfficentMax™ MegaBuck[™] EZSWITCH™ * MICROCOUPLER™ MicroFET™ MicroPak™ MillerDrive™ MotionMax™ airchild® Motion-SPM™ Fairchild Semiconductor® OPTOLOGIC[®] OPTOPLANAR[®] FACT Quiet Series™ FACT® FAST® FastvCore™ PDP SPM™ FlashWriter[®] * Power-SPM™ **FPSTM** PowerTrench[®] F-PFS™ PowerXS™

Programmable Active Droop™ QFET QS™ Quiet Series™ RapidConfigure™ Saving our world, 1mW /W /kW at a time™ SmartMax™ SMART START™ SPM[®] STEALTH™ SuperFET™ SuperSOT™-3 SuperSOT™-6 SuperSOT™-8 SupreMOS™

Uwer p we franchise TinyBoost™ TinyBuck™ TinyLogic® TINYOPTO™ TinyPower™ TinyPWM™ TinyWire™ μSerDes™

UHC® Ultra FRFET™ UniFET™ **VCX™** VisualMax™ XS™

* EZSWITCH™ and FlashWriter[®] are trademarks of System General Corporation, used under license by Fairchild Semiconductor.

DISCLAIMER

FAIRCHILD SEMICONDUCTOR RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION, OR DESIGN. FAIRCHILD DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS. THESE SPECIFICATIONS DO NOT EXPAND THE TERMS OF FAIRCHILD'S WORLDWIDE TERMS AND CONDITIONS, SPECIFICALLY THE WARRANTY THEREIN, WHICH COVERS THESE PRODUCTS.

SyncFET™

The Power Franchise[®]

LIFE SUPPORT POLICY

FAIRCHILD'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF FAIRCHILD SEMICONDUCTOR CORPORATION.

As used herein:

- Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body or (b) support or sustain life, and (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in a significant injury of the user.
- A critical component in any component of a life support, device, or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

ANTI-COUNTERFEITING POLICY

Fairchild Semiconductor Corporation's Anti-Counterfeiting Policy. Farichild's Anti-Counterfeiting Policy is also stated on our external website, www.fairchildsemi.com, under Sales Support.

Counterfeiting of semiconductor parts is a growing problem in the industry. All manufactures of semiconductor products are experiencing counterfeiting of their parts. Customers who inadvertently purchase counterfeit parts experience many problems such as loss of brand reputation, substandard performance, failed application, and increased cost of production and manufacturing delays. Fairchild is taking strong measures to protect ourselves and our customers from the proliferation of counterfeit parts. Farichild strongly encourages customers to purchase Farichild parts either directly from Fairchild or from Authorized Fairchild Distributors who are listed by country on our web page cited above. Products customers buy either from fairchild directly or from Authorized Fairchild Distributors are genuine parts, have full traceability, meet Fairchild's quality standards for handing and storage and provide access to Farichild's full range of up-to-date technical and product information. Fairchild and our Authorized Distributors will stand behind all warranties and will appropriately address and warranty issues that may arise. Fairchild will not provide any warranty coverage or other assistance for parts bought from Unauthorized Sources. Farichild is committed to combat this global problem and encourage our customers to do their part in stopping this practice by buying direct or from authorized distributors.

PRODUCT STATUS DEFINITIONS Definition of Terms

| Datasheet Identification | Product Status | Definition | | | |
|--------------------------|-----------------------|-----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|--|--|--|
| Advance Information | Formative / In Design | Datasheet contains the design specifications for product development. Specifications may change in any manner without notice. | | | |
| Preliminary | First Production | Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design. | | | |
| No Identification Needed | Full Production | Datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve the design. | | | |
| Obsolete | Not In Production | Datasheet contains specifications on a product that is discontinued by Fairchild Semiconductor. The datasheet is for reference information only. | | | |
| | • | Rev. | | | |