

April 2011

FDPF190N15A

N-Channel PowerTrench® MOSFET 150V, 27.4A, 19mΩ

Features

- $R_{DS(on)}$ = 14.7m Ω (Typ.)@ V_{GS} = 10V, I_D = 27.4A
- Low Gate Charge (Typ. 30nC)
- Low C_{rss} (Typ. 56pF)
- Fast Switching
- 100% Avalanche Tested
- · Improved dv/dt Capability
- · RoHS Compliant

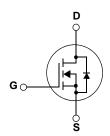
Description

This N-Channel MOSFET is produced using Fairchild Semiconductor's advance PowerTrench process that has been especially tailored to minimize the on-state resistance and yet maintain superior switching performance.

Application

- · DC to DC Converters
- Synchronous Rectification for Server/Telecom PSU
- · Battery Charger
- · AC motor drives and Uninterruptible Power Supplies
- Off-line UPS





MOSFET Maximum Ratings T_C = 25°C unless otherwise noted*

Symbol		Parameter		Rating	Units	
V _{DSS}	Drain to Source Voltage	o Source Voltage			V	
V _{GSS}	Gate to Source Voltage			±20	V	
1	Drain Current	- Continuous (T _C = 25°C)		27.4	^	
I _D	Drain Current	- Continuous (T _C = 100°C)		17.4	Α	
I _{DM}	Drain Current	- Pulsed	(Note 1)	110	Α	
E _{AS}	Single Pulsed Avalanche En	nergy	(Note 2)	261	mJ	
dv/dt	Peak Diode Recovery dv/dt		(Note 3)	6.0	V/ns	
D	Davisa Dissipation	$(T_C = 25^{\circ}C)$		33	W	
P_{D}	Power Dissipation	- Derate above 25°C		0.26	W/°C	
T _J , T _{STG}	Operating and Storage Tem	perature Range		-55 to +150	°C	
T _L	Maximum Lead Temperature 1/8" from Case for 5 Second	• •		300	°C	

Thermal Characteristics

Symbol	Parameter	Rating	Units
$R_{\theta JC}$	Thermal Resistance, Junction to Case	3.8	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	62.5	*C/VV

Package Marking and Ordering Information

Device Marking	Device	Package	Reel Size	Tape Width	Quantity
FDPF190N15A	FDPF190N15A	TO-220F	=	=	50

Electrical Characteristics $T_C = 25^{\circ}C$ unless otherwise noted

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Units
Off Charac	cteristics					
BV _{DSS}	Drain to Source Breakdown Voltage	$I_D = 250 \mu A, V_{GS} = 0 V$	150	-	-	V
$\frac{\Delta BV_{DSS}}{\Delta T_{J}}$	Breakdown Voltage Temperature Coefficient	I _D = 250μA, Referenced to 25°C	-	0.14	-	V/°C
ı	Zero Gate Voltage Drain Current	V _{DS} = 120V, V _{GS} = 0V	-	-	1	
IDSS	Zero Gate voltage Drain Current	$V_{DS} = 120V, T_{C} = 150^{\circ}C$	-	-	500	μΑ
I _{GSS}	Gate to Body Leakage Current	$V_{GS} = \pm 20V, V_{DS} = 0V$	-	-	±100	nA

On Characteristics

V _{GS(th)}	Gate Threshold Voltage	$V_{GS} = V_{DS}, I_{D} = 250 \mu A$	2.0	-	4.0	V
R _{DS(on)}	Static Drain to Source On Resistance	$V_{GS} = 10V, I_D = 27.4A$	-	14.7	19.0	$m\Omega$
9 _{FS}	Forward Transconductance	$V_{DS} = 10V, I_D = 27.4A$ (Note 4)	-	64	-	S

Dynamic Characteristics

C _{iss}	Input Capacitance	V 05V V 0V	-	2020	2685	pF
C _{oss}	Output Capacitance	$V_{DS} = 25V, V_{GS} = 0V$ f = 1MHz	-	700	930	pF
C _{rss}	Reverse Transfer Capacitance	1 - 1101112	-	56	85	pF
C _{oss(er)}	Energy Related Output Capacitance	V _{DS} = 75V, V _{GS} = 0V	-	252	-	pF
Q _{g(tot)}	Total Gate Charge at 10V	V 400V ID 07.44	-	30	39	nC
Q_{gs}	Gate to Source Gate Charge	V _{DS} = 120V, ID = 27.4A 	-	8.8	-	nC
Q_{gd}	Gate to Drain "Miller" Charge	VGS - 10 V	-	7.3	-	nC

Switching Characteristics

t _{d(on)}	Turn-On Delay Time		-	18	46	ns
t _r	Turn-On Rise Time	$V_{DD} = 75V, I_D = 27.4A$	-	16	42	ns
t _{d(off)}	Turn-Off Delay Time	$V_{GS} = 10V, R_{GEN} = 4.7\Omega$	-	32	74	ns
t _f	Turn-Off Fall Time	(Note 4, 5)	-	8	26	ns
ESR	Equivalent Series Resistance (G-S)	f = 1MHz	-	1.5	-	Ω

Drain-Source Diode Characteristics

I _S	Maximum Continuous Drain to Source Diode Forward Current		-	-	27.4	Α
I _{SM}	Maximum Pulsed Drain to Source Diode Forward Current		-	-	110	Α
V_{SD}	Drain to Source Diode Forward Voltage V ₀	_{GS} = 0V, I _{SD} = 27.4A	-	-	1.3	V
t _{rr}	Reverse Recovery Time V ₀	_{GS} = 0V, I _{SD} = 27.4A	-	76	-	ns
Q_{rr}	Reverse Recovery Charge dl	$_{F}/dt = 100A/\mu s$, $V_{DD} = 120V(Note 4)$	-	0.18	-	μC

- **Notes:**1. Repetitive Rating: Pulse width limited by maximum junction temperature
- 2. L = 3mH, I_{AS} = 13.2A, R_G = 25 Ω , Starting T_J = 25 $^{\circ}C$
- 3. $I_{SD} \le 27.4 A$, di/dt $\le 200 A/\mu s$, $V_{DD} \le BV_{DSS}$, Starting $T_J = 25^{\circ}C$
- 4. Pulse Test: Pulse width $\leq 300 \mu s, \, \text{Dual Cycle} \leq 2\%$
- 5. Essentially Independent of Operating Temperature Typical Characteristics

Typical Performance Characteristics

Figure 1. On-Region Characteristics

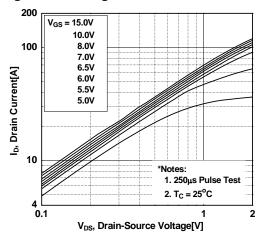


Figure 3. On-Resistance Variation vs.

Drain Current and Gate Voltage

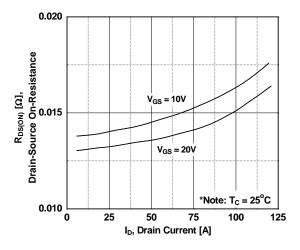


Figure 5. Capacitance Characteristics

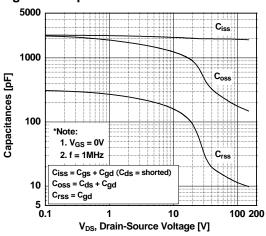


Figure 2. Transfer Characteristics

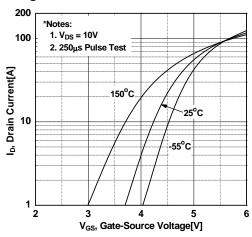


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature

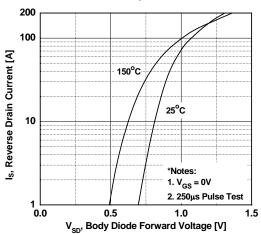
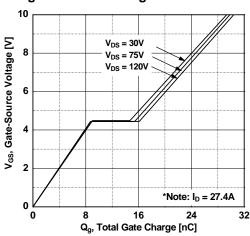


Figure 6. Gate Charge Characteristics



Typical Performance Characteristics (Continued)

Figure 7. Breakdown Voltage Variation vs. Temperature

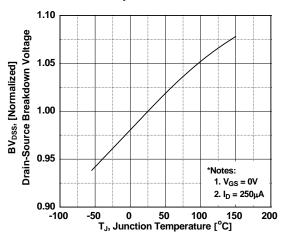


Figure 9. Maximum Safe Operating Area vs. Case Temperature

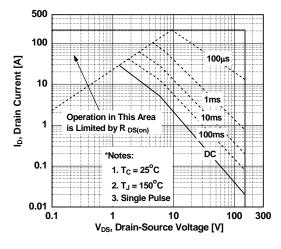


Figure 11. Eoss vs. Drain to Source Voltage

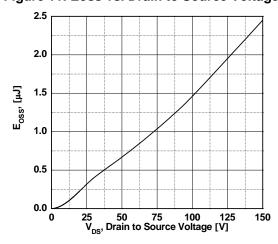


Figure 8. On-Resistance Variation vs. Temperature

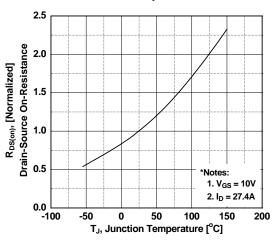
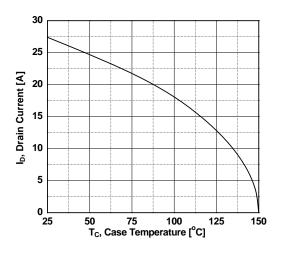
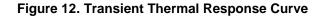
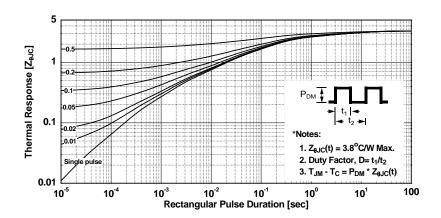


Figure 10. Maximum Drain Current

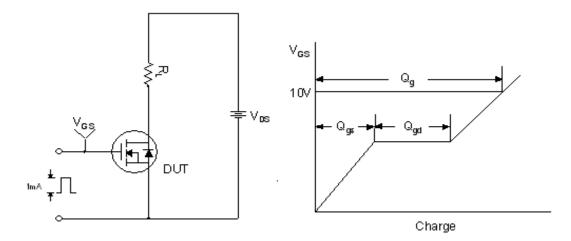


Typical Performance Characteristics (Continued)

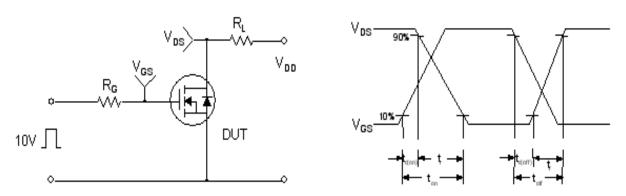




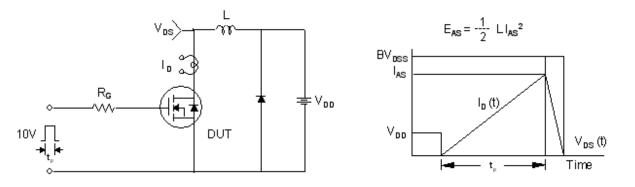
Gate Charge Test Circuit & Waveform



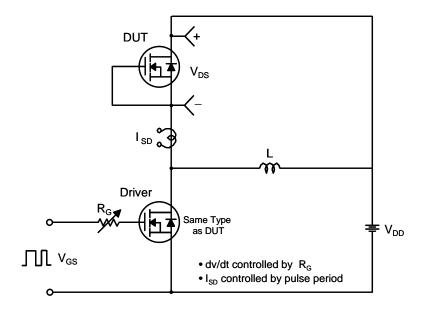
Resistive Switching Test Circuit & Waveforms

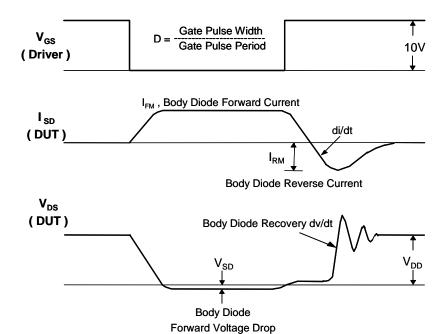


Unclamped Inductive Switching Test Circuit & Waveforms



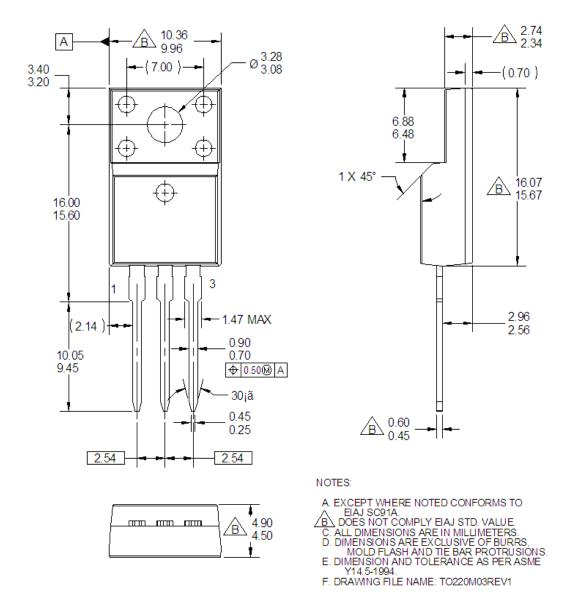
Peak Diode Recovery dv/dt Test Circuit & Waveforms





Package Dimensions

TO-220F (Retractable)



* Front/Back Side Isolation Voltage: AC 2500V

Dimensions in Millimeters





TRADEMARKS

The following includes registered and unregistered trademarks and service marks, owned by Fairchild Semiconductor and/or its global subsidiaries, and is not intended to be an exhaustive list of all such trademarks.

AccuPower™ Auto-SPM™ AX-CAPTM* BitSiC[®] Build it Now™ CorePLUS™ CorePOWER™ CROSSVOLT™

CTL™ Current Transfer Logic™ DEUXPEED® Dual Cool™ EcoSPARK[®] EfficentMax™ ESBC™

Fairchild[®]

Fairchild Semiconductor® FACT Quiet Series™ FACT[®]

FAST® FastvCore[™] FETBench™ FlashWriter® * FPS™ F-PFS™ FRFET®

Global Power ResourceSM Green FPS™

Green FPS™ e-Series™ $\mathsf{G} max^{\mathsf{TM}}$ GTO™

IntelliMAX™ ISOPLANAR™ MegaBuck™ MIČROCOUPLER™ MicroFET™ MicroPak™ MicroPak2™ MillerDrive™ MotionMax™ Motion-SPM™ mWSaver™ OptiHiT™ OPTOLOGIC®

PDP SPM™

OPTOPLANAR®

Power-SPM™ PowerTrench® PowerXSTM

Programmable Active Droop™ OFFT

QSTM

Quiet Series™ RapidConfigure™

Saving our world, 1mW/W/kW at a time™ SignalWise™ SmartMax™ SMART START™ SPM® STEALTH™

SuperFET[®] SuperSOT™-3 SuperSOT™-6 SuperSOT™-8 SupreMOS® SyncFET™ Sync-Lock™

SYSTEM®* **G**ENERAL

The Power Franchise®

The Right Technology for Your Success™

bwer franchise TinyBoost™ TinyBuck™ TinyCalc™ TinyLogic[®] TINYOPTO™ TinyPower™ TinyPWM™ TinyWire™ TranSiC[®] TriFault Detect™ TRUECURRENT®*

UHC[®] Ultra FRFET™ UniFET™ VCX™ VisualMax™ XSTM

uSerDes™

*Trademarks of System General Corporation, used under license by Fairchild Semiconductor.

DISCLAIMER

FAIRCHILD SEMICONDUCTOR RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION, OR DESIGN. FAIRCHILD DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS. THESE SPECIFICATIONS DO NOT EXPAND THE TERMS OF FAIRCHILD'S WORLDWIDE TERMS AND CONDITIONS, SPECIFICALLY THE WARRANTY THEREIN, WHICH COVERS THESE PRODUCTS.

LIFE SUPPORT POLICYFAIRCHILD'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF FAIRCHILD SEMICONDUCTOR CORPORATION.

As used here in:

- Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body or (b) support or sustain life, and (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in a significant injury of the user.
- A critical component in any component of a life support, device, or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

ANTI-COUNTERFEITING POLICY

Fairchild Semiconductor Corporation's Anti-Counterfeiting Policy. Fairchild's Anti-Counterfeiting Policy is also stated on our external website, www.Fairchildsemi.com, under Sales Support.

Counterfeiting of semiconductor parts is a growing problem in the industry. All manufactures of semiconductor products are experiencing counterfeiting of their parts. Customers who inadvertently purchase counterfeit parts experience many problems such as loss of brand reputation, substandard performance, failed application, and increased cost of production and manufacturing delays. Fairchild is taking strong measures to protect ourselves and our customers from the proliferation of counterfeit parts. Fairchild strongly encourages customers to purchase Fairchild parts either directly from Fairchild or from Authorized Fairchild Distributors who are listed by country on our web page cited above. Products customers buy either from Fairchild directly or from Authorized Fairchild Distributors are genuine parts, have full traceability, meet Fairchild's quality standards for handing and storage and provide access to Fairchild's full range of up-to-date technical and product information. Fairchild and our Authorized Distributors will stand behind all warranties and will appropriately address and warranty issues that may arise. Fairchild will not provide any warranty coverage or other assistance for parts bought from Unauthorized Sources. Fairchild is committed to combat this global problem and encourage our customers to do their part in stopping this practice by buying direct or from authorized distributors.

PRODUCT STATUS DEFINITIONS **Definition of Terms**

Datasheet Identification	Product Status	Definition
Advance Information Formative / In Design		Datasheet contains the design specifications for product development. Specifications may change in any manner without notice.
Preliminary	First Production	Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.
No Identification Needed	Full Production	Datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve the design.
Obsolete	Not In Production	Datasheet contains specifications on a product that is discontinued by Fairchild Semiconductor. The datasheet is for reference information only.