TOSHIBA INSULATED GATE BIPOLAR TRANSISTOR SILICON N CHANNEL IGBT

GT20J301

HIGH POWER SWITCHING APPLICATIONS MOTOR CONTROL APPLICATIONS

• The 3rd Generation

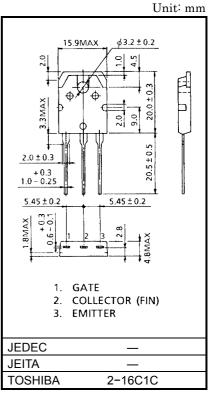
• Enhancement-Mode

• High Speed : $t_f = 0.30 \mu s$ (Max.) • Low Saturation Voltage : $V_{CE (sat)} = 2.7 V$ (Max.)

• FRD included between Emitter and Collector

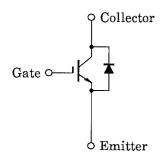
MAXIMUM RATINGS (Ta = 25°C)

CHARACTERISTIC		SYMBOL	RATING	UNIT	
Collector-Emitter Voltage		V _{CES}	600	٧	
Gate-Emitter Voltage		V_{GES}	±20	V	
Collector Current	DC	Ic	20	Α	
	1ms	I _{CP}	40	Α	
Emitter-Collector Forward Current	DC	I _F	20	Α	
	1ms	I _{FM}	40	Α	
Collector Power Dissipation (Tc = 25°C)		P _C	130	W	
Junction Temperature		Tj	150	°C	
Storage Temperature Range		T _{stg}	-55~150	°C	



Weight: 4.6g

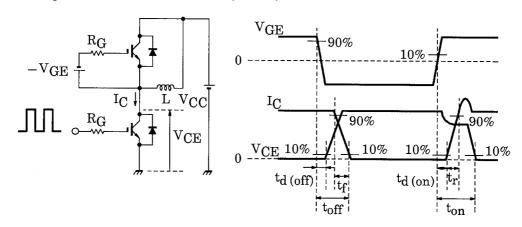
EQUIVALENT CIRCUIT



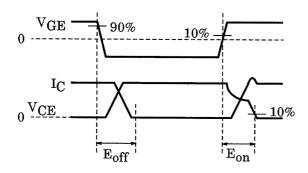
ELECTRICAL CHARACTERISTICS (Ta = 25°C)

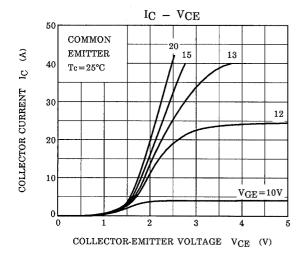
CHARACTERISTIC		SYMBOL	TEST CONDITION	MIN	TYP.	MAX	UNIT
Gate Leakage Current		I _{GES}	V _{GE} = ±20V, V _{CE} = 0	_	_	±500	nA
Collector Cut-Off Current		I _{CES}	V _{CE} = 600V, V _{GE} = 0	-	_	1.0	mA
Gate-Emitter Cut-Off Voltage		V _{GE} (OFF)	I _C = 2mA, V _{CE} = 5V	5.0	_	8.0	V
Collector-Emitter Saturation Voltage		V _{CE} (sat)	I _C = 20A, V _{GE} = 15V	-	2.1	2.7	V
Input Capacitance		C _{ies}	V _{CE} = 20V, V _{GE} = 0, f = 1MHz	-	1450	_	pF
Switching Time	Rise Time	t _r	Inductive Load V_{CC} = 300V, I_{C} = 20A V_{GG} = ±15V, R_{G} = 56 Ω (Note)	-	0.12	_	- μs
	Turn-On Time	t _{on}		-	0.40	_	
	Fall Time	t _f		-	0.15	0.30	
	Turn-Off Time	t _{off}		-	0.70	_	
Peak Forward Voltage		V _F	I _F = 20A, V _{GE} = 0	-	_	2.0	V
Reverse Recovery Time		t _{rr}	I _F = 20A, di / dt = -100A / μs	-	_	200	ns
Thermal Resistance (IGBT)		R _{th (j-c)}	_	_	_	0.96	°C/W
Thermal Resistance (Diode)		R _{th (j-c)}	_	1	_	2.5	°C/W

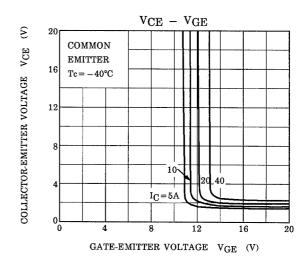
Note: Switching time measurement circuit and input / output waveforms

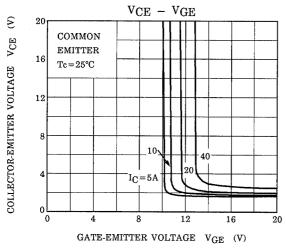


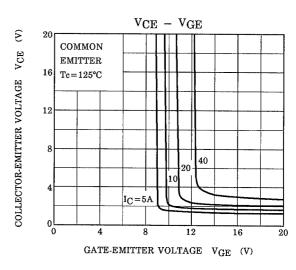
Switching loss measurement waveforms

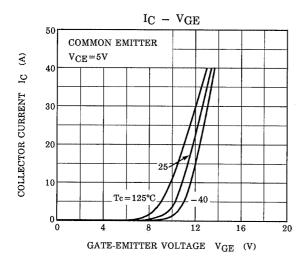


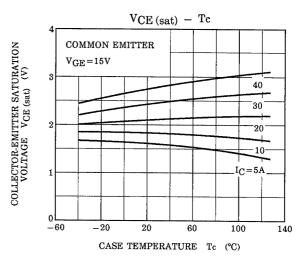


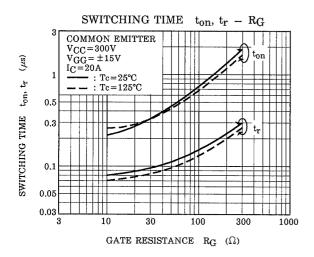


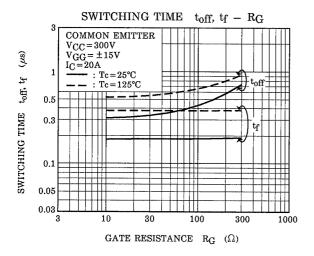


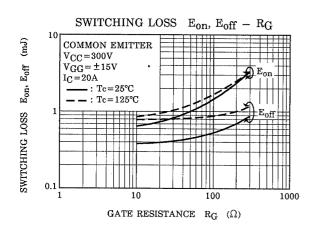


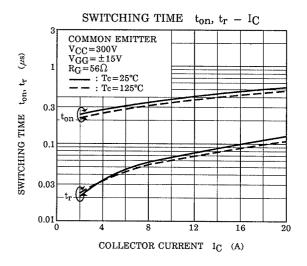


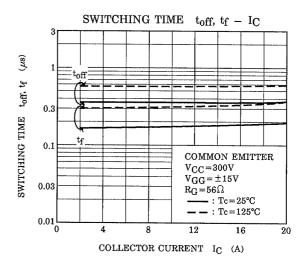


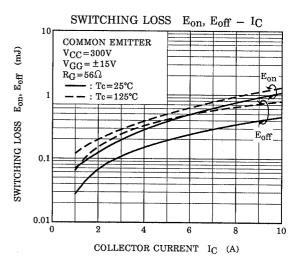


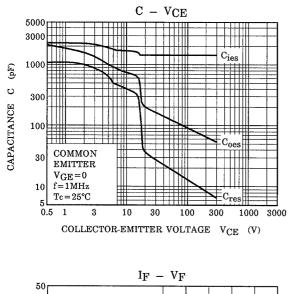


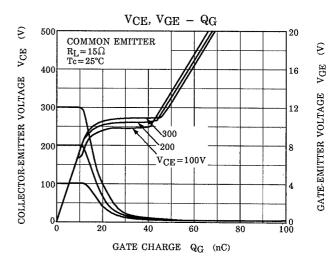


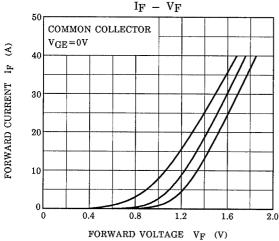


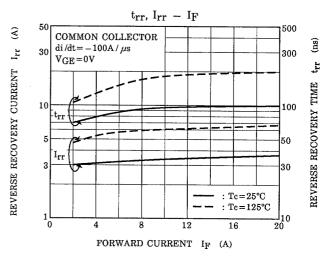


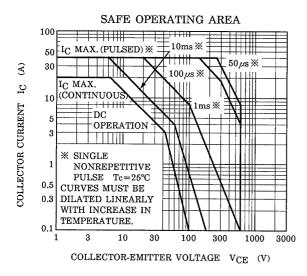


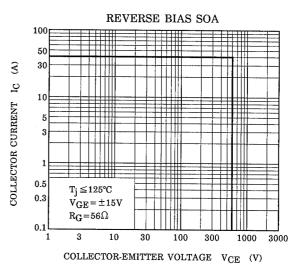


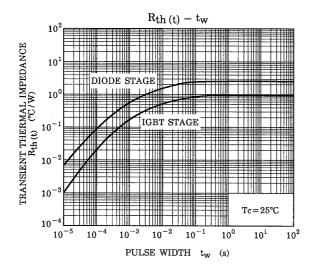












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