



SANYO Semiconductors

# DATA SHEET

An ON Semiconductor Company

## MCH6603 — P-Channel Silicon MOSFET — General-Purpose Switching Device Applications

### Features

- Low ON-resistance
- Ultrahigh-speed switching
- 1.5V drive
- Composite type with 2 MOSFETs contained in a single package, facilitating high-density mounting
- Halogen free compliance

### Specifications

#### Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	V <sub>DSS</sub>		-50	V
Gate-to-Source Voltage	V <sub>GSS</sub>		±10	V
Drain Current (DC)	I <sub>D</sub>		-0.14	A
Drain Current (Pulse)	I <sub>DP</sub>	PW≤10μs, duty cycles≤1%	-0.56	A
Allowable Power Dissipation	P <sub>D</sub>	When mounted on ceramic substrate (900mm <sup>2</sup> ×0.8mm)1unit	0.8	W
Channel Temperature	T <sub>ch</sub>		150	°C
Storage Temperature	T <sub>stg</sub>		-55 to +150	°C

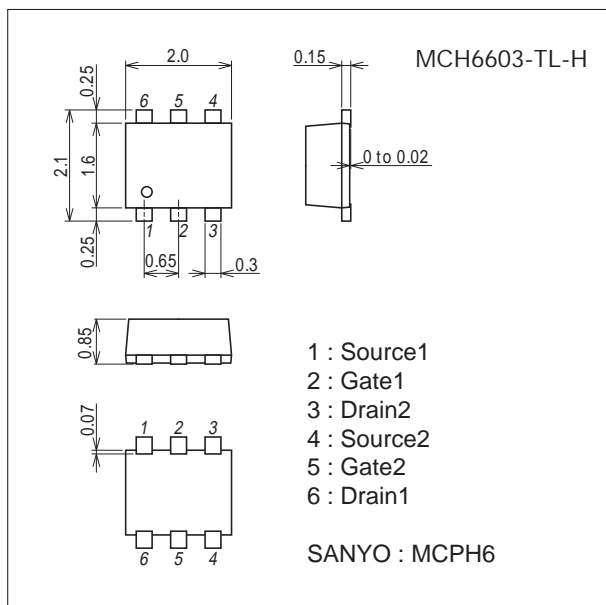
This product is designed to "ESD immunity < 200V\*\*", so please take care when handling.

\* Machine Model

### Package Dimensions

unit : mm (typ)

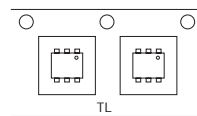
7022A-006



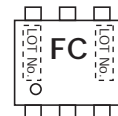
### Product & Package Information

- Package : MCPH6
- JEITA, JEDEC : SC-88, SC-70-6, SOT-363
- Minimum Packing Quantity : 3,000 pcs./reel

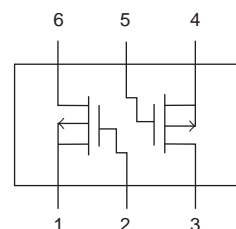
### Packing Type : TL



### Marking



### Electrical Connection

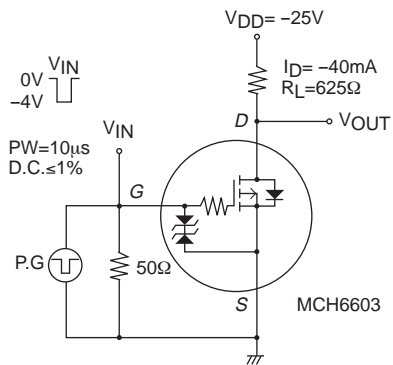


# MCH6603

## Electrical Characteristics at Ta=25°C

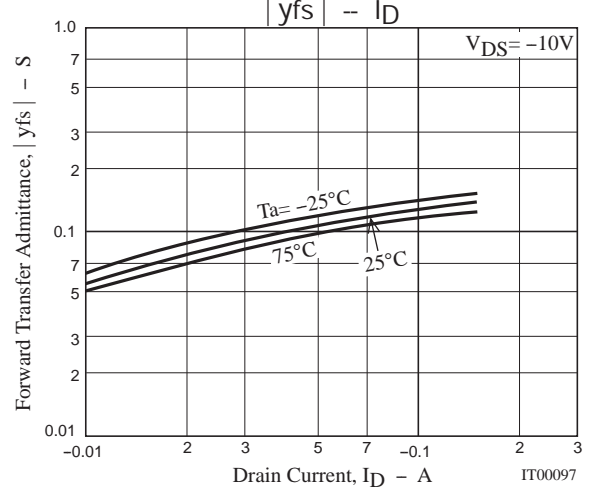
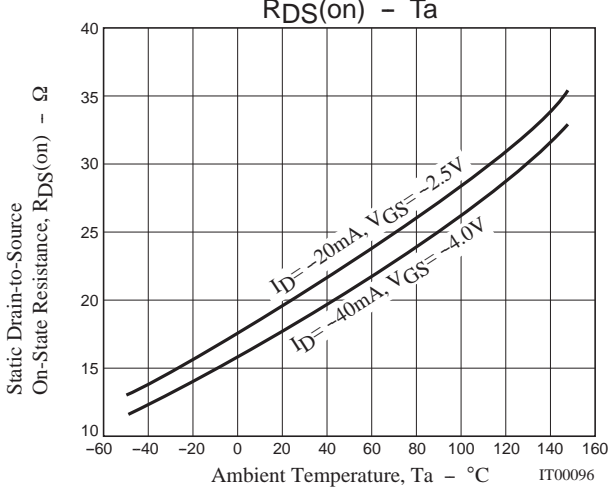
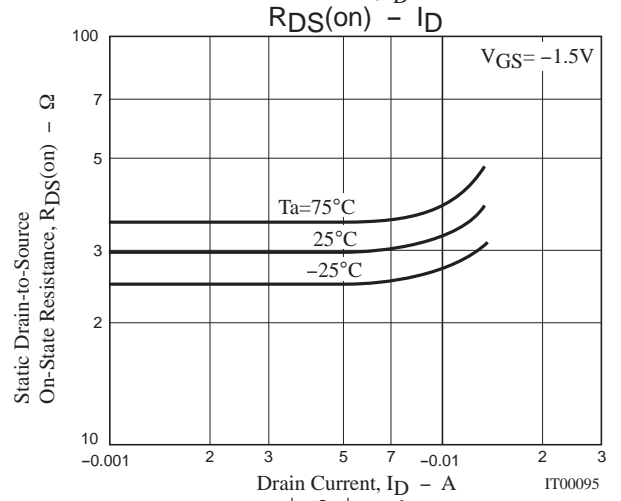
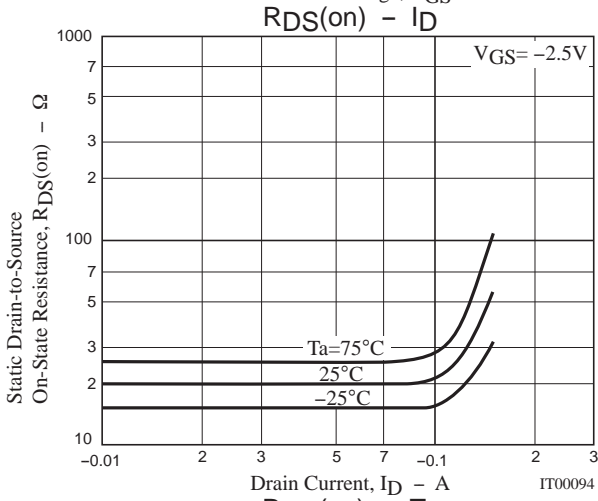
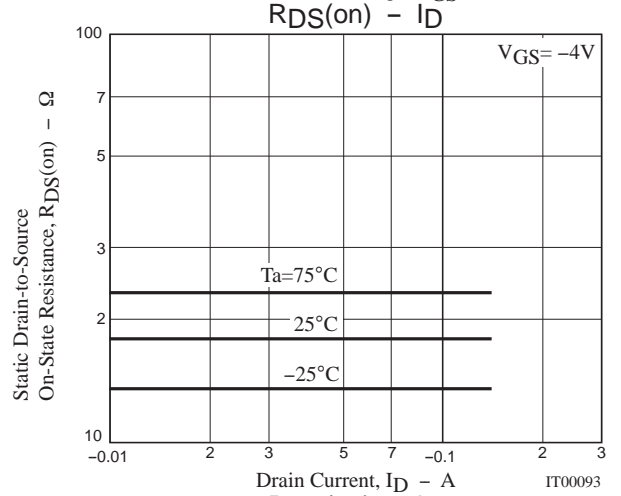
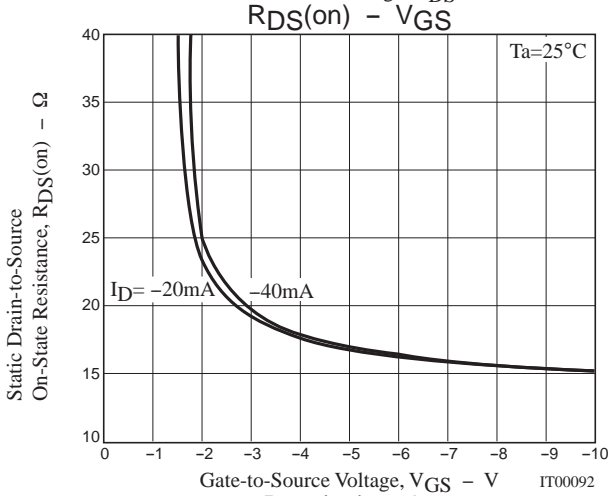
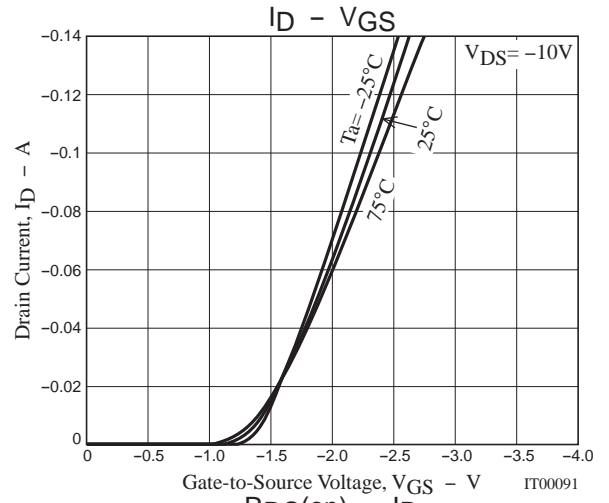
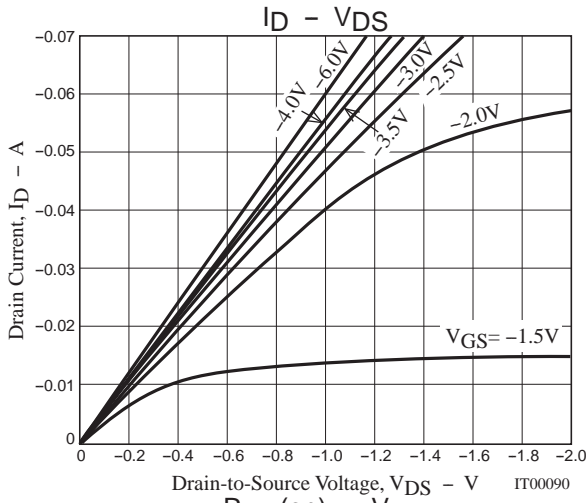
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Drain-to-Source Breakdown Voltage	V(BR)DSS	I <sub>D</sub> =-1mA, V <sub>GS</sub> =0V	-50			V
Zero-Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =-50V, V <sub>GS</sub> =0V			-1	μA
Gate-to-Source Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±8V, V <sub>DS</sub> =0V			±10	μA
Cutoff Voltage	V <sub>GS(off)</sub>	V <sub>DS</sub> =-10V, I <sub>D</sub> =-100μA	-0.4		-1.4	V
Forward Transfer Admittance	y <sub>fs</sub>	V <sub>DS</sub> =-10V, I <sub>D</sub> =-40mA	70	110		mS
Static Drain-to-Source On-State Resistance	R <sub>DS(on)1</sub>	I <sub>D</sub> =-40mA, V <sub>GS</sub> =-4V		18	23	Ω
	R <sub>DS(on)2</sub>	I <sub>D</sub> =-20mA, V <sub>GS</sub> =-2.5V		20	28	Ω
	R <sub>DS(on)3</sub>	I <sub>D</sub> =-5mA, V <sub>GS</sub> =-1.5V		30	60	Ω
Input Capacitance	C <sub>iss</sub>			7.4		pF
Output Capacitance	C <sub>oss</sub>	V <sub>DS</sub> =-10V, f=1MHz		4.2		pF
Reverse Transfer Capacitance	C <sub>rss</sub>			1.3		pF
Turn-ON Delay Time	t <sub>d(on)</sub>	See specified Test Circuit.		20		ns
Rise Time	t <sub>r</sub>			35		ns
Turn-OFF Delay Time	t <sub>d(off)</sub>			160		ns
Fall Time	t <sub>f</sub>			150		ns
Total Gate Charge	Q <sub>g</sub>				1.40	
Gate-to-Source Charge	Q <sub>gs</sub>	V <sub>DS</sub> =-10V, V <sub>GS</sub> =-10V, I <sub>D</sub> =-70mA		0.16		nC
Gate-to-Drain "Miller" Charge	Q <sub>gd</sub>			0.23		nC
Diode Forward Voltage	V <sub>SD</sub>		I <sub>S</sub> =-70mA, V <sub>GS</sub> =0V		-0.85	-1.2

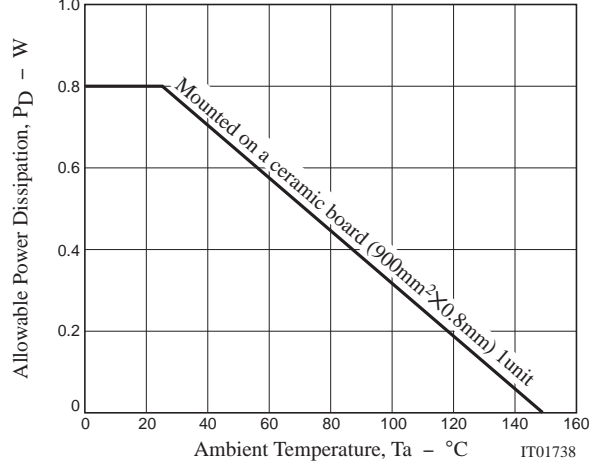
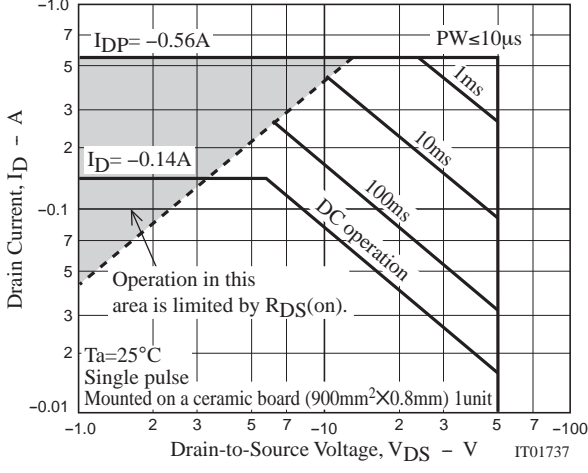
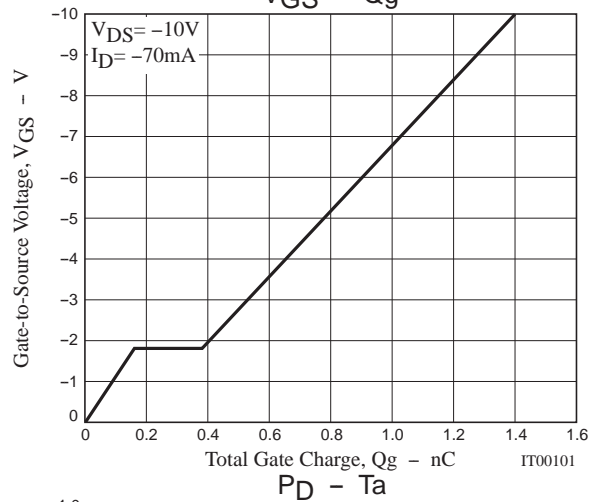
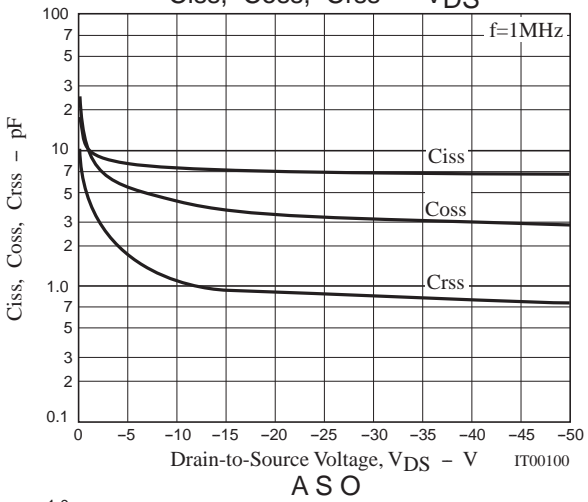
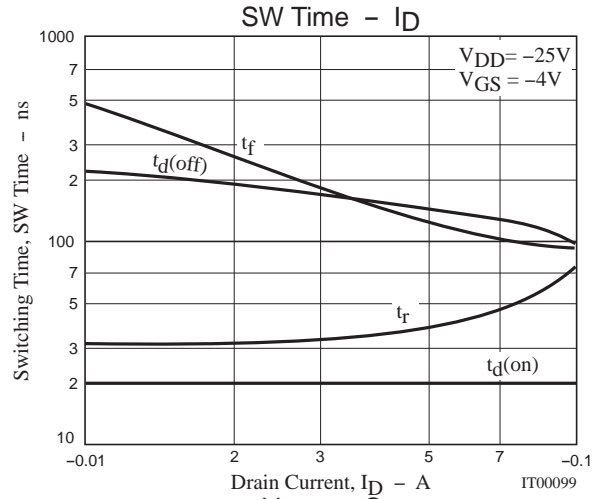
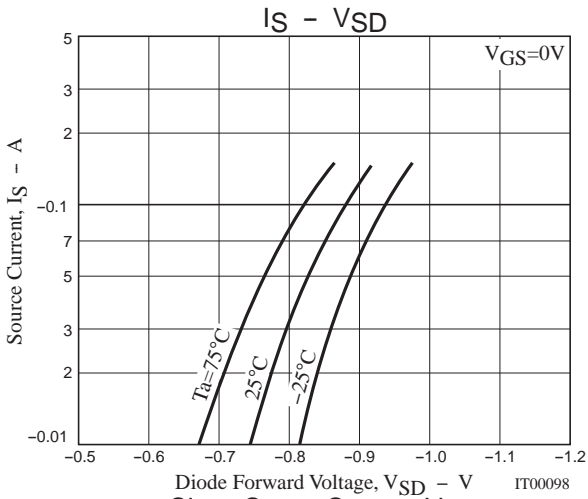
## Switching Time Test Circuit



## Ordering Information

Device	Package	Shipping	memo
MCH6603-TL-H	MCPH6	3,000pcs./reel	Pb Free and Halogen Free





Taping Specification

MCH6603-TL-H

1. Packing Format

Package Name	Carrier Tape Type	Maximum Number of devices contained (pcs)			Packing format	
		Reel	Inner box	Outer box	Inner BOX (C-1)	Outer BOX (A-7)
MCPH6	MCP4	3,000	15,000	90,000	5 reels contained Dimensions:mm (external) 183×72×185	6 inner boxes contained Dimensions:mm (external) 440×195×210

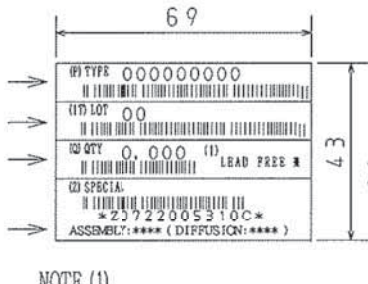
Packing method



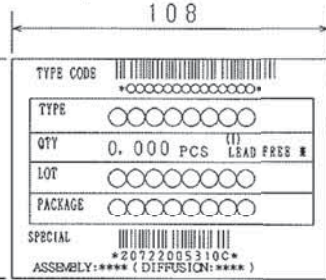
Type No.  
LOT No.  
Quantity  
Origin

Reel label

Reel label, Inner box label  
(unit:mm)



Outer box label  
(It is a label at the time of factory shipments. The form of a label may change in physical distribution process.)



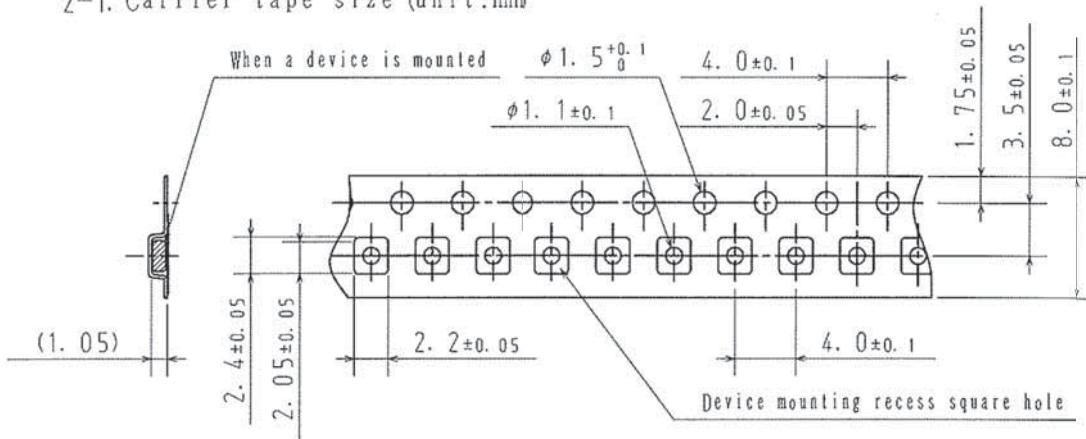
NOTE (1)

The LEAD FREE \* description shows that the surface treatment of the terminal is lead free.

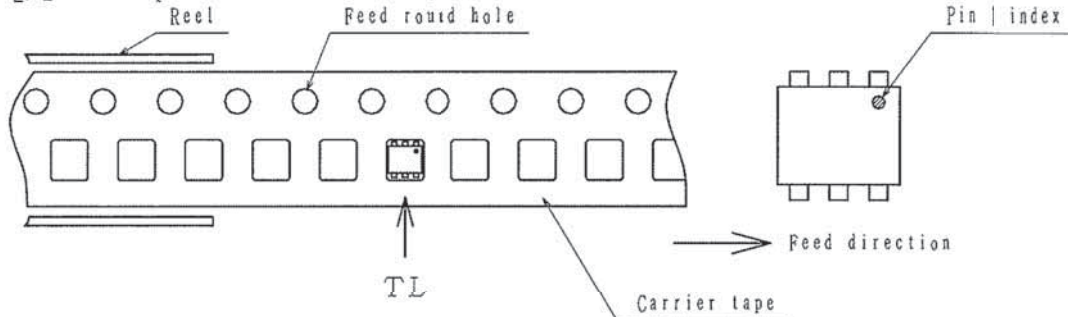
Label	JEITA Phase
LEAD FREE 3	JEITA Phase 3A
LEAD FREE 4	JEITA Phase 3

2. Taping configuration

2-1. Carrier tape size (unit:mm)



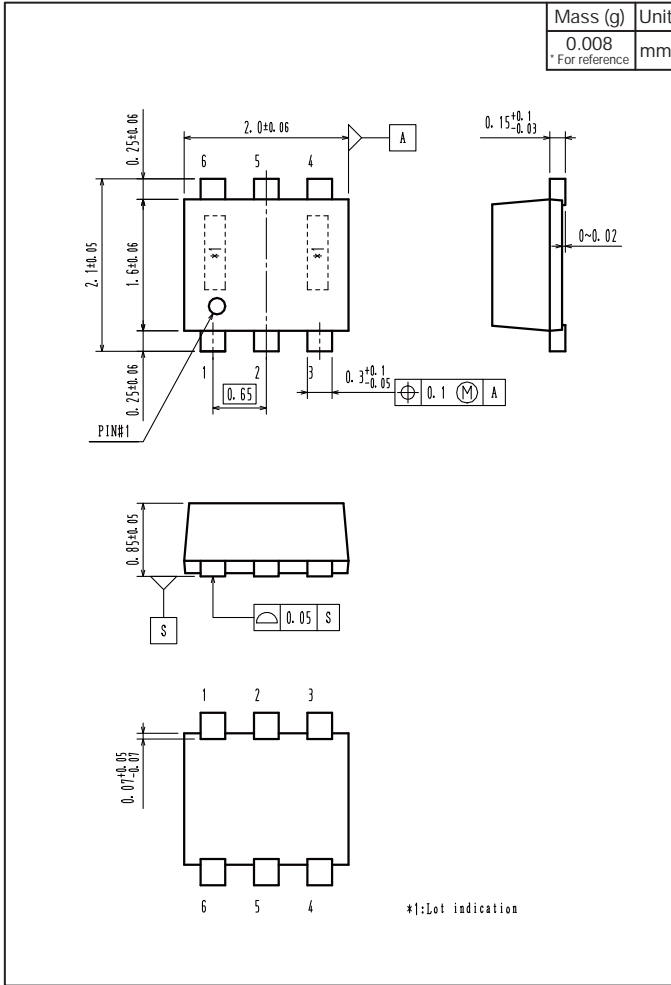
2-2. Device placement direction



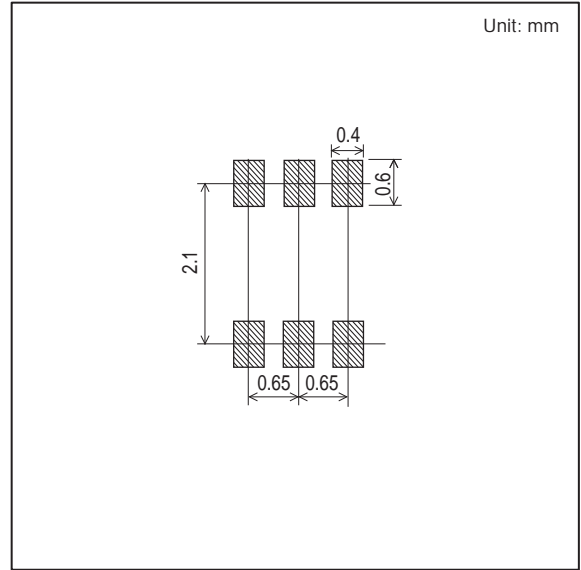
Those with pin | index on the feed hole side.....TL

# MCH6603

## Outline Drawing MCH6603-TL-H



## Land Pattern Example



Note on usage : Since the MCH6603 is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

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