

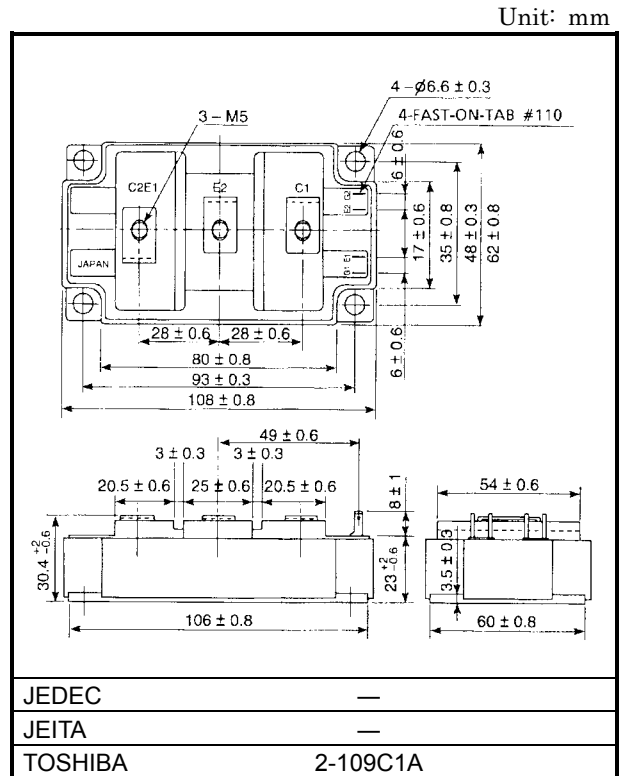
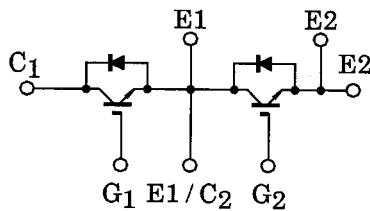
TOSHIBA GTR Module Silicon N Channel IGBT

MG90V2YS40

High Power Switching Applications
Motor Control Applications

- The electrodes are isolated from case.
- High input impedance
- Includes a complete half bridge in one package.
- Enhancement-mode
- High speed : $t_f = 1.5\mu s$ (max) ($I_C = 90A$)
 $t_{rr} = 0.3\mu s$ (max) ($I_F = 90A$)

Equivalent Circuit



Weight: 430g (typ.)

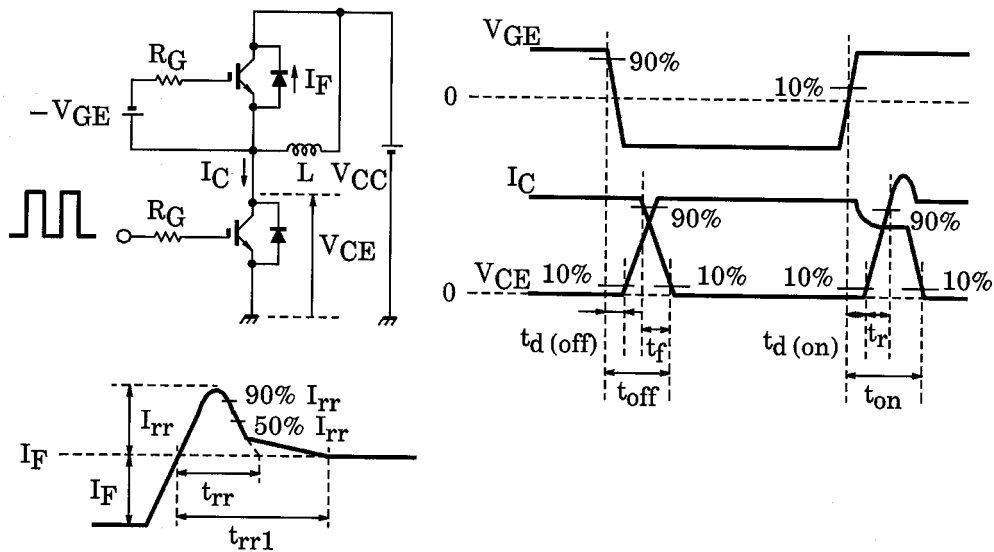
Maximum Ratings (Ta = 25°C)

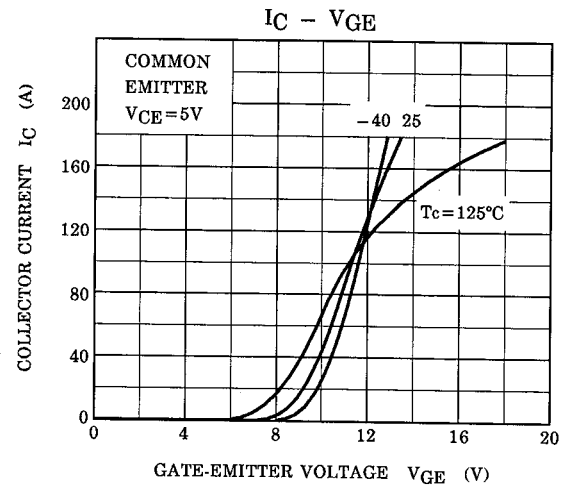
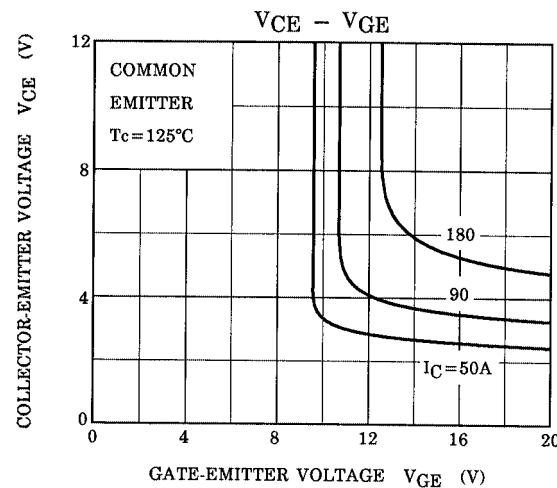
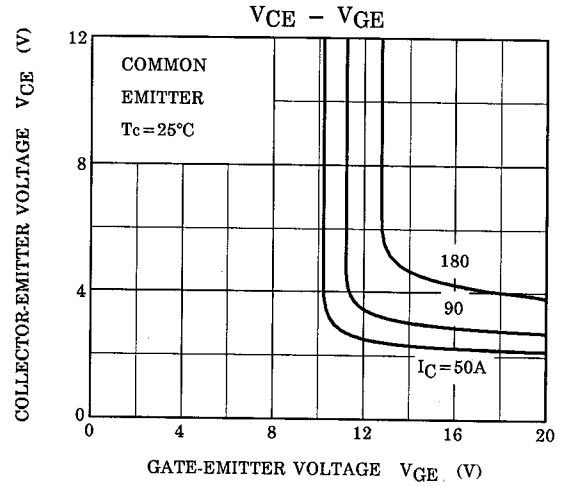
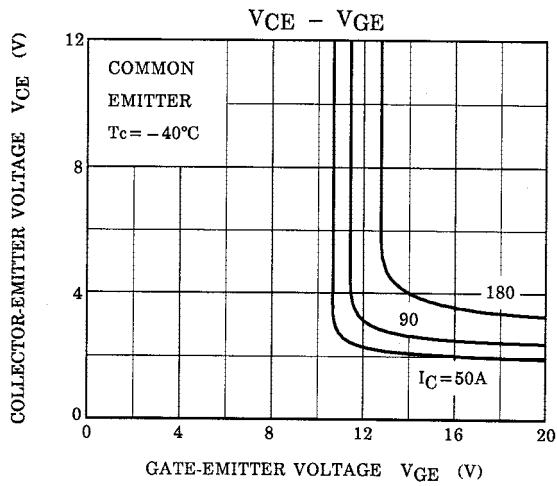
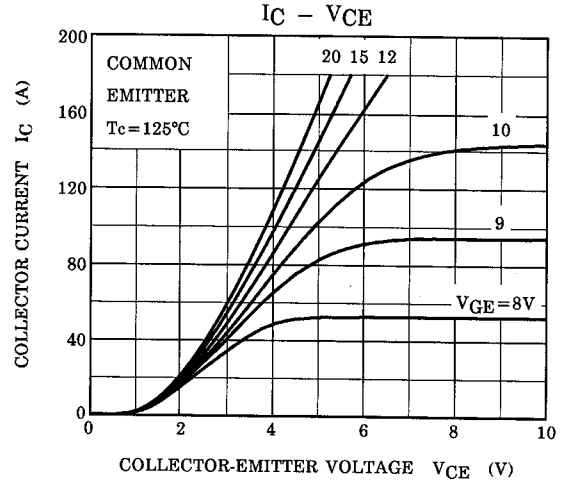
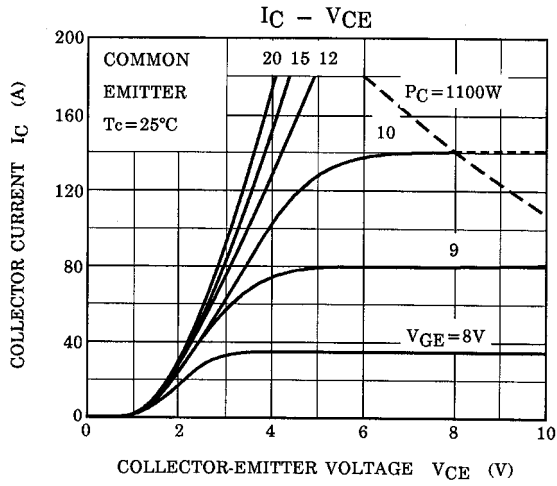
Characteristics		Symbol	Rating	Unit
Collector-emitter voltage		V_{CES}	1700	V
Gate-emitter voltage		V_{GES}	±20	V
Collector current	DC	I_C	90	A
	1ms	I_{CP}	180	
Forward current	DC	I_F	90	A
	1ms	I_{FM}	180	
Collector power dissipation ($T_c = 25^\circ C$)		P_C	1100	W
Junction temperature		T_j	150	°C
Storage temperature range		T_{stg}	-40 ~ 125	°C
Isolation voltage		V_{isol}	4000 (AC 1minute)	V
Screw torque (Terminal / mounting)		—	3 / 3	N·m

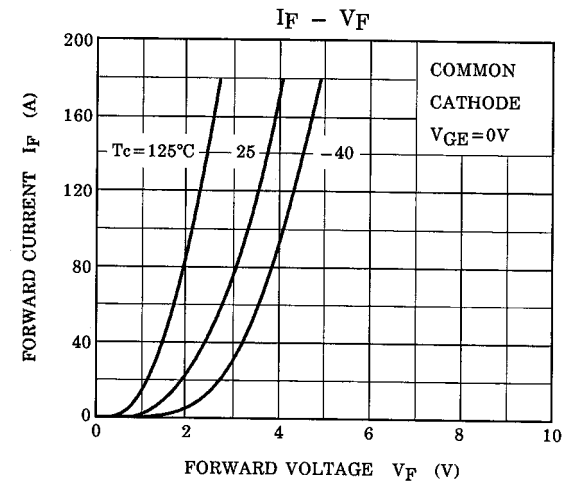
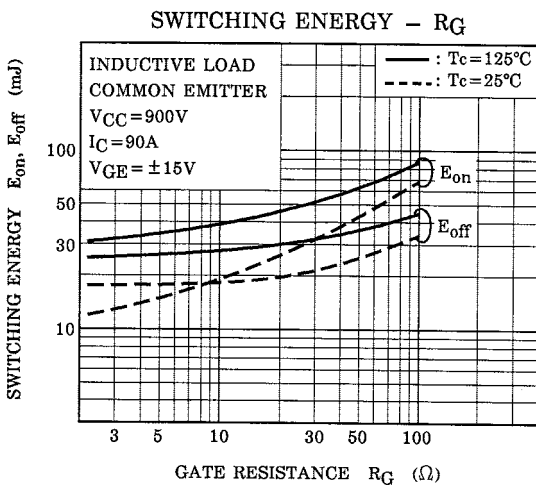
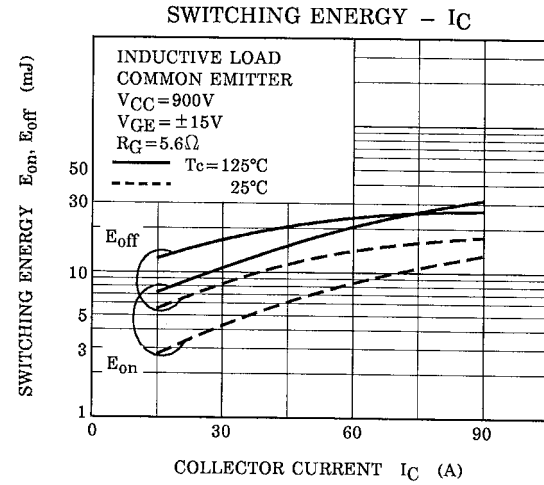
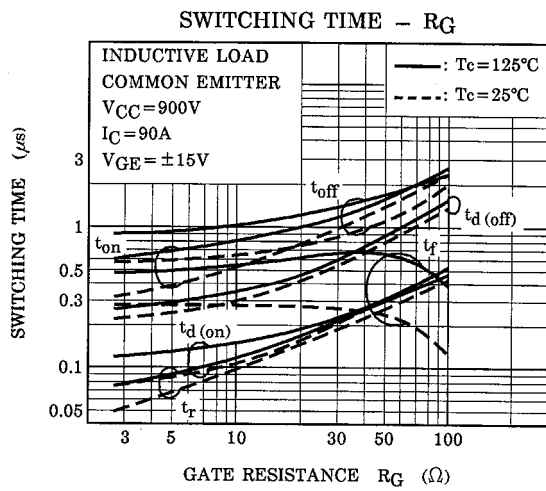
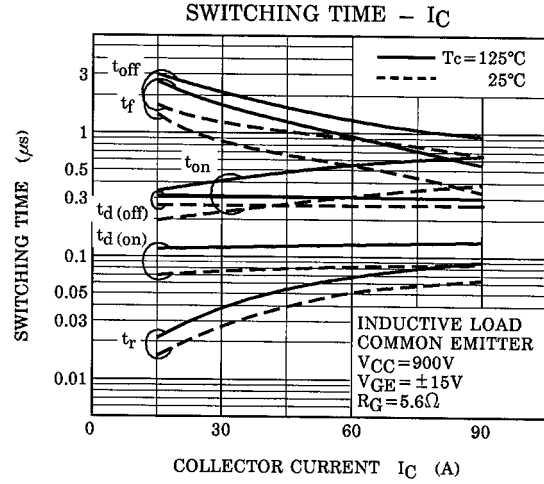
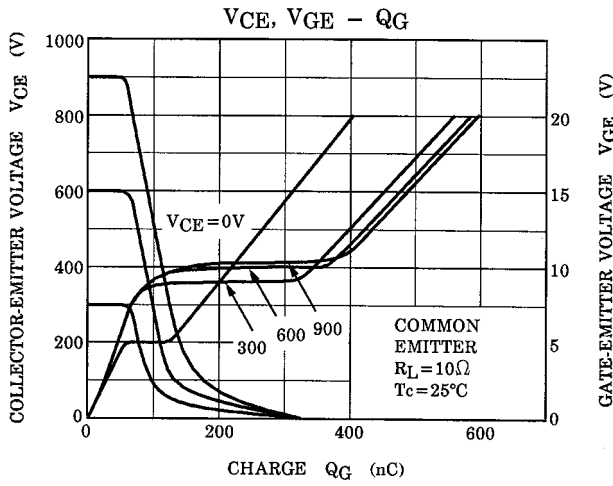
Electrical Characteristics (Ta = 25°C)

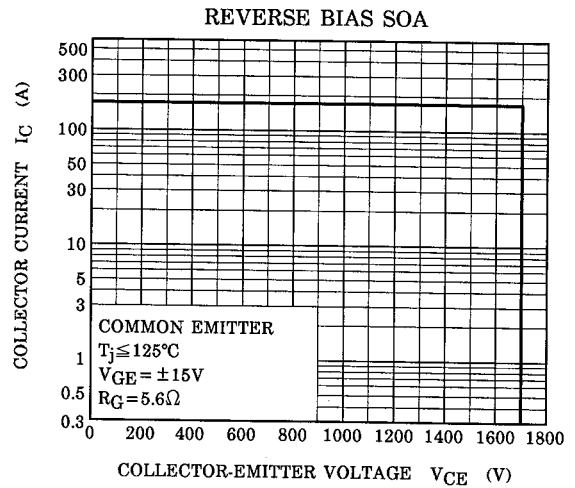
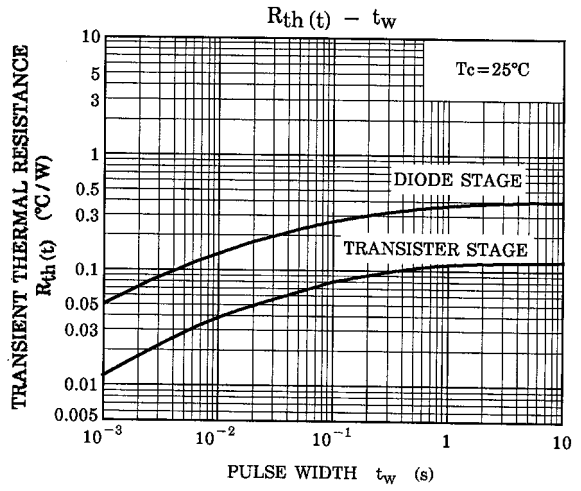
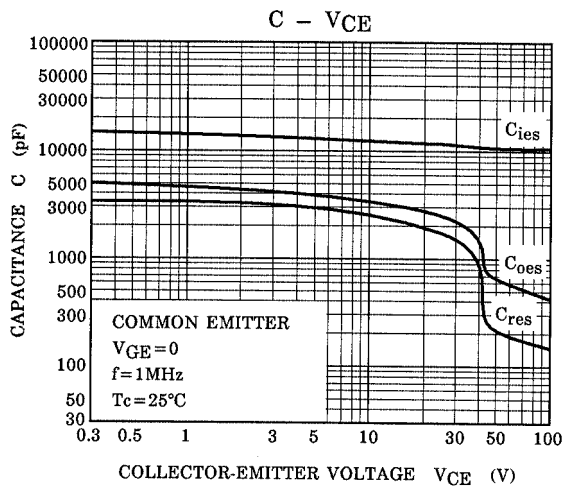
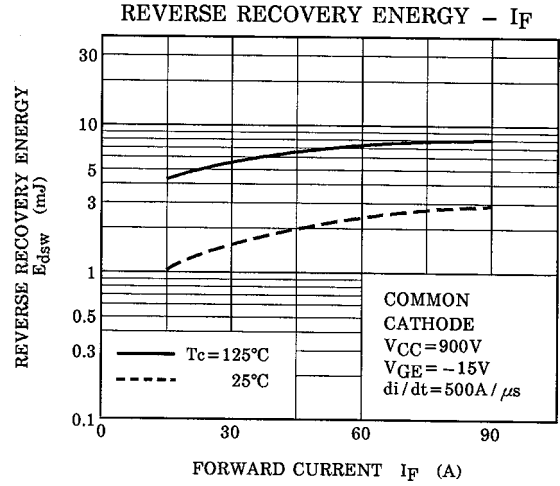
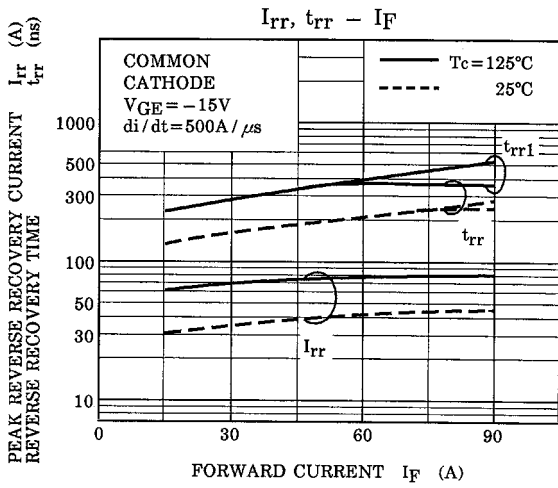
Characteristics		Symbol	Test Condition	Min	Typ.	Max	Unit
Gate leakage current		I_{GES}	$V_{GE} = \pm 20 \text{ V}, V_{CE} = 0$	—	—	± 100	nA
Collector cut-off current		I_{CES}	$V_{CE} = 1700 \text{ V}, V_{GE} = 0$	—	—	1.0	mA
Gate-emitter cut-off voltage		$V_{GE} \text{ (off)}$	$I_C = 90 \text{ mA}, V_{CE} = 5 \text{ V}$	4.0	—	8.0	V
Collector-emitter saturation voltage		$V_{CE} \text{ (sat)}$	$I_C = 90 \text{ A}, V_{GE} = 15 \text{ V}$	—	3.2	4.5	V
Input capacitance		C_{ies}	$V_{CE} = 10 \text{ V}, V_{GE} = 0, f = 1 \text{ MHz}$	—	13000	—	pF
Switching time	Turn-on delay time	$t_{d \text{ (on)}}$	Inductive Load $V_{CC} = 900 \text{ V}$ $I_C = 90 \text{ A}$ $V_{GE} = \pm 15 \text{ V}$ $R_G = 5.6 \Omega$ (Note 1)	—	0.1	—	μs
	Rise-time	t_r		—	0.1	—	
	Turn-on time	t_{on}		—	0.5	—	
	Turn-off delay time	$t_{d \text{ (off)}}$		—	0.4	—	
	Fall time	t_f		—	0.5	1.5	
	Turn-off time	t_{off}		—	1.0	—	
Forward voltage		V_F	$I_F = 90 \text{ A}, V_{GE} = 0$	—	3.2	4.5	V
Reverse recovery time		t_{rr}	$I_F = 90 \text{ A}, V_{GE} = -15 \text{ V}$ $di/dt = 500 \text{ A}/\mu\text{s}$ (Note 1)	—	0.2	0.4	μs
Thermal resistance		$R_{th \text{ (j-c)}}$	Transistor stage	—	—	0.114	$^{\circ}\text{C}/\text{W}$
			Diode stage	—	—	0.4	

Note 1: Switching time and reverse recovery time test circuit & timing chart









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