

# MSC81325M

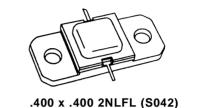
PRELIMINARY DATA

# **RF & MICROWAVE TRANSISTORS AVIONICS APPLICATIONS**

MSC81325M

#### REFRACTORY/GOLD METALLIZATION

- EMITTER BALLASTED
- RUGGEDIZED VSWR ∞:1
- INPUT/OUTPUT MATCHING
- OVERLAY GEOMETRY
- METAL/CERAMIC HERMETIC PACKAGE
- Pout = 325 W MIN. WITH 6.7 dB GAIN



hermetically sealed

**ORDER CODE** 

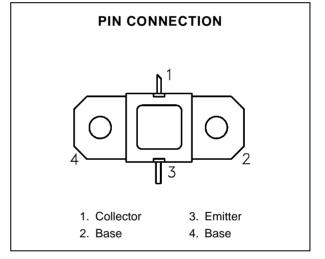
BRANDING 81325M

## DESCRIPTION

The MSC81325M device is a high power pulsed transistor specifically designed for DME/TACAN avionics applications.

This device is capable of withstanding an infinite load VSWR at any phase angle under full rated conditions. Low RF thermal resistance and semiautomatic bonding techniques ensure high reliability and product consistency.

The MSC81325M is housed in the industry-standard AMPAC<sup>™</sup> metal/ceramic hermetic package with internal input/output matching structures.



<b>ABSOLUTE MAXIMUM RATINGS</b> ( $T_{case} = 25^{\circ}C$ )
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Symbol	Parameter	Value	Unit	
PDISS	Power Dissipation* $(T_C \le 100^{\circ}C)$	880	W	
Ic	Device Current*	24	А	
Vcc	Collector-Supply Voltage*	55	V	
TJ	Junction Temperature (Pulsed RF Operation)	250	°C	
T <sub>STG</sub>	Storage Temperature	– 65 to +200	°C	

## THERMAL DATA

R <sub>TH(j-c)</sub>	Junction-Case Thermal Resistance*	0.17	°C/W		
*Applies only to rated RF amplifier operation					

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# **ELECTRICAL SPECIFICATIONS** ( $T_{case} = 25^{\circ}C$ )

#### STATIC

Symbol	Test Conditions	Value			Unit		
		Min.	Тур.	Max.	Unit		
ВУсво	$I_C = 10 \text{mA}$	$I_E = 0mA$		65	—	—	V
BVEBO	$I_E = 1mA$	$I_C = 0mA$		3.5			V
BVCER	IC = 25mA	$R_{BE} = 10\Omega$		65	_		V
ICES	$V_{BE} = 0V$	$V_{CE} = 50V$		_	_	25	mA
hFE	$V_{CE} = 5V$	$I_C = 1A$		15		120	—

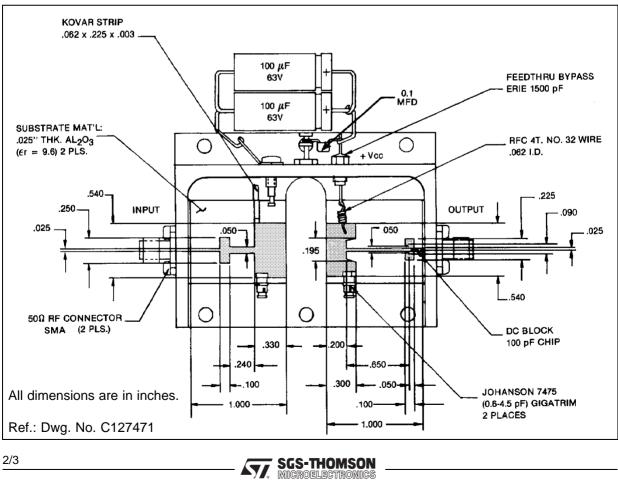
#### DYNAMIC

Symbol	Test Conditions		Value		
Symbol			Тур.	Max.	Unit
Роит	$f = 1025 - 1150 \text{ MHz} P_{IN} = 70 \text{ W} V_{CC} =$	50 V 325	360	_	W
ηc	$f = 1025 - 1150 \text{ MHz} P_{IN} = 70 \text{ W} V_{CC} =$	50 V 40	41	_	%
GP	$f = 1025 - 1150 \text{ MHz} P_{IN} = 70 \text{ W} V_{CC} =$	50 V 6.7	7.1		dB

Pulse Width =  $10\mu$ Sec Note:

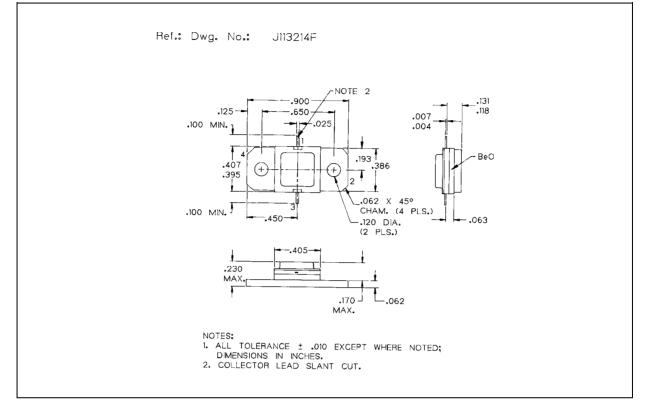
Duty Cycle = 1%

## **TEST CIRCUIT**



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#### PACKAGE MECHANICAL DATA



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