

### I. Power section

Absolute maximum ratings		$T_s = 25^\circ\text{C}$ unless otherwise specified	
Symbol	Conditions	Values	Units
IGBT			
$V_{CES}$	Operating DC link voltage	1700	V
$V_{CC}^{1)}$		1200	V
$V_{GES}$		$\pm 20$	V
$I_C$		$T_s = 25 (70)^\circ\text{C}$	2000 (1500)
Inverse diode			
$I_F = -I_C$	$T_s = 25 (70)^\circ\text{C}$	1500 (1120)	A
$I_{FSM}$	$T_j = 150^\circ\text{C}$ , $t_p = 10\text{ms}$ ; sin	17280	A
$I^2t$ (Diode)	Diode, $T_j = 150^\circ\text{C}$ , 10ms	1493	$\text{kA}^2\text{s}$
$T_j, (T_{stg})$		-40...+150 (125)	$^\circ\text{C}$
$V_{isol}$	rms, AC, 1min	4000	V
$I_{AC-terminal}$	per AC terminal, rms, $T_s = 70^\circ\text{C}$ , $T_{terminal} < 115^\circ\text{C}$	400	A

Characteristics $T_s = 25^\circ\text{C}$ unless otherwise specified								
Symbol	Conditions	min.	typ.	max.	Units			
IGBT								
$V_{CEsat}$	$I_C = 1200\text{A}$ , $T_j = 25 (125)^\circ\text{C}$ ; measured at terminal	-	1,9 (2,2)	2,4	V			
$V_{CEO}$	$T_j = 25 (125)^\circ\text{C}$ ; at terminal	-	1,0 (0,9)	1,2 (1,1)	V			
$r_{CE}$	$T_j = 25 (125)^\circ\text{C}$ ; at terminal	-	0,8 (1,0)	1,0 (1,3)	$\text{m}\Omega$			
$I_{CES}$	$V_{GE}=0, V_{CE}=V_{CES}, T_j=25(125)^\circ\text{C}$	-	4,8 (288)	-	$\text{mA}$			
$E_{on} + E_{off}$	$I_C=1200\text{A}$ , $V_{CC}=900\text{V}$	-	968	-	$\text{mJ}$			
	$T_j=125^\circ\text{C}$ , $V_{CC}=1200\text{V}$	-	1428	-	$\text{mJ}$			
$L_{CE}$	top, bottom	-	3	-	$\text{nH}$			
$C_{CHC}$	per phase, AC side	-	6,8	-	$\text{nF}$			
$R_{CC'-EE'}$	terminal-chip, $T_j=25^\circ\text{C}$	-	0,13	-	$\text{m}\Omega$			
Inverse diode								
$V_F = V_{EC}$	$I_F = 1200\text{A}$ ; $T_j = 25(125)^\circ\text{C}$ measured at terminal	-	1,6 (1,5)	2,0	V			
$V_{TO}$	$T_j = 25 (125)^\circ\text{C}$	-	1,1 (0,8)	1,3 (1,0)	V			
$r_T$	$T_j = 25 (125)^\circ\text{C}$	-	0,4 (0,6)	0,6 (0,8)	$\text{m}\Omega$			
$E_{RR}$	$I_C=1200\text{A}$ , $V_{CC}=900\text{V}$	-	172	-	$\text{mJ}$			
	$T_j=125^\circ\text{C}$ , $V_{CC}=1200\text{V}$	-	204	-	$\text{mJ}$			
Mechanical data								
$M_{dc}$	DC terminals, SI Units	6	-	8	Nm			
$M_{ac}$	AC terminals, SI Units	13	-	15	Nm			
w	SKiIP® 3 System w/o heat sink	-	3,1	-	kg			
w	heat sink	-	9,7	-	kg			
Thermal characteristics (PX16 heat sink with fan SKF16B-230-1); "s" reference to heat sink; "r" reference to built-in temperature sensor (acc. IEC 60747-15)								
$R_{thjsIGBT}$	per IGBT	-	-	0,015	$^\circ\text{C/W}$			
$R_{thjsdiode}$	per diode	-	-	0,029	$^\circ\text{C/W}$			
$Z_{th}$	$R_i$ (mK/W) (max. values)				$\tau_i$ (s)			
	1	2	3	4	1	2	3	4
$IGBT_{jr}$	5,6	6,0	6,4	0,0	363,0	0,18	0,04	1,0
$diode_{jr}$	10,0	8,4	14,8	14,8	50,0	5,0	0,25	0,04
$heatsink_{ra}$	3,1	17,3	3,7	0,9	230	78	13,0	0,4

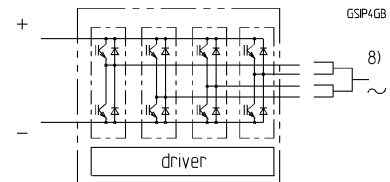
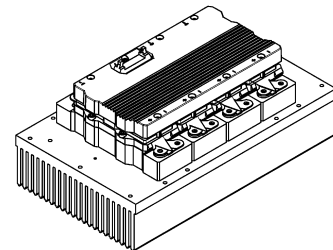
### SKiIP® 3

### SK integrated intelligent Power 2-pack

### SKiIP 2013GB173-4DL

Preliminary data

Case S43



### Features

- SKiIP technology inside
- Trench IGBTs
- CAL HD diode technology
- integrated current sensor
- integrated temperature sensor
- integrated heat sink
- IEC 60721-3-3 (humidity) class 3K3/IE32 (SKiIP® 3 System)
- IEC 68T.1 (climate) 40/125/56 (SKiIP® 3 power section)
- UL recognized File no. E63532 (SKiIP® 3 power section)

1) with assembly of suitable MKP capacitor per terminal (SEMIKRON type is recommended)

8) AC connection busbars must be connected by the user; copper busbars available on request

This technical information specifies semiconductor devices but promises no characteristics. No warranty or guarantee, expressed or implied is made regarding delivery, performance or suitability.

## SKiiP 2013GB173-4DL

### SKiiP 3®

### SK integrated intelligent Power

## SKiiP 2013GB173-4DL

Preliminary data

#### Gate driver features

- CMOS compatible inputs
- wide range power supply
- integrated circuitry to sense phase current, heat sink temperature and DC-bus voltage (option)
- short circuit protection
- over current protection
- over voltage protection (option)
- power supply protected against under voltage
- interlock of top/bottom switch
- isolation by transformers
- fibre optic interface (option for GB-types only)
- IEC 68T.1 (climate) 40/85/56 (SKiiP® 3 gate driver)

## II. Integrated gate driver

### Absolute maximum ratings

Symbol	Term	Value	Unit
V <sub>S2</sub>	unstabilized 24V power supply	30	V
V <sub>iH</sub>	input signal voltage (high)	15 + 0,3	V
dv/dt	secondary to primary side	75	kV/μs
V <sub>isolIO</sub>	input / output (AC, rms, 2 s)	4000	V
V <sub>isolPD</sub>	partial discharge extinction voltage, rms, Q <sub>PD</sub> ≤ 10 pC;	1500	V
V <sub>isol12</sub>	output 1 / output 2 (AC, rms, 2s)	1500	V
f	switching frequency	6	kHz
T <sub>op</sub> (T <sub>stg</sub> )	operating / storage temperature	- 40 ... + 85	°C

### Electrical characteristics (T<sub>a</sub> = 25 °C)

Symbol	Term	Values			Units
		min	typ	max.	
V <sub>S2</sub>	supply voltage non stabilized	13	24	27	V
I <sub>S2</sub>	V <sub>S2</sub> = 24V	324 + 45*f / kHz + 0,00011 * (I <sub>AC</sub> /A) <sup>2</sup>			mA
V <sub>IT+</sub>	input threshold voltage (High)	11,2	–	–	V
V <sub>IT-</sub>	input threshold voltage (Low)	–	–	5,4	V
R <sub>in</sub>	input resistance	–	10	–	kΩ
C <sub>in</sub>	input capacitance	–	1	–	nF
t <sub>d(on)IO</sub>	input-output turn-on propagation time	–	1,1	–	μs
t <sub>d(off)IO</sub>	input-output turn-off propagation time	–	1,1	–	μs
t <sub>pERRRESET</sub>	error memory reset time	–	9	–	μs
t <sub>TD</sub>	top/bottom switch: interlock time	–	3,3	–	μs
I <sub>analogOUT</sub>	max. 5mA ; 8 V corresponds to 15 V supply voltage for external components; max load current	–	2000	–	A
I <sub>S1out</sub>		–	–	50	mA
I <sub>TRIPSC</sub>	over current trip level ( I <sub>analog OUT</sub> = 10V)	–	2500	–	A
T <sub>tp</sub>	over temperature protection	110	–	120	°C
U <sub>DCTRIP</sub>	U <sub>DC</sub> -protection ( U <sub>analog OUT</sub> = 9V) (option for GB types)	not implemented	–	–	V

For electrical and thermal design support please use SEMISEL. Access to SEMISEL is via SEMIKRON website <http://www.semikron.com>.

This technical information specifies semiconductor devices but promises no characteristics. No warranty or guarantee expressed or implied is made regarding delivery, performance or suitability.