

## SKiiP 342GDL120-411CTV

### I. Power section 3 phase bridge

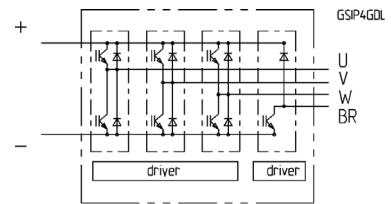
Absolute maximum ratings		$T_s = 25^\circ\text{C}$ unless otherwise specified	
Symbol	Conditions	Values	Units
IGBT			
$V_{CES}$	Operating DC link voltage	1200	V
$V_{CC}^{1)}$		900	V
$V_{GES}$		$\pm 20$	V
$I_C$		$T_s = 25 (70)^\circ\text{C}$	300 (225)
Inverse diode			
$I_F = -I_C$	$T_s = 25 (70)^\circ\text{C}$	300 (225)	A
$I_{FSM}$	$T_j = 150^\circ\text{C}$ , $t_p = 10\text{ms}$ ; sin	2160	A
$I^2t$ (Diode)	Diode, $T_j = 150^\circ\text{C}$ , 10ms	23	$\text{kA}^2\text{s}$
$T_j, (T_{stg})$		-40 (-25) ... +150 (125)	$^\circ\text{C}$
$V_{isol}$	AC, 1min.	3000	V

### SKiiP<sup>®</sup> 2

### SK integrated intelligent Power 7-pack

### SKiiP 342GDL120-411CTV

Case S5



Characteristics		$T_s = 25^\circ\text{C}$ unless otherwise specified			
Symbol	Conditions	min.	typ.	max.	Units
IGBT					
$V_{CESat}$	$I_C = 250\text{A}$ , $T_j = 25 (125)^\circ\text{C}$	-	2,6 (3,1)	3,1	V
$V_{CEO}$	$T_j = 25 (125)^\circ\text{C}$	-	1,2 (1,3)	1,5 (1,6)	V
$r_{CE}$	$T_j = 25 (125)^\circ\text{C}$	-	5,3 (7,0)	6,3 (8,1)	$\text{m}\Omega$
$I_{CES}$	$V_{GE}=0, V_{CE}=V_{CES}, T_j=25(125)^\circ\text{C}$	-	(15)	0,4	$\text{mA}$
$E_{on} + E_{off}$	$I_C=250\text{A}$ , $V_{CC}=600\text{V}$	-	-	75	$\text{mJ}$
	$T_j=125^\circ\text{C}$ , $V_{CC}=900\text{V}$	-	-	132	$\text{mJ}$
$R_{CC-EE}$	terminal chip, $T_j = 125^\circ\text{C}$	-	0,50	-	$\text{m}\Omega$
$L_{CE}$	top, bottom	-	15,0	-	$\text{nH}$
$C_{CHC}$	per phase, AC-side	-	1,4	-	$\text{nF}$
Inverse diode					
$V_F = V_{EC}$	$I_F = 250\text{A}$ ; $T_j = 25(125)^\circ\text{C}$	-	2,1 (2,0)	2,6	V
$V_{TO}$	$T_j = 25 (125)^\circ\text{C}$	-	1,3 (1,0)	1,4 (1,1)	V
$r_T$	$T_j = 25 (125)^\circ\text{C}$	-	3,3 (4,0)	4,5 (5,2)	$\text{m}\Omega$
$E_{RR}$	$I_C=250\text{A}$ , $V_{CC}=600\text{V}$	-	-	10	$\text{mJ}$
	$T_j=125^\circ\text{C}$ , $V_{CC}=900\text{V}$	-	-	12	$\text{mJ}$
Mechanical data					
$M_{dc}$	DC terminals, SI Units	6	-	8	$\text{Nm}$
$M_{ac}$	AC terminals, SI Units	13	-	15	$\text{Nm}$
w	SKiiP <sup>®</sup> 2 System w/o heat sink	-	3,5	-	kg
w	heat sink	-	8,5	-	kg
Thermal characteristics (P16 heat sink; 275 $\text{m}^3/\text{h}$ ); "r" reference to temperature sensor					
$R_{thjrIGBT}$	per IGBT	-	-	0,090	$\text{K/W}$
$R_{thjrdiode}$	per diode	-	-	0,250	$\text{K/W}$
$R_{thra}$	per module	-	-	0,036	$\text{K/W}$
$Z_{th}$	$R_i$ ( $\text{mK/W}$ ) (max.)	$\tau_{ui}$ (s)			
		1	2	3	4
$IGBT_{jr}$		10	69	11	-
$diode_{jr}$		28	193	30	-
$heatsink_{ra}$		1,7	24,0	7,6	2,6
		494	165	20	0,03

### Features

- SKiiP technology inside
- low loss IGBTs
- CAL diode technology
- integrated current sensor
- integrated temperature sensor
- integrated heat sink
- IEC 60721-3-3 (humidity) class 3K3/IE32 (SKiiP<sup>®</sup> 2 System)
- IEC 68T.1 (climate) 40/125/56 (SKiiP<sup>®</sup> 2 power section)

1) with assembly of suitable MKP capacitor per terminal (SEMIKRON type is recommended)

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## SKiiP 342GDL120-411CTV

### SKiiP 2®

### SK integrated intelligent Power

### SKiiP 342GDL120-411CTV

## II. Integrated gate driver 3 phase bridge

Absolute maximum ratings			
Symbol	Term	Value	Unit
V <sub>S1</sub>	stabilized 15V power supply	18	V
V <sub>S2</sub>	unstabilized 24V power supply	30	V
V <sub>iH</sub>	input signal voltage (high)	15 + 0,3	V
dv/dt	secondary to primary side	75	kV/μs
V <sub>isolIO</sub>	input / output (AC)	3000	Vac
V <sub>isol12</sub>	output 1 / output 2 (AC)	1500	Vac
f <sub>max</sub>	switching frequency	20	kHz
T <sub>op</sub> (T <sub>stg</sub> )	operating / storage temperature	- 25 ... + 85	°C

#### Gate driver features

- CMOS compatible inputs
- wide range power supply
- integrated circuitry to sense phase current, heat sink temperature and DC-bus voltage (option)
- short circuit protection
- over current protection
- over voltage protection (option)
- power supply protected against under voltage
- interlock of top/bottom switch
- isolation by transformers
- fibre optic interface (option for GB-types only)
- IEC 68T.1 (climate) 25/85/56 (SKiiP® 2 gate driver)

Electrical characteristics (T <sub>a</sub> = 25 °C)			Values			
Symbol	Term		min	typ	max.	Units
V <sub>S1</sub>	supply voltage stabilized		14,4	15	15,6	V
V <sub>S2</sub>	supply voltage non stabilized		20	24	30	V
I <sub>S1</sub>	V <sub>S1</sub> = 15V	$340 + 520 \cdot f / f_{max} + 3,5 \cdot (I_{AC}/A)$				mA
I <sub>S2</sub>	V <sub>S2</sub> = 24V	$250 + 380 \cdot f / f_{max} + 2,6 \cdot (I_{AC}/A)$				mA
V <sub>iT+</sub>	input threshold voltage (High)		11,2	–	–	V
V <sub>iT-</sub>	input threshold voltage (Low)		–	–	5,4	V
R <sub>in</sub>	input resistance		–	10	–	kΩ
t <sub>d(on)IO</sub>	turn-on propagation time (system)		–	1,2	–	μs
t <sub>d(off)IO</sub>	turn-off propagation time (system)		–	1,6	–	μs
t <sub>pERRRESET</sub>	error memory reset time		9	–	–	μs
t <sub>TD</sub>	top/bottom switch: interlock time		–	2,3	–	μs
I <sub>analogOUT</sub>	8 V corresponds to		–	300	–	A
I <sub>Vs1outmax</sub>	max. current of 15 V supply voltage (available when supplied with 24V)		–	–	50	mA
I <sub>A0max</sub>	output current at pin 13/20/22/24/26		–	–	5	mA
V <sub>ol</sub>	logic low output voltage		–	–	0,6	V
V <sub>oH</sub>	logic high output voltage		–	–	30	V
I <sub>TRIPSC</sub>	over current trip level ( I <sub>analog OUT</sub> = 10V)		–	375	–	A
I <sub>TRIPLG</sub>	ground fault protection		–	87	–	A
T <sub>ip</sub>	over temperature protection		110	–	120	°C
U <sub>DCTRIP</sub>	trip level of U <sub>DC</sub> -protection ( U <sub>analog OUT</sub> = 9V); (option)		900	–	–	V

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