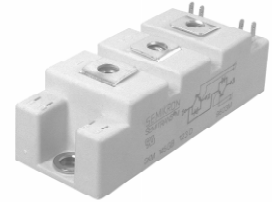


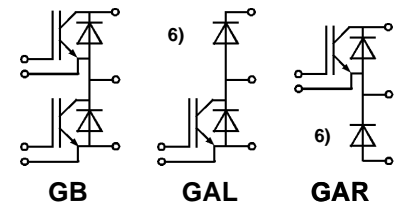
| Absolute Maximum Ratings | | Values | | Units |
|--------------------------------------|---|--------------------|-------------------|------------------|
| Symbol | Conditions ¹⁾ | | | |
| V _{CES} | | 600 | | V |
| V _{CGR} | R _{GE} = 20 kΩ | 600 | | V |
| I _C | T _{case} = 25/75 °C | 100 / 75 | | A |
| I _{CM} | T _{case} = 25/75 °C; t _p = 1 ms | 200 / 150 | | A |
| V _{GES} | | ± 20 | | V |
| P _{tot} | per IGBT, T _{case} = 25 °C | 350 | | W |
| T _j , (T _{stg}) | | -40 ... +(125) 150 | | °C |
| V _{isol} | AC, 1 min. | 2500 | | V |
| humidity | DIN 40040 | Class F | | |
| climate | DIN IEC 68 T.1 | 40/125/56 | | |
| Inverse Diode | | | FWD ⁶⁾ | |
| I _F = -I _C | T _{case} = 25/80 °C | 75 / 50 | 100 / 75 | A |
| I _{FM} = -I _{CM} | T _{case} = 25/80 °C; t _p = 1 ms | 200 / 150 | 260 / 180 | A |
| I _{FSM} | t _p = 10 ms; sin.; T _j = 150 °C | 440 | 720 | A |
| I ² t | t _p = 10 ms; T _j = 150 °C | 970 | 2600 | A ² s |

SEMITRANS® M Superfast NPT-IGBT Modules

SKM 75 GB 063 D
SKM 75 GAR 063 D ⁶⁾
SKM 75 GAL 063 D ⁶⁾



SEMITRANS 2



Features

- N channel, homogeneous Silicon structure (NPT- Non punch-through IGBT)
- Low tail current with low temperature dependence
- High short circuit capability, self limiting if term. G is clamped to E
- Pos. temp.-coeff. of V_{CEsat}
- 50 % less turn off losses ⁹⁾
- 30 % less short circuit current ⁹⁾
- Very low C_{ies}, C_{oes}, C_{res} ⁹⁾
- Latch-up free
- Fast & soft inverse CAL diodes ⁸⁾
- Isolated copper baseplate using DCB Direct Copper Bonding Technology without hard mould
- Large clearance (10 mm) and creepage distances (20 mm)

Typical Applications

- Switching (not for linear use)
- Switched mode power supplies
- UPS
- Three phase inverters for servo / AC motor speed control
- Pulse frequencies also > 10 kHz

| Characteristics | | min. | typ. | max. | Units |
|--|--|--------------------|------------|----------|-------|
| Symbol | Conditions ¹⁾ | | | | |
| V _{(BR)CES} | V _{GE} = 0, I _C = 2 mA | ≥ V _{CES} | – | – | V |
| V _{GE(th)} | V _{GE} = V _{CE} , I _C = 1 mA | 4,5 | 5,5 | 6,5 | V |
| I _{CES} | V _{GE} = 0 } T _j = 25 °C | – | 0,1 | 2,3 | mA |
| | V _{CE} = V _{CES} } T _j = 125 °C | – | 3 | – | mA |
| I _{GES} | V _{GE} = 20 V, V _{CE} = 0 | – | – | 150 | nA |
| V _{CEsat} | I _C = 50 A } V _{GE} = 15 V; | – | 1,8(2,0) | – | V |
| V _{CEsat} | I _C = 75 A } T _j = 25 (125) °C } | – | 2,1(2,4) | 2,5(2,8) | V |
| g _{fs} | V _{CE} = 20 V, I _C = 75 A | 25 | – | – | S |
| C _{CHC} | per IGBT | – | – | 350 | pF |
| C _{ies} | V _{GE} = 0 | – | 4200 | – | pF |
| C _{oes} | V _{CE} = 25 V | – | 500 | – | pF |
| C _{res} | f = 1 MHz | – | 300 | – | pF |
| L _{CE} | | – | – | 30 | nH |
| t _{d(on)} | V _{CC} = 300 V | – | 60 | – | ns |
| t _r | V _{GE} = -15 V / +15 V ³⁾ | – | 50 | – | ns |
| t _{d(off)} | I _C = 75 A, ind. load | – | 350 | – | ns |
| t _f | R _{Gon} = R _{Goff} = 15 Ω | – | 35 | – | ns |
| E _{on} | T _j = 125 °C | – | 3 | – | mWs |
| E _{off} | | – | 2,5 | – | mWs |
| Inverse Diode ⁸⁾ | | | | | |
| V _F = V _{EC} | I _F = 50 A } V _{GE} = 0 V; | – | 1,45(1,35) | 1,7 | V |
| | I _F = 75 A } T _j = 25 (125) °C } | – | 1,55(1,55) | 1,9 | V |
| V _{TO} | T _j = 125 °C | – | – | 0,9 | V |
| r _t | T _j = 125 °C | – | 10 | 15 | mΩ |
| I _R RM | I _F = 75 A; T _j = 125 °C ²⁾ | – | 30 | – | A |
| Q _{rr} | I _F = 75 A; T _j = 125 °C ²⁾ | – | 3,7 | – | μC |
| FWD of types "GAL", "GAR" ^{8) 6)} | | | | | |
| V _F = V _{EC} | I _F = 75 A } V _{GE} = 0 V; | – | 1,45(1,3) | 1,7 | V |
| | I _F = 100 A } T _j = 25 (125) °C } | – | 1,55(1,55) | 1,9 | V |
| V _{TO} | T _j = 125 °C | – | – | 0,9 | V |
| r _t | T _j = 125 °C | – | 8 | 11 | mΩ |
| I _R RM | I _F = 100 A; T _j = 125 °C | – | 44 | – | A |
| Q _{rr} | I _F = 100 A; T _j = 125 °C | – | 6,0 | – | μC |
| Thermal characteristics | | | | | |
| R _{thjc} | per IGBT | – | – | 0,35 | °C/W |
| R _{thjc} | per diode | – | – | 1,0 | °C/W |
| R _{thch} | per module | – | – | 0,05 | °C/W |

⁶⁾ The free-wheeling diode of the GAL and GAR types have the data of the inverse diodes of SKM 100 GB 063 D

¹⁾ T_{case} = 25 °C, unless otherwise specified

²⁾ I_F = -I_C, V_R = 300 V, -di_F/dt = 800 A/μs, V_{GE} = 0 V

³⁾ Use V_{GEoff} = -5... -15 V

⁸⁾ CAL = Controlled Axial Lifetime Technology

⁹⁾ Compared to PT-IGBT

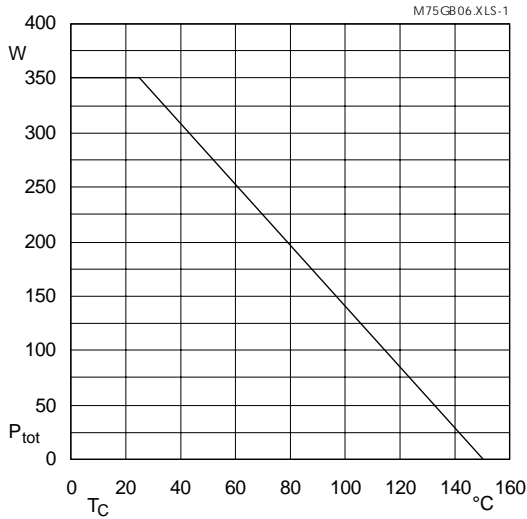


Fig. 1 Rated power dissipation $P_{tot} = f(T_C)$

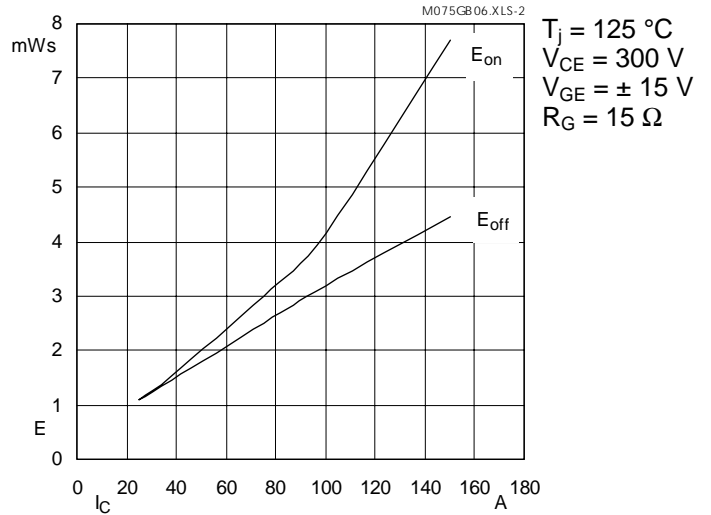


Fig. 2 Turn-on /-off energy $= f(I_C)$

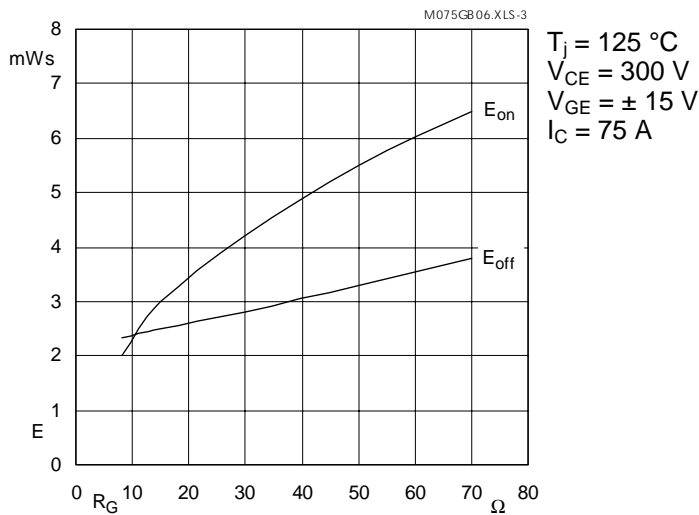


Fig. 3 Turn-on /-off energy $= f(R_G)$

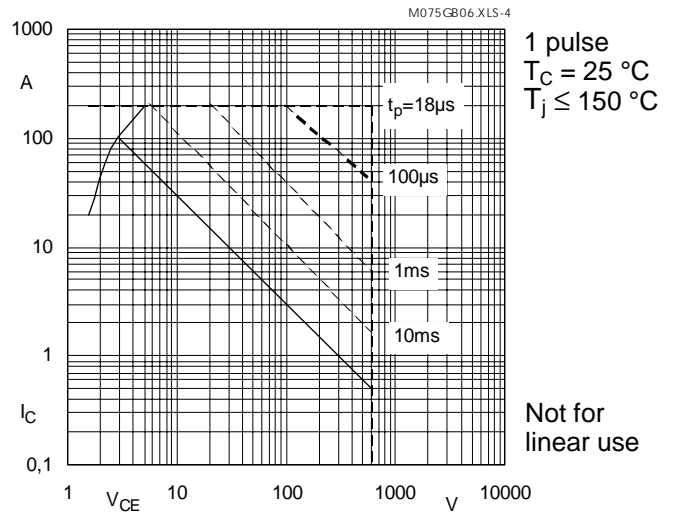


Fig. 4 Maximum safe operating area (SOA) $I_C = f(V_{CE})$

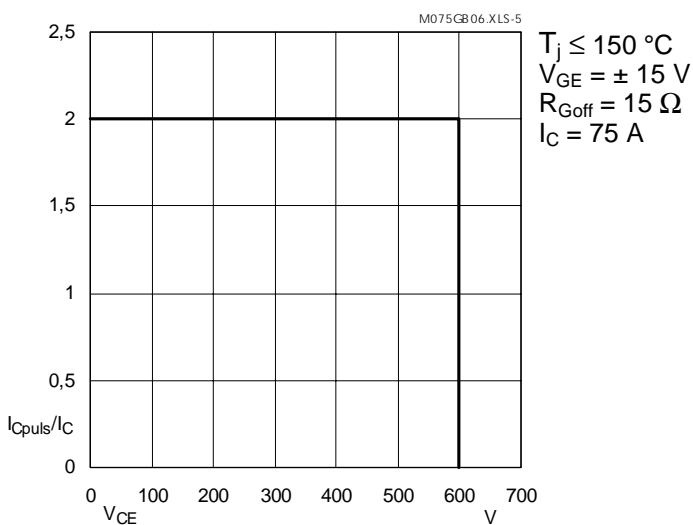


Fig. 5 Turn-off safe operating area (RBSOA)

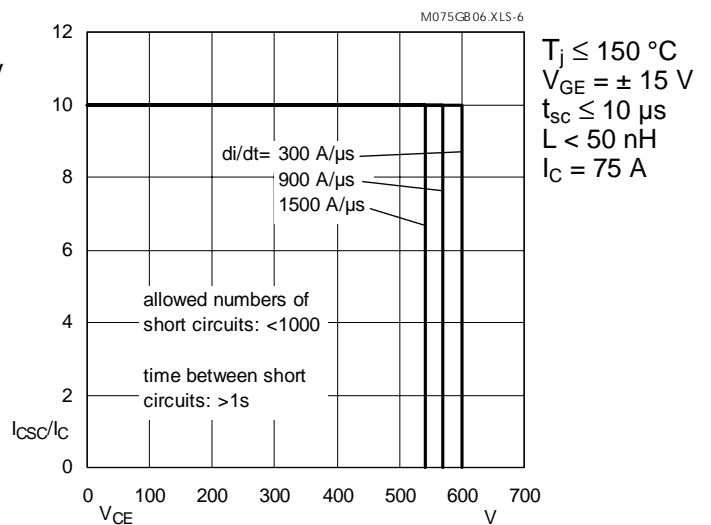


Fig. 6 Safe operating area at short circuit $I_C = f(V_{CE})$

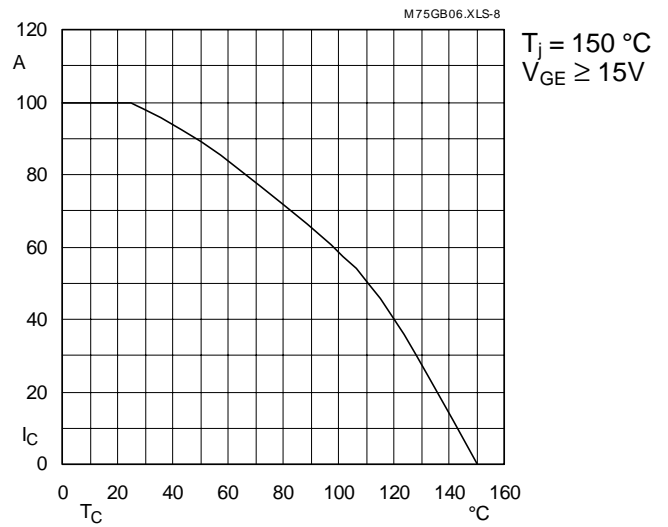


Fig. 8 Rated current vs. temperature $I_C = f(T_C)$

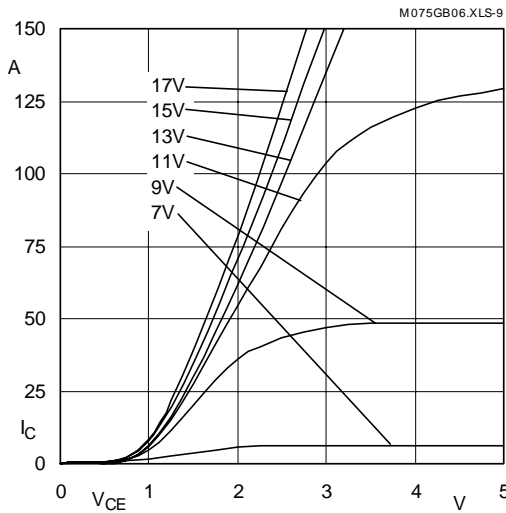


Fig. 9 Typ. output characteristic, $t_p = 250 \mu s$; $T_j = 25 \text{ }^\circ\text{C}$

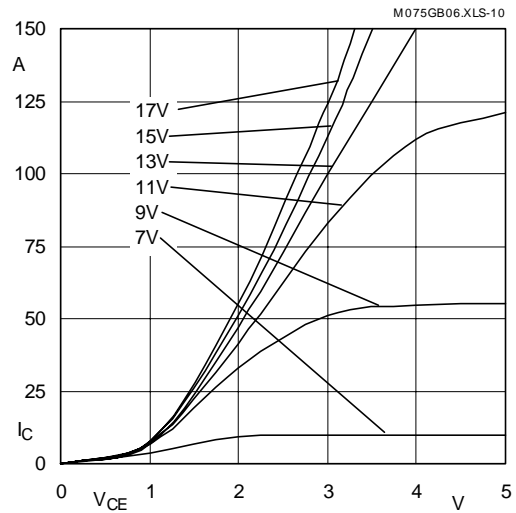


Fig. 10 Typ. output characteristic, $t_p = 250 \mu s$; $T_j = 125 \text{ }^\circ\text{C}$

$$P_{\text{cond}(t)} = V_{\text{CEsat}(t)} \cdot I_{\text{C}(t)}$$

$$V_{\text{CEsat}(t)} = V_{\text{CE(TO)(T}_j)} + r_{\text{CE(T}_j)} \cdot I_{\text{C}(t)}$$

$$V_{\text{CE(TO)(T}_j)} \leq 1,2 - 0,001 (T_j - 25) \text{ [V]}$$

$$\text{typ.: } r_{\text{CE(T}_j)} = 0,0120 + 0,000053 (T_j - 25) \text{ [\Omega]}$$

$$\text{max.: } r_{\text{CE(T}_j)} = 0,0173 + 0,000053 (T_j - 25) \text{ [\Omega]}$$

$$\text{valid for } V_{\text{GE}} = +15 \pm 2 \text{ [V]; } I_{\text{C}} \geq 0,3 I_{\text{Cn}}$$

Fig. 11 Saturation characteristic (IGBT)
Calculation elements and equations

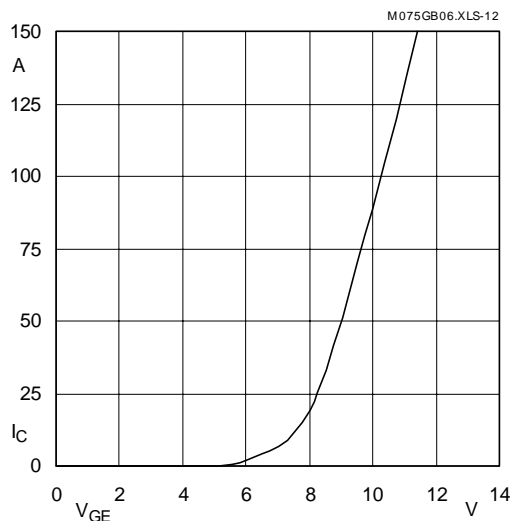


Fig. 12 Typ. transfer characteristic, $t_p = 250 \mu s$; $V_{\text{CE}} = 20 \text{ V}$

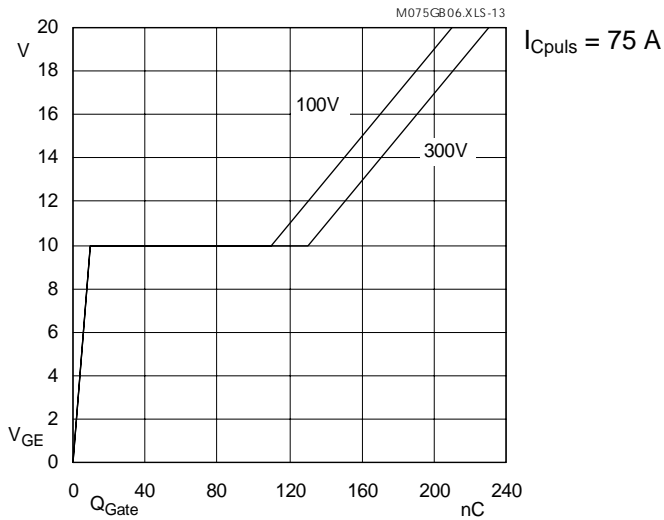


Fig. 13 Typ. gate charge characteristic

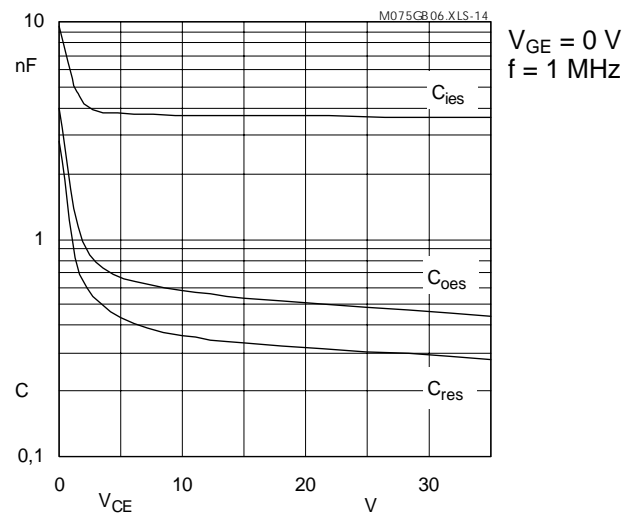


Fig. 14 Typ. capacitances vs. V_{CE}

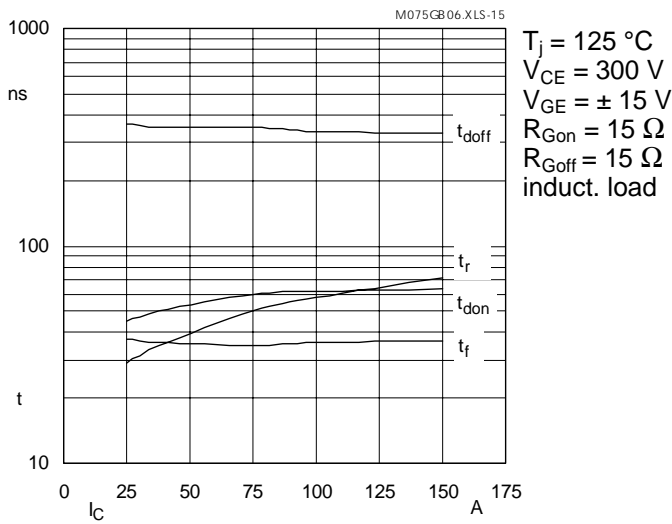


Fig. 15 Typ. switching times vs. I_C

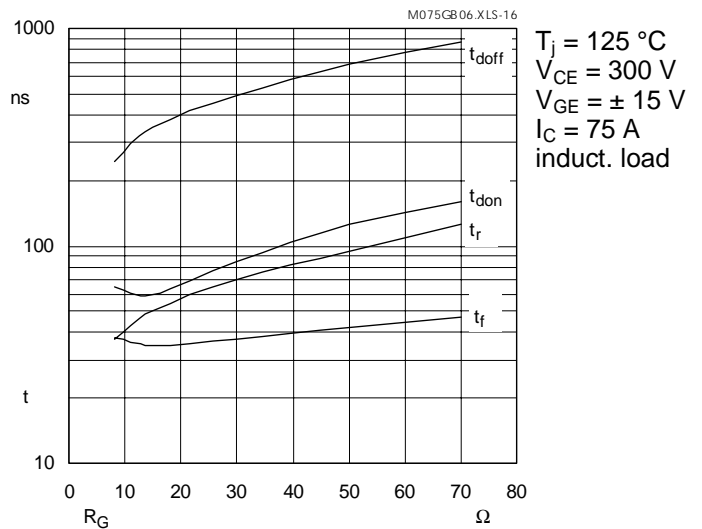


Fig. 16 Typ. switching times vs. gate resistor R_G

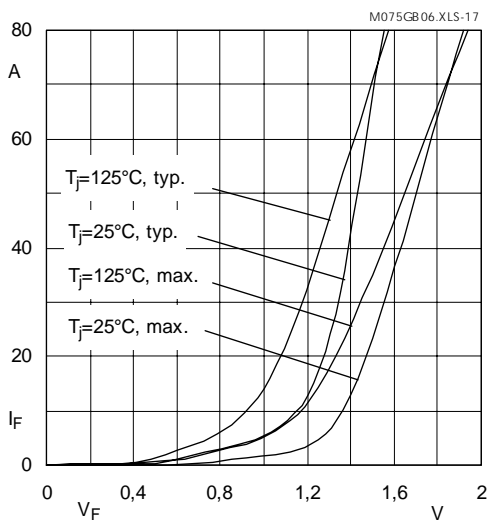


Fig. 17 Typ. CAL diode forward characteristic

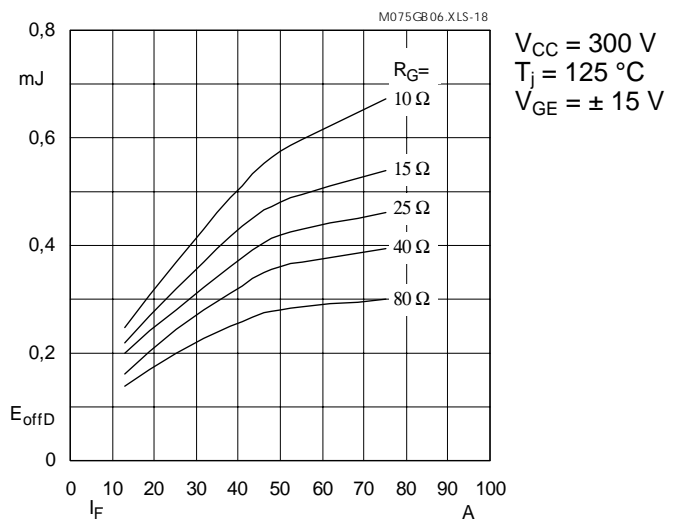


Fig. 18 Diode turn-off energy dissipation per pulse

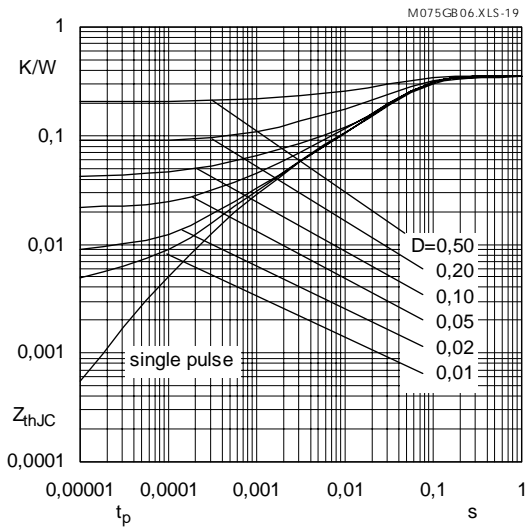


Fig. 19 Transient thermal impedance of IGBT
 $Z_{thJC} = f(t_p)$; $D = t_p / t_c = t_p \cdot f$

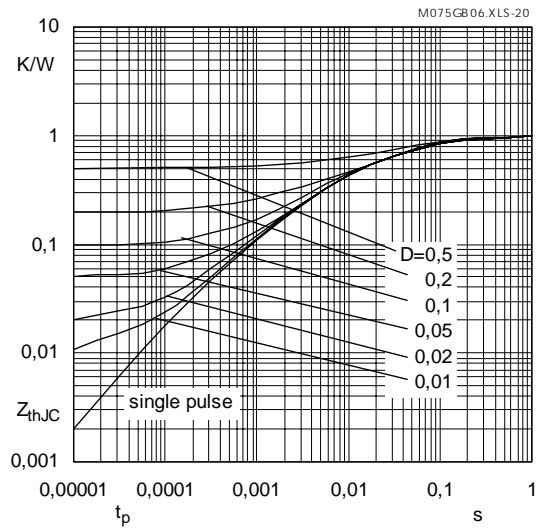


Fig. 20 Transient thermal impedance of inverse CAL diodes
 $Z_{thJC} = f(t_p)$; $D = t_p / t_c = t_p \cdot f$

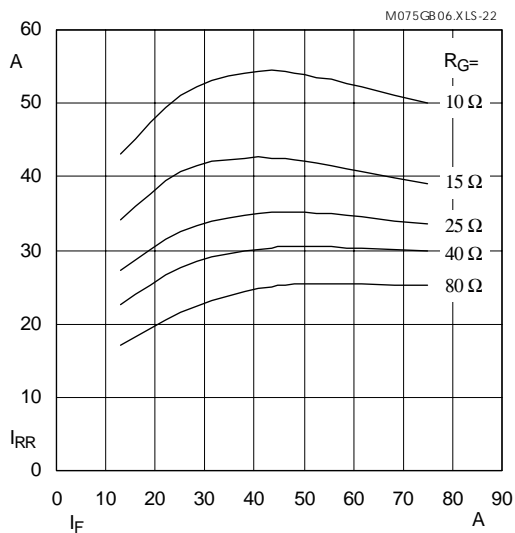


Fig. 22 Typ. CAL diode peak reverse recovery current $I_{RR} = f(I_F; R_G)$

$V_{CC} = 300\text{ V}$
 $T_j = 125\text{ °C}$
 $V_{GE} = \pm 15\text{ V}$

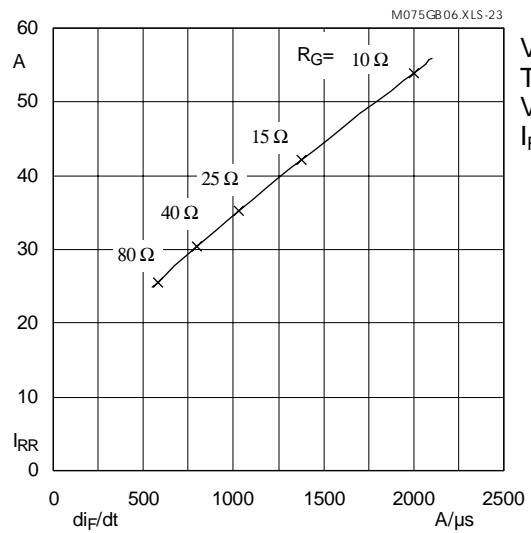


Fig. 23 Typ. CAL diode peak reverse recovery current $I_{RR} = f(di/dt)$

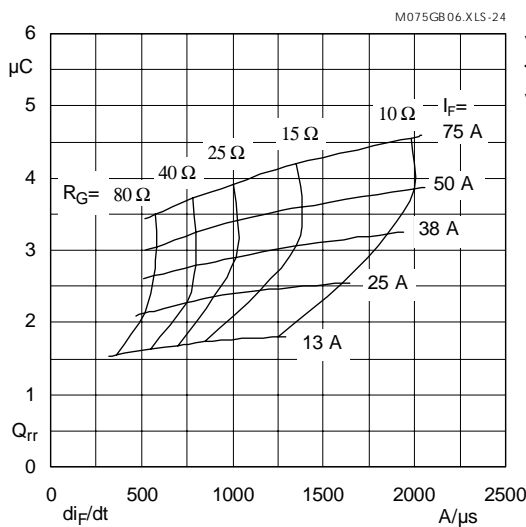
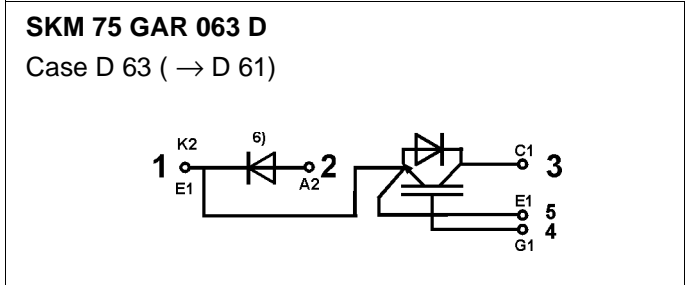
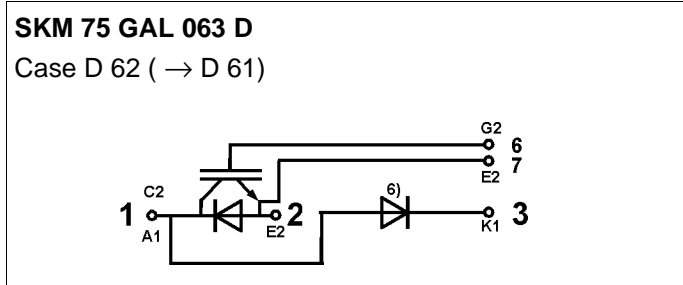
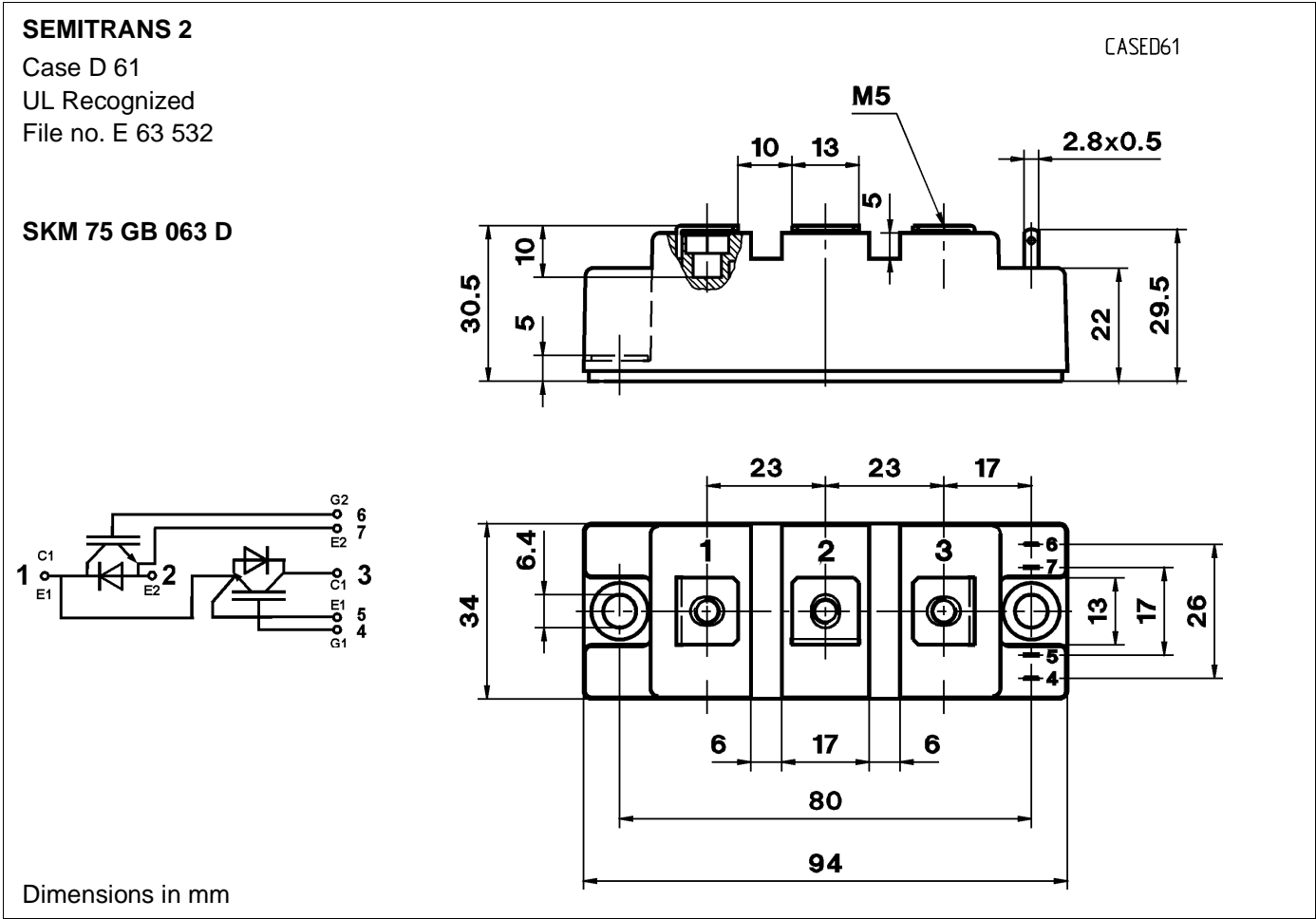


Fig. 24 Typ. CAL diode recovered charge

$V_{CC} = 300\text{ V}$
 $T_j = 125\text{ °C}$
 $V_{GE} = \pm 15\text{ V}$



Case outline and circuit diagrams

| Mechanical Data | | Values | | | Units |
|-----------------|--|--------|------|--------|------------------|
| Symbol | Conditions | min. | typ. | max. | |
| M ₁ | to heatsink, SI Units(M6) to heatsink, US Units | 3 | — | 5 | Nm lb.in. |
| M ₂ | for terminals, SI Units(M5) for terminals, US Units | 2,5 | — | 5 | Nm lb.in. |
| a | | — | — | 5x9,81 | m/s ² |
| w | | — | — | 160 | g |

⁶⁾ Freewheeling diode → B 6 – 15, remark 6.

This is an electrostatic discharge sensitive device (ESDS). Please observe the international standard IEC 747-1, Chapter IX.

Eight devices are supplied in one SEMIBOX A without mounting hardware, which can be ordered separately under Ident No. 33321100 (for 10 SEMITRANS 2)

Larger packing units of 20 or 42 pieces are used if suitable

Accessories → B 6 – 4
 SEMIBOX → C - 1.