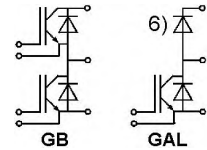


SEMISTRANS® M IGBT Modules

SKM 75 GB 123 D
SKM 75 GAL 123 D ⁶⁾



SEMISTRANS 2



Features

- MOS input (voltage controlled)
- N channel, Homogeneous Si
- Low inductance case
- Very low tail current with low temperature dependence
- High short circuit capability, self limiting to $6 \cdot I_{cnom}$
- Latch-up free
- Fast & soft inverse CAL diodes⁸⁾
- Isolated copper baseplate using DCB Direct Copper Bonding Technology
- Large clearance (10 mm) and creepage distances (20 mm).

Typical Applications: → B 6 - 27

- Switching (not for linear use)

¹⁾ $T_{case} = 25\text{ °C}$, unless otherwise specified

²⁾ $I_F = -I_C$, $V_R = 600\text{ V}$, $-di/dt = 800\text{ A}/\mu\text{s}$, $V_{GE} = 0\text{ V}$

³⁾ Use $V_{GEOff} = -5 \dots -15\text{ V}$

⁵⁾ See fig. 2 + 3; $R_{Goff} = 22\ \Omega$

⁶⁾ The free-wheeling diodes of the GAL types have the data of the inverse diodes of SKM 100 GB 123 D

⁷⁾ $V_{isol} = 4000\text{ V}_{rms}$ on request

⁸⁾ CAL = Controlled Axial Lifetime Technology.

Cases and mech. data → B6 - 28
SEMISTRANS 2

Absolute Maximum Ratings		Values		Units
Symbol	Conditions ¹⁾			
V_{CES}		1200		V
V_{CGR}	$R_{GE} = 20\text{ k}\Omega$	1200		V
I_C	$T_{case} = 25/80\text{ °C}$	78 / 50		A
I_{CM}	$T_{case} = 25/80\text{ °C}$; $t_p = 1\text{ ms}$	156 / 100		A
V_{GES}		± 20		V
P_{tot}	per IGBT, $T_{case} = 25\text{ °C}$	400		W
T_j , (T_{stg})		- 40 ... +150 (125)		°C
V_{isol}	AC, 1 min.	2 500 ⁷⁾		V
humidity	DIN 40 040	Class F		
climate	DIN IEC 68 T.1	55/150/56		
Inverse Diode				
$I_F = -I_C$	$T_{case} = 25/80\text{ °C}$	75 / 50	FWD 95/65	A
$I_{FM} = -I_{CM}$	$T_{case} = 25/80\text{ °C}$; $t_p = 1\text{ ms}$	152 / 100	152/100	A
I_{FSM}	$t_p = 10\text{ ms}$; $\sin.$; $T_j = 150\text{ °C}$	550	720	A
t^2	$t_p = 10\text{ ms}$; $T_j = 150\text{ °C}$	150	2600	A ² s

Characteristics		min.	typ.	max.	Units
Symbol	Conditions ¹⁾				
$V_{I(BR)CES}$	$V_{GE} = 0$, $I_C = 4\text{ mA}$	$\geq V_{CES}$	-	-	V
$V_{GE(th)}$	$V_{GE} = V_{CES}$, $I_C = 2\text{ mA}$	4,5	5,5	6,5	V
I_{CES}	$V_{GE} = 0$ $T_j = 25\text{ °C}$	-	0,8	1	mA
	$V_{CE} = V_{CES}$ $T_j = 125\text{ °C}$	-	3,5	-	mA
I_{GES}	$V_{GE} = 20\text{ V}$, $V_{CE} = 0$	-	-	200	nA
V_{CESat}	$I_C = 50\text{ A}$ $V_{GE} = 15\text{ V}$;	-	2,5(3,1)	3(3,7)	V
V_{CESat}	$I_C = 75\text{ A}$ $T_j = 25\text{ (125) °C}$	-	3(3,8)	-	V
g_s	$V_{CE} = 20\text{ V}$, $I_C = 50\text{ A}$	23	40	-	S
C_{CHC}	per IGBT	-	-	350	pF
C_{ies}	$V_{GE} = 0$	-	3,3	4,3	nF
C_{oes}	$V_{CE} = 25\text{ V}$	-	500	600	pF
C_{res}	$f = 1\text{ MHz}$	-	220	300	pF
L_{CE}		-	-	30	nH
$t_{d(on)}$	$V_{CC} = 600\text{ V}$	-	44	100	ns
t_r	$V_{GE} = +15\text{ V}$, -15 V ³⁾	-	56	100	ns
$t_{d(off)}$	$I_C = 50\text{ A}$, ind. load	-	380	500	ns
t_f	$R_{Gon} = R_{Goff} = 22\ \Omega$	-	70	100	ns
E_{on} ⁵⁾	$T_j = 125\text{ °C}$	-	8	-	mWs
E_{off} ⁵⁾		-	5	-	mWs
Inverse Diode ⁸⁾					
$V_F = V_{EC}$	$I_F = 50\text{ A}$ $V_{GE} = 0\text{ V}$;	-	2,0(1,8)	2,5	V
$V_F = V_{EC}$	$I_F = 75\text{ A}$ $T_j = 25\text{ (125) °C}$	-	2,25 (2,1)	-	V
V_{TO}	$T_j = 125\text{ °C}$	-	-	1,2	V
r_T	$T_j = 125\text{ °C}$	-	18	22	mΩ
I_{RRM}	$I_F = 50\text{ A}$; $T_j = 25\text{ (125) °C}$ ²⁾	-	23(35)	-	A
Q_{rr}	$I_F = 50\text{ A}$; $T_j = 25\text{ (125) °C}$ ²⁾	-	2,3(7)	-	μC
FWD of types "GAL" ⁸⁾					
$V_F = V_{EC}$	$I_F = 50\text{ A}$ $V_{GE} = 0\text{ V}$;	-	1,85(1,6)	2,2	V
$V_F = V_{EC}$	$I_F = 75\text{ A}$ $T_j = 25\text{ (125) °C}$	-	2,0(1,8)	-	V
V_{TO}	$T_j = 125\text{ °C}$	-	-	1,2	V
r_T	$T_j = 125\text{ °C}$	-	12	15	mΩ
I_{RRM}	$I_F = 50\text{ A}$; $T_j = 25\text{ (125) °C}$ ²⁾	-	27(40)	-	A
Q_{rr}	$I_F = 50\text{ A}$; $T_j = 25\text{ (125) °C}$ ²⁾	-	2,5(8)	-	μC
Thermal Characteristics					
R_{thjc}	per IGBT	-	-	0,30	°C/W
R_{thjc}	per diode / FWD "GAL"	-	-	0,60/0,50	°C/W
R_{thch}	per module	-	-	0,05	°C/W

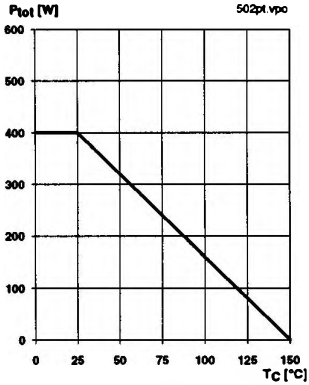
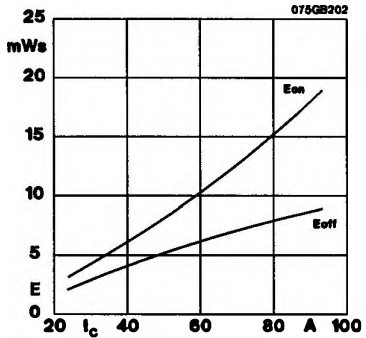
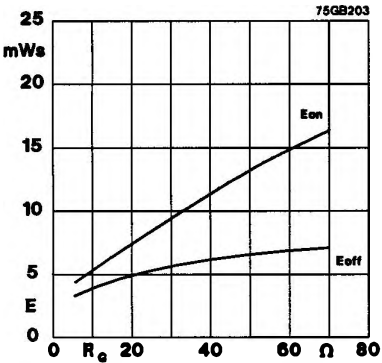


Fig. 1 Rated power dissipation $P_{tot} = f(T_c)$



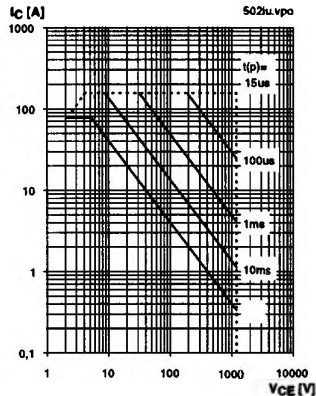
$T_j = 125\text{ °C}$
 $V_{CE} = 600\text{ V}$
 $V_{GE} = \pm 15\text{ V}$
 $R_G = 22\text{ }\Omega$

Fig. 2 Turn-on /-off energy = $f(I_c)$



$T_j = 125\text{ °C}$
 $V_{CE} = 600\text{ V}$
 $V_{GE} = \pm 15\text{ V}$
 $I_c = 50\text{ A}$

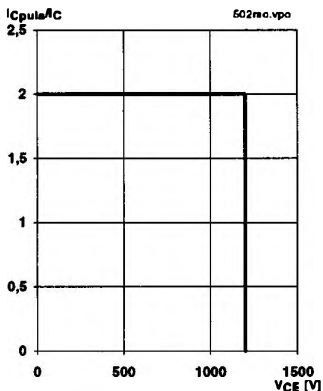
Fig. 3 Turn-on /-off energy = $f(R_g)$



1 pulse
 $T_c = 25\text{ °C}$
 $T_j \leq 150\text{ °C}$

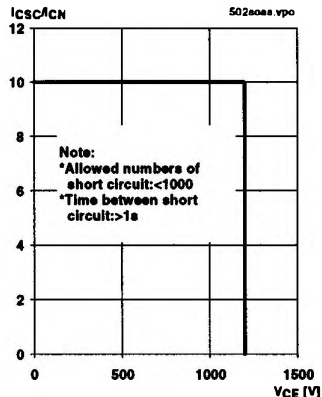
Not for linear use

Fig. 4 Maximum safe operating area (SOA) $I_c = f(V_{CE})$



$T_j \leq 150\text{ °C}$
 $V_{GE} = 15\text{ V}$
 $R_{g(off)} = 22\text{ }\Omega$
 $I_c = 50\text{ A}$

Fig. 5 Turn-off safe operating area (RBSOA)



$T_j \leq 150\text{ °C}$
 $V_{GE} = \pm 15\text{ V}$
 $t_{sc} \leq 10\text{ }\mu\text{s}$
 $L < 25\text{ nH}$
 $I_{CN} = 50\text{ A}$

Note:
 *Allowed numbers of short circuit:<1000
 *Time between short circuit:>1s

Fig. 6 Safe operating area at short circuit $I_c = f(V_{CE})$

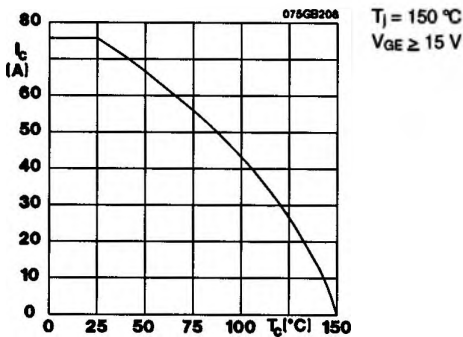


Fig. 8 Rated current vs. temperature $I_c = f(T_c)$

Fig. 7 Short circuit current vs. turn-on gate voltage

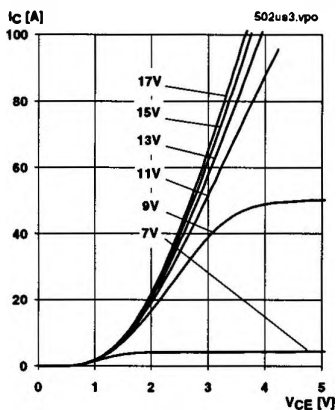


Fig. 9 Typ. output characteristic, $t_p = 80 \mu s$; $25^\circ C$

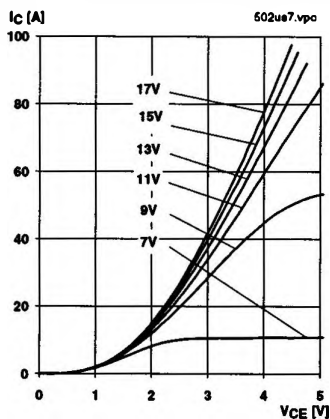


Fig. 10 Typ. output characteristic, $t_p = 80 \mu s$; $125^\circ C$

$$P_{cond(t)} = V_{CEsat(t)} \cdot I_c(t)$$

$$V_{CEsat(t)} = V_{CE(TO)(T)} + r_{CE(T)} \cdot I_c(t)$$

$$V_{CE(TO)(T)} \leq 1,5 + 0,002 (T_j - 25) [V]$$

$$r_{CE(T)} = 0,020 + 0,00008 (T_j - 25) [\Omega]$$

valid for $V_{GE} = +15 \begin{smallmatrix} +2 \\ -1 \end{smallmatrix} [V]$; $I_c \geq 0,3 I_{cnom}$

Fig. 11 Typ. saturation characteristic (IGBT)
Calculation elements and equations

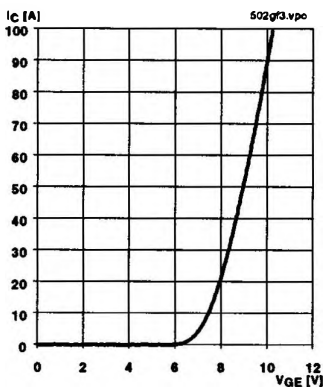


Fig. 12 Typ. transfer characteristic, $t_p = 80 \mu s$; $V_{CE} = 20 V$

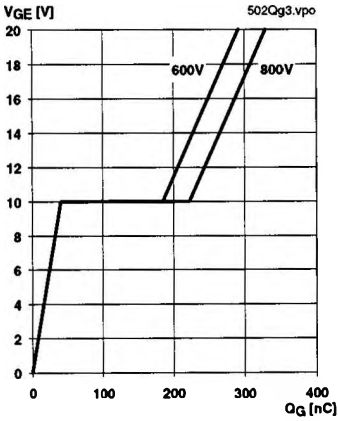


Fig. 13 Typ. gate charge characteristic

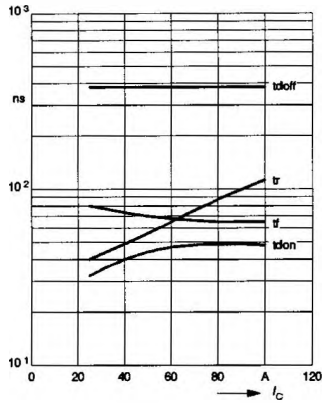


Fig. 15 Typ. switching times vs. I_c

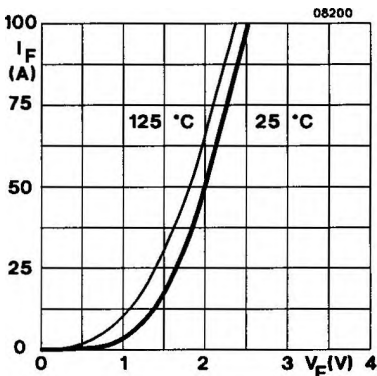


Fig. 17 Typ. CAL diode forward characteristic

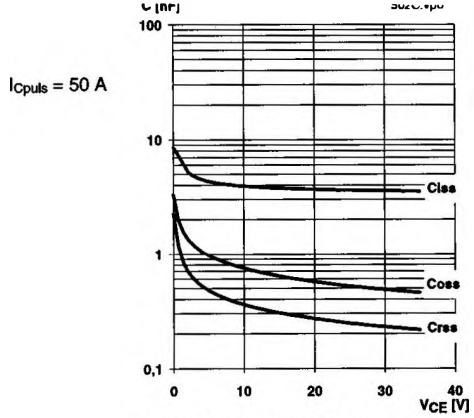


Fig. 14 Typ. capacitances vs. V_{CE}

$T_j = 125\text{ }^\circ\text{C}$
 $V_{CE} = 600\text{ V}$
 $V_{GE} = \pm 15\text{ V}$
 $R_{gon} = 22\ \Omega$
 $R_{goff} = 22\ \Omega$
 induct. load

$V_{GE} = 0\text{ V}$
 $f = 1\text{ MHz}$

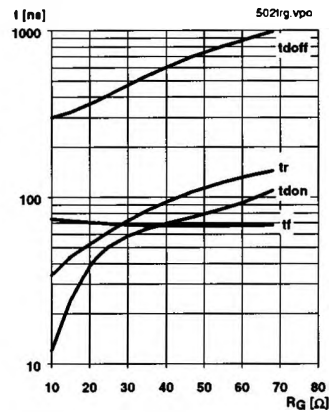


Fig. 16 Typ. switching times vs. gate resistor R_G

$T_j = 125\text{ }^\circ\text{C}$
 $V_{CE} = 600\text{ V}$
 $V_{GE} = \pm 15\text{ V}$
 $I_c = 50\text{ A}$
 induct. load

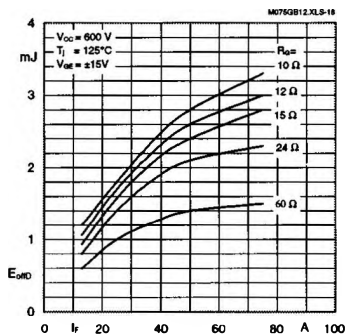


Fig. 18 Diode turn-off energy dissipation per pulse

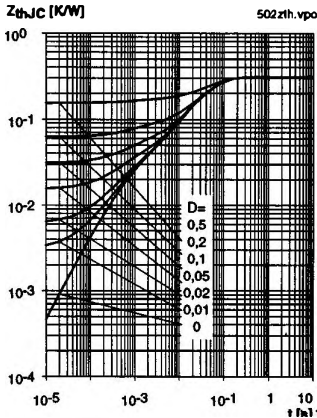


Fig. 19 Transient thermal impedance of IGBT
 $Z_{thJC} = f(t_p)$; $D = t_p / t_c = t_p \cdot f$

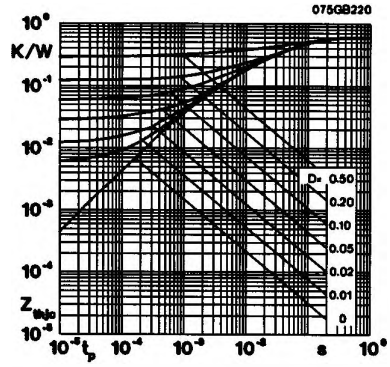


Fig. 20 Transient thermal impedance of inverse CAL diodes
 $Z_{thJC} = f(t_p)$; $D = t_p / t_c = t_p \cdot f$

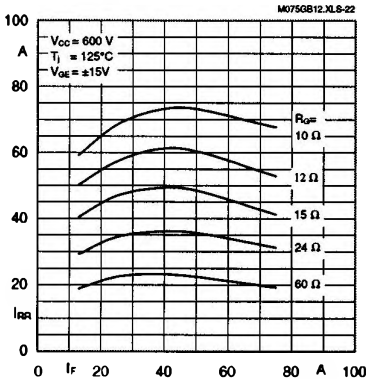


Fig. 22 Typ. CAL diode peak reverse recovery current $I_{RR} = f(I_F, R_G)$

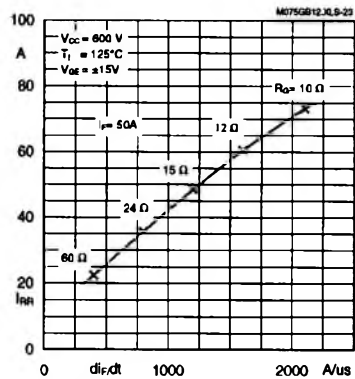


Fig. 23 Typ. CAL diode peak reverse recovery current $I_{RR} = f(di/dt)$

Typical Applications

Include

- Switched mode power supplies
- DC servo and robot drives
- Inverters
- DC choppers (versions GAL)
- AC motor speed control
- Inductive heating
- UPS Uninterruptable power supplies
- General power switching applications
- Electronic (also portable) welders
- Pulse frequencies also above 15 kHz

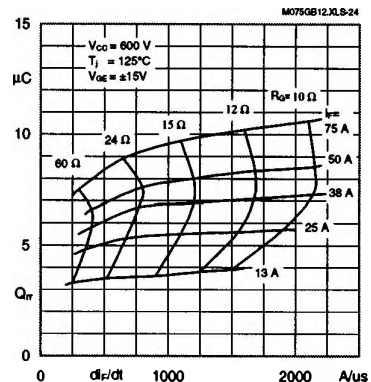


Fig. 24 Typ. CAL diode recovered charge $Q_{rr} = f(di/dt)$

SEMITRANS 2

CASED61

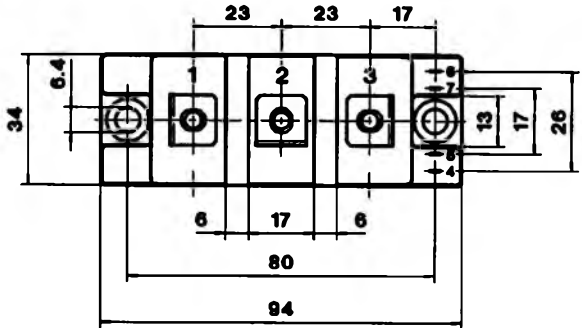
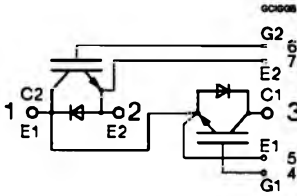
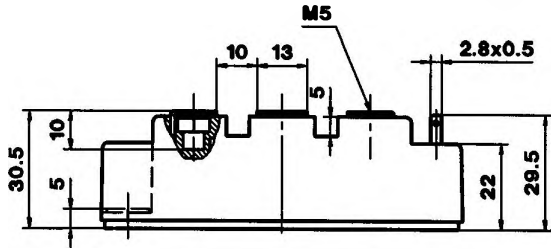
Case D 61

UL Recognized

File no. E 63 532

SKM 75 GB 123 D

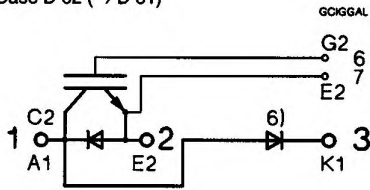
SKM 75 GB 173 D



Dimensions in mm

SKM 75 GAL 123 D

Case D 62 (→ D 61)



Case outline and circuit diagrams

Mechanical Data			Values			Units
Symbol	Conditions		min.	typ.	max.	
M ₁	to heatsink, SI Units	(M6)	3	-	5	Nm
	to heatsink, US Units		27	-	44	lb.in.
M ₂	for terminals, SI Units	(M5)	2,5	-	5	Nm
	for terminals US Units		22	-	44	lb.in.
a			-	-	5x9,81	m/s ²
w			-	-	250	g

This is an electrostatic discharge sensitive device (ESDS). Please observe the international standard IEC 747-1, Chapter IX.

Eight devices are supplied in one SEMIBOX A without mounting hardware, which can be ordered separately under Ident No. 33321100 (for 10 SEMITRANS 2)
 Accessories → page B 6 - 4.
 SEMIBOX → page C - 1.
 Larger packing units of 20 or 42 pieces are used if suitable.

⁶⁾ Freewheeling diode → page B 6 - 23, remark 6.