

# STP25N06 STP25N06FI

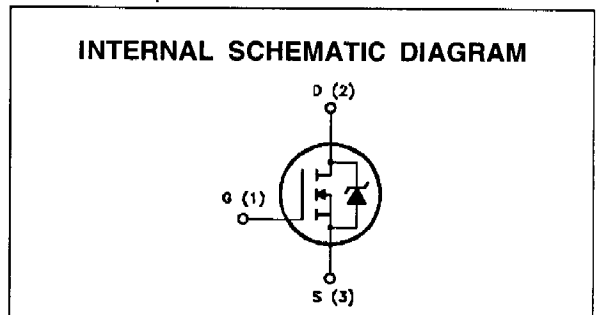
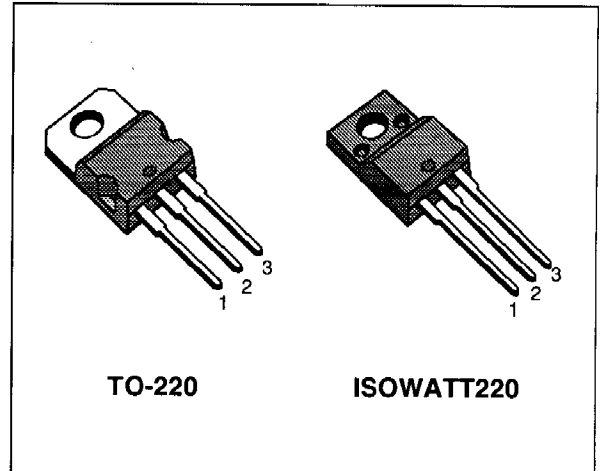
## N - CHANNEL ENHANCEMENT MODE POWER MOS TRANSISTOR

TYPE	V <sub>DSS</sub>	R <sub>DS(on)</sub>	I <sub>D</sub>
STP25N06	60 V	< 0.065 Ω	25 A
STP25N06FI	60 V	< 0.065 Ω	16 A

- TYPICAL R<sub>DS(on)</sub> = 0.048 Ω
- AVALANCHE RUGGED TECHNOLOGY
- 100% AVALANCHE TESTED
- REPETITIVE AVALANCHE DATA AT 100°C
- LOW GATE CHARGE
- HIGH CURRENT CAPABILITY
- 175°C OPERATING TEMPERATURE
- APPLICATION ORIENTED CHARACTERIZATION

### APPLICATIONS

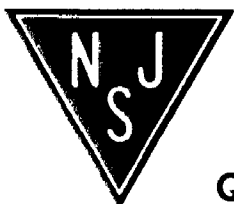
- HIGH CURRENT, HIGH SPEED SWITCHING
- SOLENOID AND RELAY DRIVERS
- REGULATORS
- DC-DC & DC-AC CONVERTERS
- MOTOR CONTROL, AUDIO AMPLIFIERS
- AUTOMOTIVE ENVIRONMENT (INJECTION, ABS, AIR-BAG, LAMPDRIVERS, Etc.)



### ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Value		Unit
		STP25N06	STP25N06FI	
V <sub>DS</sub>	Drain-source Voltage (V <sub>GS</sub> = 0)	60		V
V <sub>DGR</sub>	Drain- gate Voltage (R <sub>GS</sub> = 20 kΩ)	60		V
V <sub>GS</sub>	Gate-source Voltage	± 20		V
I <sub>D</sub>	Drain Current (continuous) at T <sub>c</sub> = 25 °C	25	16	A
I <sub>D</sub>	Drain Current (continuous) at T <sub>c</sub> = 100 °C	17	11	A
I <sub>DM</sub> (*)	Drain Current (pulsed)	100	100	A
P <sub>tot</sub>	Total Dissipation at T <sub>c</sub> = 25 °C	90	40	W
	Derating Factor	0.6	0.27	W/°C
V <sub>ISO</sub>	Insulation Withstand Voltage (DC)	—	2000	V
T <sub>stg</sub>	Storage Temperature	-65 to 175		°C
T <sub>j</sub>	Max. Operating Junction Temperature	175		°C

(\*) Pulse width limited by safe operating area



## STP25N06/FI

### THERMAL DATA

			TO-220	ISOWATT220	
R <sub>thj-case</sub>	Thermal Resistance Junction-case	Max	1.57	3.75	°C/W
R <sub>thj-amb</sub>	Thermal Resistance Junction-ambient	Max	62.5		°C/W
R <sub>thc-sink</sub>	Thermal Resistance Case-sink	Typ	0.5		°C/W
T <sub>l</sub>	Maximum Lead Temperature For Soldering Purpose		300		°C

### AVALANCHE CHARACTERISTICS

Symbol	Parameter	Max Value	Unit
I <sub>AR</sub>	Avalanche Current, Repetitive or Not-Repetitive (pulse width limited by T <sub>j</sub> max, δ < 1%)	25	A
E <sub>AS</sub>	Single Pulse Avalanche Energy (starting T <sub>j</sub> = 25 °C, I <sub>D</sub> = I <sub>AR</sub> , V <sub>DD</sub> = 25 V)	100	mJ
E <sub>AR</sub>	Repetitive Avalanche Energy (pulse width limited by T <sub>j</sub> max, δ < 1%)	25	mJ
I <sub>AR</sub>	Avalanche Current, Repetitive or Not-Repetitive (T <sub>c</sub> = 100 °C, pulse width limited by T <sub>j</sub> max, δ < 1%)	17	A

### ELECTRICAL CHARACTERISTICS (T<sub>case</sub> = 25 °C unless otherwise specified)

OFF

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
V <sub>(BR)DSS</sub>	Drain-source Breakdown Voltage	I <sub>D</sub> = 250 μA V <sub>GS</sub> = 0	60			V
I <sub>DSS</sub>	Zero Gate Voltage Drain Current (V <sub>GS</sub> = 0)	V <sub>DS</sub> = Max Rating V <sub>DS</sub> = Max Rating x 0.8 T <sub>c</sub> = 125 °C			1 10	μA μA
I <sub>GSS</sub>	Gate-body Leakage Current (V <sub>DS</sub> = 0)	V <sub>GS</sub> = ± 20 V			± 100	nA

ON (\*)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> = V <sub>GS</sub> I <sub>D</sub> = 250 μA	2	2.9	4	V
R <sub>DS(on)</sub>	Static Drain-source On Resistance	V <sub>GS</sub> = 10V I <sub>D</sub> = 12.5 A		0.048	0.065	Ω
I <sub>D(on)</sub>	On State Drain Current	V <sub>DS</sub> > I <sub>D(on)</sub> × R <sub>DS(on)max</sub> V <sub>GS</sub> = 10 V	25			A

DYNAMIC

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
g <sub>fs</sub> (*)	Forward Transconductance	V <sub>DS</sub> > I <sub>D(on)</sub> × R <sub>DS(on)max</sub> I <sub>D</sub> = 12.5 A	7	11		S
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> = 25 V f = 1 MHz V <sub>GS</sub> = 0		700	900	pF
C <sub>oss</sub>	Output Capacitance			320	450	pF
C <sub>rss</sub>	Reverse Transfer Capacitance			90	150	pF

**ELECTRICAL CHARACTERISTICS** (continued)

**SWITCHING ON**

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
$t_{d(on)}$	Turn-on Time	$V_{DD} = 30\text{ V}$ $I_D = 3\text{ A}$		30	45	ns
$t_r$	Rise Time	$R_G = 50\ \Omega$ $V_{GS} = 10\text{ V}$ (see test circuit, figure 3)		90	130	ns
$(di/dt)_{on}$	Turn-on Current Slope	$V_{DD} = 40\text{ V}$ $I_D = 25\text{ A}$ $R_G = 50\ \Omega$ $V_{GS} = 10\text{ V}$ (see test circuit, figure 5)		230		A/ $\mu$ s
$Q_g$	Total Gate Charge	$V_{DD} = 40\text{ V}$ $I_D = 25\text{ A}$ $V_{GS} = 10\text{ V}$		26	40	nC
$Q_{gs}$	Gate-Source Charge			8		nC
$Q_{gd}$	Gate-Drain Charge			9		nC

**SWITCHING OFF**

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
$t_{r(Voff)}$	Off-voltage Rise Time	$V_{DD} = 40\text{ V}$ $I_D = 25\text{ A}$		80	120	ns
$t_f$	Fall Time	$R_G = 50\ \Omega$ $V_{GS} = 10\text{ V}$ (see test circuit, figure 5)		80	120	ns
$t_c$	Cross-over Time			170	250	ns

**SOURCE DRAIN DIODE**

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
$I_{SD}$	Source-drain Current				25	A
$I_{SDM}(\bullet)$	Source-drain Current (pulsed)				100	A
$V_{SD}(\ast)$	Forward On Voltage	$I_{SD} = 25\text{ A}$ $V_{GS} = 0$			1.5	V
$t_{rr}$	Reverse Recovery Time	$I_{SD} = 25\text{ A}$ $di/dt = 100\text{ A}/\mu\text{s}$ $V_{DD} = 30\text{ V}$ $T_j = 150\text{ }^\circ\text{C}$ (see test circuit, figure 5)		80		ns
$Q_{rr}$	Reverse Recovery Charge			0.22		$\mu$ C
$I_{RRM}$	Reverse Recovery Current			5.5		A

(\*) Pulsed: Pulse duration = 300  $\mu$ s, duty cycle 1.5 %

( $\bullet$ ) Pulse width limited by safe operating area

Safe Operating Areas For TO-220

Safe Operating Areas For ISOWATT220

