UP04601

Silicon NPN epitaxial planar transistor (Tr1) Silicon PNP epitaxial planar transistor (Tr2)

For general amplification

Features

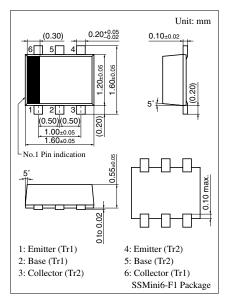
- Two elements incorporated into one package (Each transistor is separated)
- Reduction of the mounting area and assembly cost by one half

Basic Part Number of Element

• 2SD0601A (2SD601A) + 2SB0709A (2SB709A)

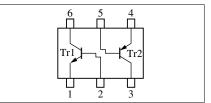
	Parameter	Symbol	Rating	Unit	
Tr1	Collector to base voltage	V _{CBO}	60	V	
	Collector to emitter voltage	V _{CEO}	50	V	
	Emitter to base voltage	V _{EBO}	7	V	
	Collector current	I _C	100	mA	
	Peak collector current	I _{CP}	200	mA	
Tr2	Collector to base voltage	V _{CBO}	-60	V	
	Collector to emitter voltage	V _{CEO}	-50	V	
	Emitter to base voltage	V _{EBO}	-7	V	
	Collector current	I _C	-100	mA	
	Peak collector current	I _{CP}	-200	mA	
Total	Total power dissipation	P _T	125	mW	
	Junction temperature	Tj	125	°C	
	Storage temperature	T _{stg}	-55 to +125	°C	

Absolute Maximum Ratings $T_a = 25^{\circ}C$



Marking Symbol: 5C

Internal Connection



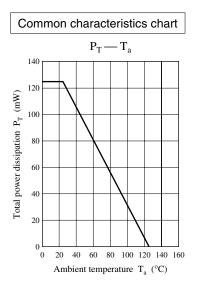
\blacksquare Electrical Characteristics $T_a = 25^{\circ}C \pm 3^{\circ}C$

• Tr1

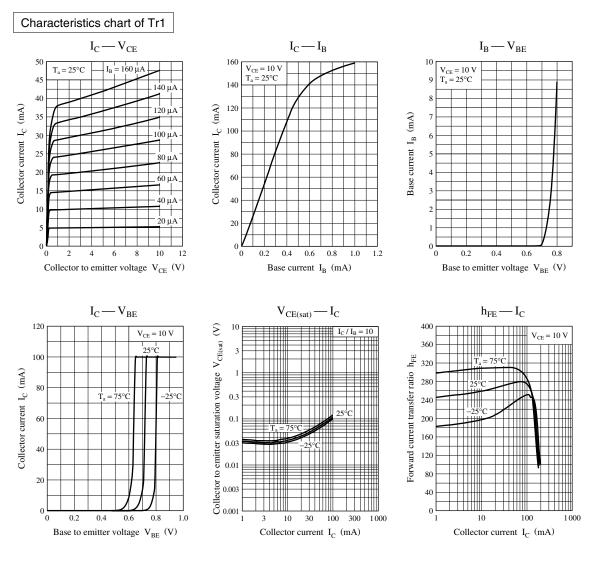
Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Collector to base voltage	V _{CBO}	$I_{C} = 10 \ \mu A, I_{E} = 0$	60			V
Collector to emitter voltage	V _{CEO}	$I_{\rm C} = 2 \text{ mA}, I_{\rm B} = 0$	50			V
Emitter to base voltage	V _{EBO}	$I_E = 10 \ \mu A, \ I_C = 0$	7			V
Collector cutoff current	I _{CBO}	$V_{CB} = 20 \text{ V}, I_E = 0$			0.1	μΑ
	I _{CEO}	$V_{CE} = 10 \text{ V}, I_B = 0$			100	
Forward current transfer ratio	h _{FE}	$V_{CE} = 10 \text{ V}, I_C = 2 \text{ mA}$	180		390	
Collector to emitter saturation voltage	V _{CE(sat)}	$I_{\rm C} = 100 \text{ mA}, I_{\rm B} = 10 \text{ mA}$		0.1	0.3	V
Collector output capacitance	C _{ob}	$V_{CB} = 10 \text{ V}, I_E = 0, f = 1 \text{ MHz}$		3.5		pF
Gain bandwidth product	f _T	$V_{CB} = 10 \text{ V}, I_E = -2 \text{ mA}, f = 200 \text{ MHz}$		150		MHz

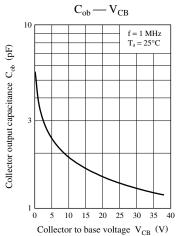
• Tr2

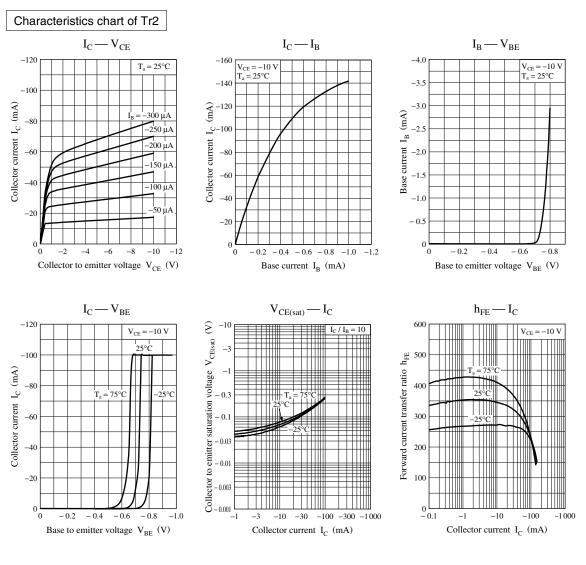
Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Collector to base voltage	V _{CBO}	$I_{\rm C} = -10 \ \mu A, \ I_{\rm E} = 0$	-60			V
Collector to emitter voltage	V _{CEO}	$I_{\rm C} = -2 \text{ mA}, I_{\rm B} = 0$	-50			V
Emitter to base voltage	V _{EBO}	$I_{\rm E} = -10 \ \mu A, \ I_{\rm C} = 0$	-7			V
Collector cutoff current	I _{CBO}	$V_{CB} = -20 \text{ V}, I_E = 0$			- 0.1	μΑ
	I _{CEO}	$V_{CE} = -10 \text{ V}, I_B = 0$			-100	
Forward current transfer ratio	h _{FE}	$V_{CE} = -10 \text{ V}, I_C = -5 \text{ mA}$	180		390	—
Collector to emitter saturation voltage	V _{CE(sat)}	$I_{\rm C} = -100 \text{ mA}, I_{\rm B} = -10 \text{ mA}$		- 0.3	- 0.5	V
Collector output capacitance	C _{ob}	$V_{CB} = -10 \text{ V}, I_E = 0, f = 1 \text{ MHz}$		2.7		pF
Gain bandwidth product	f_{T}	$V_{CB} = -10 \text{ V}, I_E = 1 \text{ mA}, f = 200 \text{ MHz}$		80		MHz

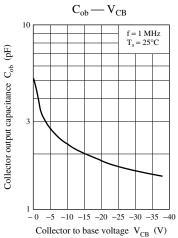


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