# XN04503 (XN4503)

## Silicon NPN epitaxial planer transistor

For amplification of low frequency output

### Features

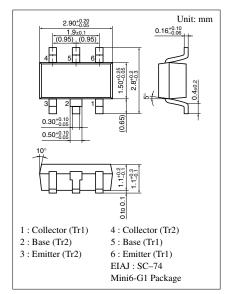
- Two elements incorporated into one package.
- Reduction of the mounting area and assembly cost by one half.

## Basic Part Number of Element

• 2SD0813(2SD813) × 2 elements

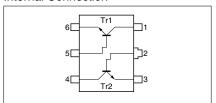
## Absolute Maximum Ratings (Ta=25°C)

Parameter		Symbol	Ratings	Unit	
Rating of element	Collector to base voltage	$V_{CBO}$	25	V	
	Collector to emitter voltage	$V_{CEO}$	20	V	
	Emitter to base voltage	$V_{EBO}$	7	V	
	Collector current	$I_{C}$	0.5	A	
	Peak collector current	$I_{CP}$	1	A	
Overall	Total power dissipation	$P_{T}$	300	mW	
	Junction temperature	$T_{j}$	150	°C	
	Storage temperature	$T_{stg}$	-55 to +150	°C	



Marking Symbol: 5Y

#### Internal Connection

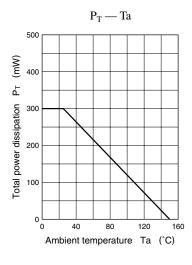


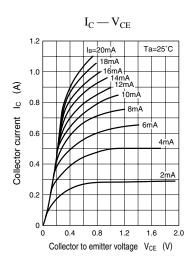
### Electrical Characteristics (Ta=25°C)

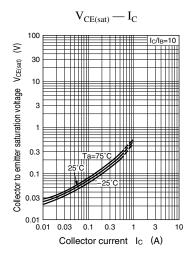
Parameter	Symbol	Conditions	min	typ	max	Unit
Collector to base voltage	V <sub>CBO</sub>	$I_C = 10 \mu A, I_E = 0$	25			V
Collector to emitter voltage	V <sub>CEO</sub>	$I_C = 1 \text{ mA}, I_B = 0$	20			V
Emitter to base voltage	$V_{EBO}$	$I_{\rm E} = 10\mu A, I_{\rm C} = 0$	7			V
Collector cutoff current	$I_{CBO}$	$V_{CB} = 25V, I_E = 0$			0.1	μΑ
Collector cutoff current	$I_{CEO}$	$V_{CE} = 20V, I_B = 0$			1	μΑ
Forward current transfer ratio	h <sub>FE1</sub>	$V_{CE} = 2V, I_{C} = 500 \text{mA*}$	65		350	
Forward current transfer ratio	h <sub>FE2</sub>	$V_{CE} = 2V, I_C = 1A*$	50			
Collector to emitter saturation voltage	V <sub>CE(sat)</sub>	$I_C = 500 \text{mA}, I_B = 20 \text{mA}^*$		0.2	0.4	V
Base to emitter saturation voltage	V <sub>BE(sat)</sub>	$I_C = 500 \text{mA}, I_B = 50 \text{mA}^*$			1.2	V
Transition frequency	$f_T$	$V_{CB} = 10V$ , $I_E = -50$ mA, $f = 200$ MHz		150		MHz
Collector output capacitance	C <sub>ob</sub>	$V_{CB} = 10V, I_E = 0, f = 1MHz$		6		pF

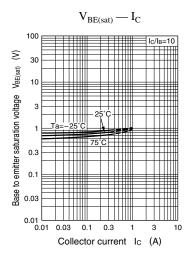
\* Pulse measurement

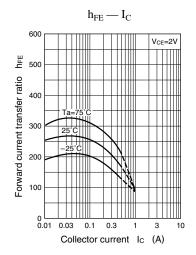
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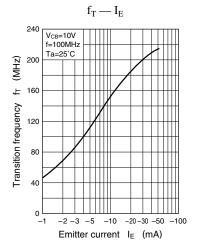


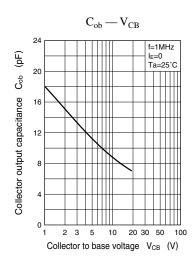












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