XN04504 (XN4504)

Silicon NPN epitaxial planer transistor

For amplification of low frequency output

Features

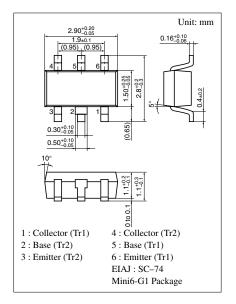
- Two elements incorporated into one package.
- Reduction of the mounting area and assembly cost by one half.

Basic Part Number of Element

• $2SD1328 \times 2$ elements

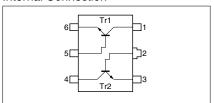
Absolute Maximum Ratings (Ta=25°C)

Parameter		Symbol	Ratings	Unit	
Rating of element	Collector to base voltage	V_{CBO}	25	V	
	Collector to emitter voltage	V_{CEO}	20	V	
	Emitter to base voltage	V_{EBO}	12	V	
	Collector current	I_{C}	0.5	A	
	Peak collector current	I_{CP}	1	A	
Overall	Total power dissipation	P_{T}	300	mW	
	Junction temperature	T_{j}	150	°C	
	Storage temperature	T_{stg}	-55 to +150	°C	



Marking Symbol: 5X

Internal Connection

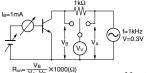


Electrical Characteristics (Ta=25°C)

Parameter	Symbol	Conditions	min	typ	max	Unit
Collector to base voltage	V _{CBO}	$I_{\rm C} = 10 \mu A, I_{\rm E} = 0$	25			V
Collector to emitter voltage	V _{CEO}	$I_C = 1 \text{ mA}, I_B = 0$	20			V
Emitter to base voltage	V _{EBO}	$I_{\rm E} = 10 \mu A, I_{\rm C} = 0$	12			V
Collector cutoff current	I_{CBO}	$V_{CB} = 25V, I_{E} = 0$			0.1	μΑ
F	h _{FE1}	$V_{CE} = 2V, I_C = 500 \text{mA}^{*1}$	200		800	
Forward current transfer ratio	h _{FE2}	$V_{CE} = 2V, I_C = 1A^{*1}$	60			
Collector to emitter saturation voltage	V _{CE(sat)}	$I_{\rm C} = 500 \mathrm{mA}, I_{\rm B} = 20 \mathrm{mA}$		0.13	0.4	V
Base to emitter saturation voltage	V _{BE(sat)}	$I_C = 500 \text{mA}, I_B = 50 \text{mA}$			1.2	V
Transition frequency	f_T	$V_{CB} = 10V, I_{E} = -50mA, f = 200MHz$		200		MHz
Collector output capacitance	C _{ob}	$V_{CB} = 10V, I_E = 0, f = 1MHz$		10		pF
ON Resistance	R _{on} *2			1.0		Ω

^{*1} Pulse measurement

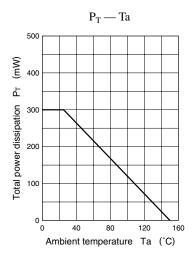
^{*2} R_{on} test circuit

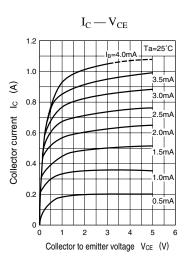


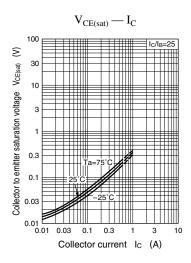
Note) The Part number in the Parenthesis shows conventional part number.

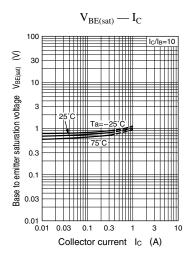
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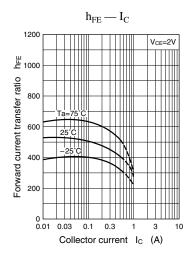
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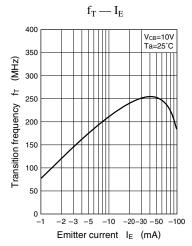


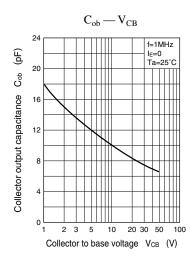












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