XN0A554 (XN6A554)

Silicon NPN epitaxial planer transistor

For high speed switching

Features

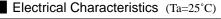
- Two elements incorporated into one package.
- Reduction of the mounting area and assembly cost by one half.
- Low V_{CE(sat)}.

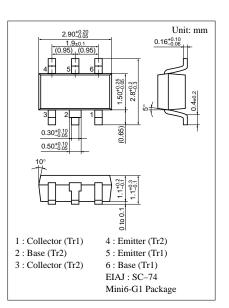
Basic Part Number of Element

• $2SC3757 \times 2$ elements

Parameter		Symbol	Ratings	Unit				
Rating of element	Collector to base voltage	V _{CBO}	40	V				
	Collector to emitter voltage	V _{CEO}	40	V				
	Emitter to base voltage	V_{EBO}	5	V				
	Collector current	I _C	100	mA				
	Peak collector current	I _{CP}	300	mA				
Overall	Total power dissipation	P _T	300	mW				
	Junction temperature	Tj	150	°C				
	Storage temperature	T _{stg}	-55 to +150	°C				

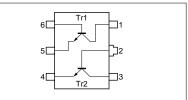
Absolute Maximum Ratings (Ta=25°C)





Marking Symbol: DT

Internal Connection



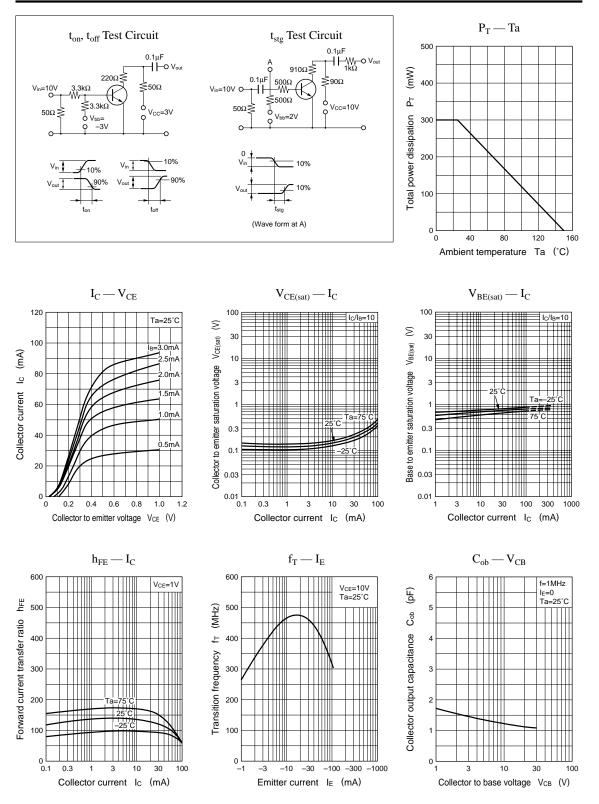
Parameter	Symbol	Conditions	min	typ	max	Unit
Collector cutoff current	I _{CBO}	$V_{CB} = 40V, I_E = 0$			0.1	μΑ
Emitter cutoff current	I _{EBO}	$V_{EB} = 4V$, $I_C = 0$			0.1	μΑ
Forward current transfer ratio	h _{FE}	$V_{CE} = 1V, I_{C} = 10mA$	60		320	
Forward current transfer h_{FE} ratio	h _{FE} (small/large)*1	$V_{CE} = 1V, I_{C} = 10mA$	0.5	0.99		
Collector to emitter saturation voltage	V _{CE(sat)}	$I_C = 10mA$, $I_B = 1mA$		0.17	0.25	v
Base to emitter saturation voltage	V _{BE(sat)}	$I_C = 10mA$, $I_B = 1mA$			1.0	v
Transition frequency	f _T	$V_{CE} = 10V, I_E = -10mA, f = 200MHz$		450		MHz
Collector output capacitance	C _{ob}	$V_{CB} = 10V, I_E = 0, f = 1MHz$		2	6	pF
Turn-off time	t _{on}			17		
Turn-on time	t _{off}	*2		17		ns
Storage time	t _{stg}			10		1

*1 Ratio between 2 elements

*2 Test Circuits

Note) The Part number in the Parenthesis shows conventional part number.

Composite Transistors



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