# **XP04313** (XP04313)

## Silicon NPN epitaxial planar transistor (Tr1) Silicon PNP epitaxial planar transistor (Tr2)

## For switching/digital circuits

### ■ Features

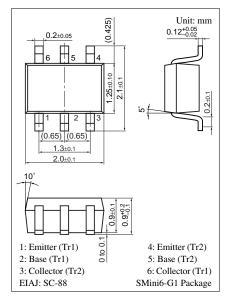
- Two elements incorporated into one package
- Reduction of the mounting area and assembly cost by one half

#### ■ Basic Part Number of Element

• UNR1213 (UN1213) + UNR1113 (UN1113)

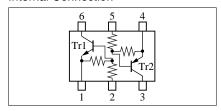
## ■ Absolute Maximum Ratings $T_a = 25$ °C

	Parameter	Symbol	Rating	Unit	
Tr1	Collector to base voltage	$V_{CBO}$	50	V	
	Collector to emitter voltage	V <sub>CEO</sub>	50	V	
	Collector current	$I_C$	100	mA	
Tr2	Collector to base voltage	$V_{CBO}$	-50	V	
	Collector to emitter voltage	$V_{CEO}$	-50	V	
	Collector current	$I_C$	-100	mA	
Total	Total power dissipation	$P_{T}$	150	mW	
	Junction temperature	$T_{j}$	150	°C	
	Storage temperature	$T_{stg}$	-55 to +150	°C	



Marking Symbol: BZ

#### Internal Connection



Note) The part number in the parenthesis shows conventional part number.

## $\blacksquare$ Electrical Characteristics $T_a = 25^{\circ}C \pm 3^{\circ}C$

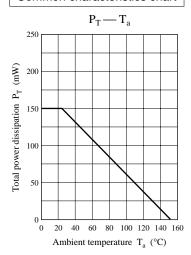
## • Tr1

Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Collector to base voltage	V <sub>CBO</sub>	$I_C = 10 \mu\text{A},  I_E = 0$	50			V
Collector to emitter voltage	V <sub>CEO</sub>	$I_{\rm C} = 2 \text{ mA}, I_{\rm B} = 0$	50			V
Collector cutoff current	I <sub>CBO</sub>	$V_{CB} = 50 \text{ V}, I_{E} = 0$			0.1	μΑ
	I <sub>CEO</sub>	$V_{CE} = 50 \text{ V}, I_{B} = 0$			0.5	
Emitter cutoff current	$I_{EBO}$	$V_{EB} = 6 \text{ V}, I_{C} = 0$			0.1	mA
Forward current transfer ratio	h <sub>FE</sub>	$V_{CE} = 10 \text{ V}, I_{C} = 5 \text{ mA}$	80			
High level output voltage	V <sub>OH</sub>	$V_{CC} = 5 \text{ V}, V_B = 0.5 \text{ V}, R_L = 1 \text{ k}\Omega$	4.9			V
Low level output voltage	V <sub>OL</sub>	$V_{CC} = 5 \text{ V}, V_B = 3.5 \text{ V}, R_L = 1 \text{ k}\Omega$			- 0.2	V
Input resistance	R <sub>1</sub>		-30%	47	+30%	kΩ
Resistance ratio	$R_1/R_2$		0.8	1.0	1.2	
Collector to emitter saturation voltage	V <sub>CE(sat)</sub>	$I_C = 10 \text{ mA}, I_B = 0.3 \text{ mA}$			0.25	V
Gain bandwidth product	$f_{\mathrm{T}}$	$V_{CB} = 10 \text{ V}, I_E = -2 \text{ mA}, f = 200 \text{ MHz}$		150		MHz

## • Tr2

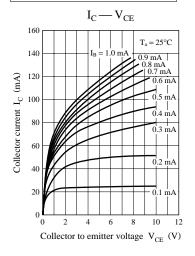
Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Collector to base voltage	V <sub>CBO</sub>	$I_{\rm C} = -10 \; \mu \text{A},  I_{\rm E} = 0$	-50			V
Collector to emitter voltage	V <sub>CEO</sub>	$I_{\rm C} = -2 \text{ mA}, I_{\rm B} = 0$	-50			V
Collector cutoff current	I <sub>CBO</sub>	$V_{CB} = -50 \text{ V}, I_{E} = 0$			- 0.1	μΑ
	I <sub>CEO</sub>	$V_{CE} = -50 \text{ V}, I_{B} = 0$			- 0.5	
Emitter cutoff current	I <sub>EBO</sub>	$V_{EB} = -6 \text{ V}, I_C = 0$			- 0.1	mA
Forward current transfer ratio	h <sub>FE</sub>	$V_{CE} = -10 \text{ V}, I_{C} = -5 \text{ mA}$	80			
High level output voltage	V <sub>OH</sub>	$V_{CC} = -5 \text{ V}, V_{B} = -0.5 \text{ V}, R_{L} = 1 \text{ k}\Omega$	-4.9			V
Low level output voltage	V <sub>OL</sub>	$V_{CC} = -5 \text{ V}, V_{B} = -3.5 \text{ V}, R_{L} = 1 \text{ k}\Omega$			- 0.2	V
Input resistance	$R_1$		-30%	47	+30%	kΩ
Resistance ratio	R <sub>1</sub> / R <sub>2</sub>		0.8	1.0	1.2	
Collector to emitter saturation voltage	V <sub>CE(sat)</sub>	$I_C = -10 \text{ mA}, I_B = -0.3 \text{ mA}$			- 0.25	V
Gain bandwidth product	$f_T$	$V_{CB} = -10 \text{ V}, I_E = 1 \text{ mA}, f = 200 \text{ MHz}$		80		MHz

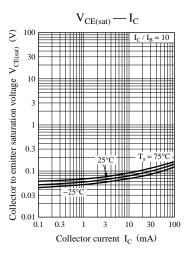
## Common characteristics chart

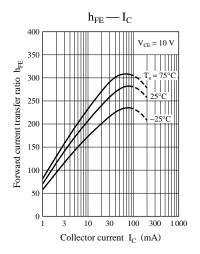


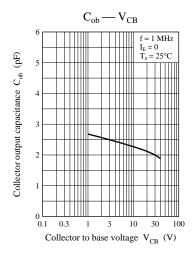
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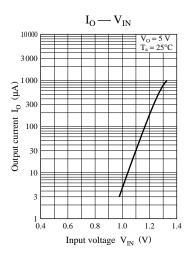
## Characteristics charts of Tr1

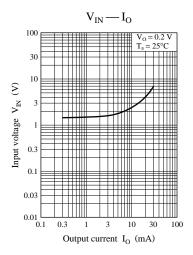








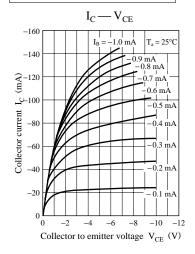


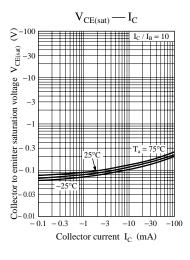


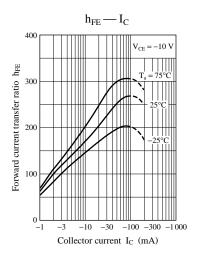
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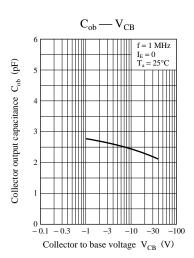
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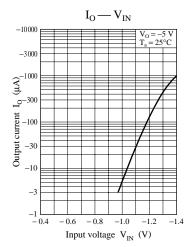
## Characteristics charts of Tr2

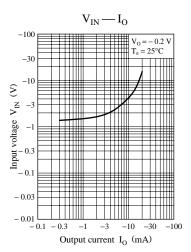












4 SJJ00178BED

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